SPEC CPU®2017 Floating Point Rate Result

Dell Inc.

$SPECrate^{®}2017\_fp\_base = \quad 240$

PowerEdge R650xs (Intel Xeon Gold 6314U, 2.30 GHz)

$SPECrate^{®}2017\_fp\_peak = Not\ Run$

CPU2017 License: 55　　　　　　　　　　　　　　　　　　　　　　　**Test Date:** Nov-2022
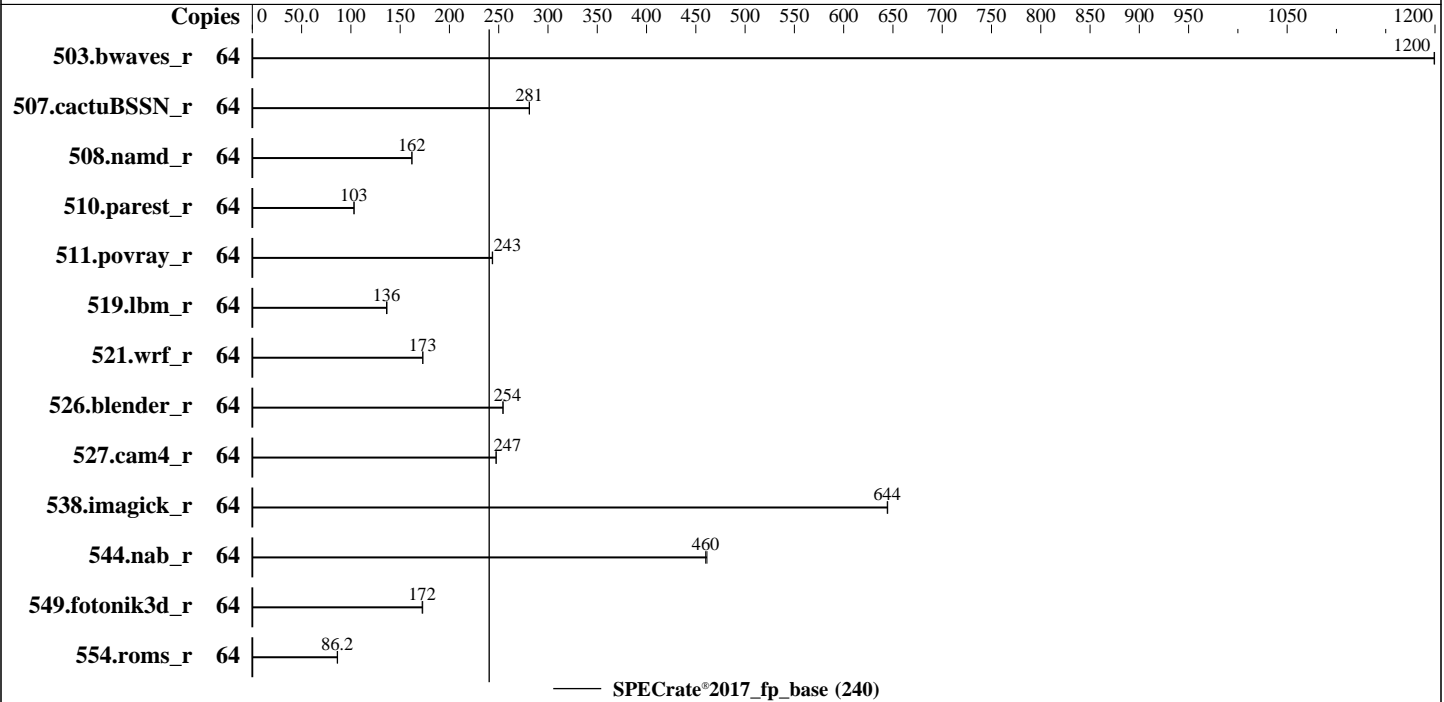**Test Sponsor:** Dell Inc.　　　　　　　　　　　　　　　　　　**Hardware Availability:** Jul-2021
Tested by: Dell Inc.　　　　　　　　　　　　　　　　　　**Software Availability:** May-2022

Copies	Value
503.bwaves_r 64	1200
507.cactuBSSN_r 64	281
508.namd_r 64	162
510.parest_r 64	103
511.povray_r 64	243
519.lbm_r 64	136
521.wrf_r 64	173
526.blender_r 64	254
527.cam4_r 64	247
538.imagick_r 64	644
544.nab_r 64	460
549.fotonik3d_r 64	172
554.roms_r 64	86.2

SPECrate®2017_fp_base (240)

Hardware

CPU Name: Intel Xeon Gold 6314U
　Max MHz: 3400
　Nominal: 2300
Enabled: 32 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 32 KB I + 48 KB D on chip per core
　L2: 1.25 MB I+D on chip per core
　L3: 48 MB I+D on chip per chip
　Other: None
Memory: 256 GB (8 x 32 GB 2Rx8 PC4-3200AA-R)
Storage: 60 GB on tmpfs
Other: None

Software

OS: Red Hat Enterprise Linux 8.6 (Ootpa)
　　4.18.0-372.9.1.el8.x86_64
Compiler: C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
　　Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;
Parallel: No
Firmware: Version 1.7.4 released Jun-2022
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.

SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R650xs (Intel Xeon Gold 6314U, 2.30 GHz)

| SPECrate®2017_fp_base = | 240 |
| SPECrate®2017_fp_peak = | Not Run |

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Nov-2022
Hardware Availability: Jul-2021
Software Availability: May-2022

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	64	**535**	**1200**	535	1200									
507.cactuBSSN_r	64	**288**	**281**	288	281									
508.namd_r	64	**376**	**162**	376	162									
510.parest_r	64	**1624**	**103**	1622	103									
511.povray_r	64	613	244	**614**	**243**									
519.lbm_r	64	**495**	**136**	494	137									
521.wrf_r	64	**829**	**173**	829	173									
526.blender_r	64	**383**	**254**	383	254									
527.cam4_r	64	**453**	**247**	452	247									
538.imagick_r	64	247	645	**247**	**644**									
544.nab_r	64	**234**	**460**	234	461									
549.fotonik3d_r	64	**1447**	**172**	1443	173									
554.roms_r	64	**1180**	**86.2**	1176	86.5									
SPECrate®2017_fp_base =		**240**												
SPECrate®2017_fp_peak =		**Not Run**												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/mnt/ramdisk/cpu2017-1.1.8-ic2022.1/lib/intel64:/mnt/ramdisk/cpu2017-1.
     1.8-ic2022.1/je5.0.1-64"
MALLOC_CONF = "retain:true"
```

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default

(Continued on next page)

SPEC CPU®2017 Floating Point Rate Result

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Tested by: Dell Inc.

Test Date: Nov-2022
Hardware Availability: Jul-2021
Software Availability: May-2022

General Notes (Continued)

```
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>        /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.


Benchmark run from a 60 GB ramdisk created with the cmd: "mount -t tmpfs -o size=60G tmpfs /mnt/ramdisk"
```

Platform Notes

```
BIOS settings:
        DIMM Self Healing on
    Uncorrectable Memory Error : Disabled
     Virtualization Technology : Disabled
             Sub NUMA Cluster : 2-way Clustering
       DCU Streamer Prefetcher : Disabled
                 LLC Prefetch : Disabled
           Dead Line LLC Alloc : Disabled
                Optimizer Mode : Enabled

                System Profile : Custom
        CPU Power Management : Maximum Performance
                          C1E : Disabled
                     C States : Autonomous
          Memory Patrol Scrub : Disabled
       Energy Efficiency Policy : Performance
           PCI ASPM L1 Link
             Power Management : Disabled

 Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-ic2022.1/bin/sysinfo
 Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
 running on auvcetillem1m1.corp.emc.com Sun Nov 13 06:29:16 2022

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
```

(Continued on next page)

Standard Performance Evaluation Corporation (info@spec.org)   https://www.spec.org/

Dell Inc.

PowerEdge R650xs (Intel Xeon Gold 6314U, 2.30 GHz)

SPECrate®2017_fp_base = 240

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 55	**Test Date:** Nov-2022
Test Sponsor: Dell Inc.	**Hardware Availability:** Jul-2021
Tested by: Dell Inc.	**Software Availability:** May-2022

Platform Notes (Continued)

```
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 6314U CPU @ 2.30GHz
       1  "physical id"s (chips)
      64  "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 32
       siblings  : 64
       physical 0: cores 0 1 2 3 4 5 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
       28 29 30 31

 From lscpu from util-linux 2.32.1:
       Architecture:        x86_64
       CPU op-mode(s):      32-bit, 64-bit
       Byte Order:          Little Endian
       CPU(s):              64
       On-line CPU(s) list: 0-63
       Thread(s) per core:  2
       Core(s) per socket:  32
       Socket(s):           1
       NUMA node(s):        2
       Vendor ID:           GenuineIntel
       BIOS Vendor ID:      Intel
       CPU family:          6
       Model:               106
       Model name:          Intel(R) Xeon(R) Gold 6314U CPU @ 2.30GHz
       BIOS Model name:     Intel(R) Xeon(R) Gold 6314U CPU @ 2.30GHz
       Stepping:            6
       CPU MHz:             3400.000
       CPU max MHz:         3400.0000
       CPU min MHz:         800.0000
       BogoMIPS:            4600.00
       L1d cache:           48K
       L1i cache:           32K
       L2 cache:            1280K
       L3 cache:            49152K
       NUMA node0 CPU(s):
       0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58
       ,60,62
       NUMA node1 CPU(s):
       1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59
       ,61,63
       Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
       pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
       lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
```

(Continued on next page)

SPEC CPU®2017 Floating Point Rate Result

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Dell Inc.

PowerEdge R650xs (Intel Xeon Gold 6314U, 2.30 GHz)

SPECrate®2017_fp_base =   240

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Nov-2022
Hardware Availability: Jul-2021
Software Availability: May-2022

Platform Notes (Continued)

```
     aperfmperf pni pclmulqdq dtes64 monitor ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr
     pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx
     f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single
     intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 avx2 smep
     bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
     clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves
     cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect wbnoinvd dtherm
     ida arat pln pts avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
     avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid fsrm md_clear pconfig
     flush_l1d arch_capabilities

 /proc/cpuinfo cache data
    cache size : 49152 KB

 From numactl --hardware
 WARNING: a numactl 'node' might or might not correspond to a physical chip.
   available: 2 nodes (0-1)
   node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46 48 50
   52 54 56 58 60 62
   node 0 size: 128152 MB
   node 0 free: 116288 MB
   node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47 49 51
   53 55 57 59 61 63
   node 1 size: 128972 MB
   node 1 free: 116936 MB
   node distances:
   node   0   1
     0:  10  11
     1:  11  10

 From /proc/meminfo
    MemTotal:       263295952 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 /sbin/tuned-adm active
     Current active profile: throughput-performance

 /sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
    performance

 From /etc/*release* /etc/*version*
    os-release:
       NAME="Red Hat Enterprise Linux"
       VERSION="8.6 (Ootpa)"
       ID="rhel"
       ID_LIKE="fedora"
```

(Continued on next page)

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Tested by: Dell Inc.

Test Date: Nov-2022
Hardware Availability: Jul-2021
Software Availability: May-2022

Platform Notes (Continued)

```
        VERSION_ID="8.6"
        PLATFORM_ID="platform:el8"
        PRETTY_NAME="Red Hat Enterprise Linux 8.6 (Ootpa)"
        ANSI_COLOR="0;31"
     redhat-release: Red Hat Enterprise Linux release 8.6 (Ootpa)
     system-release: Red Hat Enterprise Linux release 8.6 (Ootpa)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:8::baseos

  uname -a:
     Linux auvcetillem1m1.corp.emc.com 4.18.0-372.9.1.el8.x86_64 #1 SMP Fri Apr 15 22:12:19
     EDT 2022 x86_64 x86_64 x86_64 GNU/Linux


  Kernel self-reported vulnerability status:

  CVE-2018-12207 (iTLB Multihit):               Not affected
  CVE-2018-3620 (L1 Terminal Fault):            Not affected
  Microarchitectural Data Sampling:             Not affected
  CVE-2017-5754 (Meltdown):                     Not affected
  CVE-2018-3639 (Speculative Store Bypass):     Mitigation: Speculative Store
                                                Bypass disabled via prctl and
                                                seccomp
  CVE-2017-5753 (Spectre variant 1):            Mitigation: usercopy/swapgs
                                                barriers and __user pointer
                                                sanitization
  CVE-2017-5715 (Spectre variant 2):            Mitigation: Enhanced IBRS, IBPB:
                                                conditional, RSB filling
  CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
  CVE-2019-11135 (TSX Asynchronous Abort):      Not affected


  run-level 3 Nov 13 03:45

  SPEC is set to: /mnt/ramdisk/cpu2017-1.1.8-ic2022.1
     Filesystem      Type   Size  Used Avail Use% Mounted on
     tmpfs           tmpfs   60G   18G   43G  30% /mnt/ramdisk

  From /sys/devices/virtual/dmi/id
     Vendor:         Dell Inc.
     Product:        PowerEdge R650xs
     Product Family: PowerEdge
     Serial:         GL216S3

  Additional information from dmidecode 3.3 follows.  WARNING: Use caution when you
  interpret this section. The 'dmidecode' program reads system data which is "intended to
  allow hardware to be accurately determined", but the intent may not be met, as there are
  frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
     Memory:
       5x 002C00B3002C 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200
```

(Continued on next page)

Dell Inc.

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SPECrate®2017_fp_base = 240

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 55		**Test Date:**	Nov-2022
Test Sponsor: Dell Inc.		**Hardware Availability:**	Jul-2021
Tested by: Dell Inc.		**Software Availability:**	May-2022

Platform Notes (Continued)

```
    3x 002C069D002C 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200

 BIOS:
    BIOS Vendor:        Dell Inc.
    BIOS Version:       1.7.4
    BIOS Date:          06/27/2022
    BIOS Revision:      1.7

  (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C                 | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C++               | 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C++, C            | 511.povray_r(base) 526.blender_r(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C++, C, Fortran | 507.cactuBSSN_r(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
```

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PowerEdge R650xs (Intel Xeon Gold 6314U, 2.30 GHz)

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SPECrate®2017_fp_peak = Not Run

CPU2017 License: 55		**Test Date:**	Nov-2022
Test Sponsor: Dell Inc.		**Hardware Availability:** Jul-2021
Tested by: Dell Inc.		**Software Availability:** May-2022

Compiler Version Notes (Continued)

```
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
  2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------


==============================================================================
Fortran       | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
  2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------


==============================================================================
Fortran, C    | 521.wrf_r(base) 527.cam4_r(base)
------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
  2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
```
icx
```

C++ benchmarks:
```
icpx
```

Fortran benchmarks:
```
ifx
```

Benchmarks using both Fortran and C:
```
ifx icx
```

Benchmarks using both C and C++:
```
icpx icx
```

Benchmarks using Fortran, C, and C++:
```
icpx icx ifx
```

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Tested by:	Dell Inc.	**Software Availability:**	May-2022

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

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CPU2017 License: 55
Test Sponsor: Dell Inc.
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Test Date: Nov-2022
Hardware Availability: Jul-2021
Software Availability: May-2022

Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.html
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.2.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.2.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2022-11-13 07:29:15-0500.
Report generated on 2022-12-07 10:44:24 by CPU2017 PDF formatter v6442.
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