SPEC CPU®2017 Integer Rate Result

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Inspur Corporation

Inspur NF5280A6 (AMD EPYC 7663)

$SPECrate^{®}2017\_int\_base = 683$

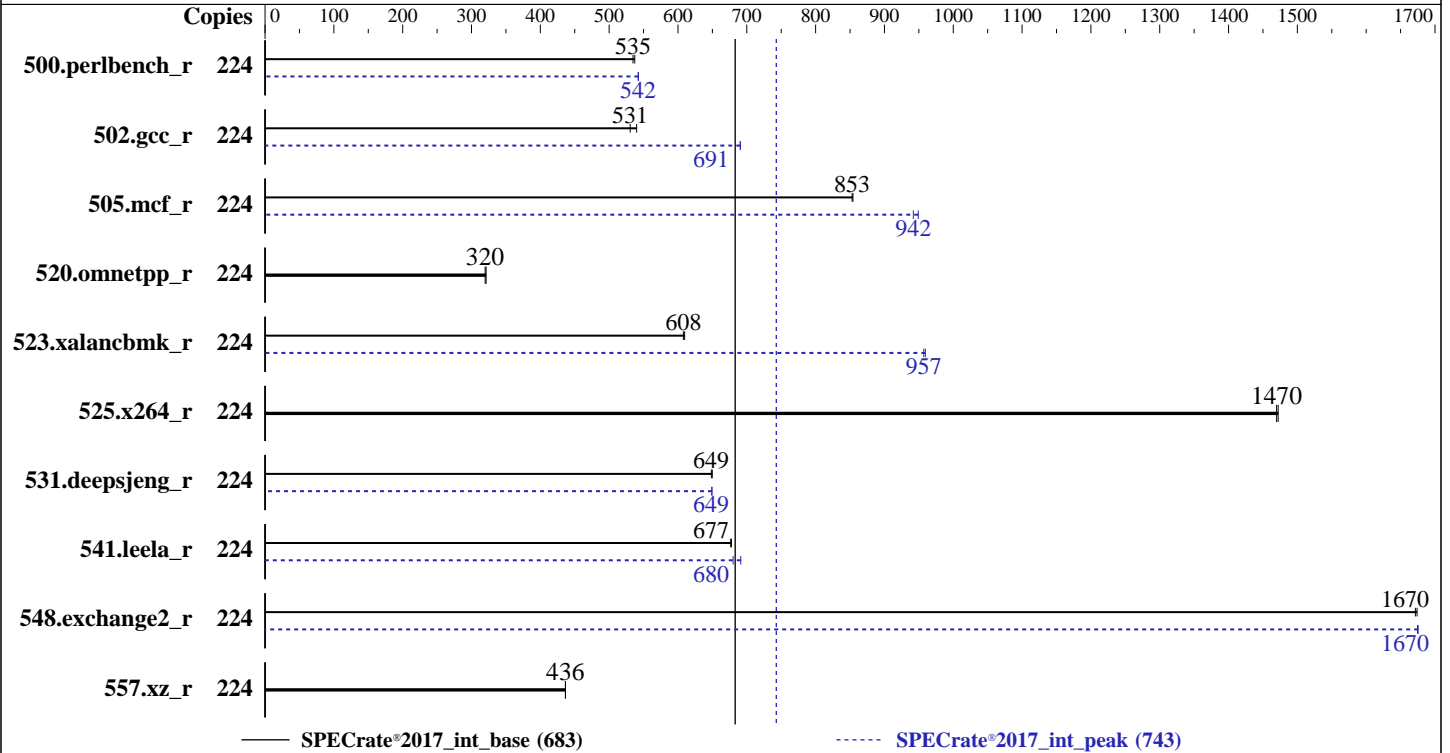$SPECrate^{®}2017\_int\_peak = 743$

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test Date: Jul-2022
Hardware Availability: Aug-2022
Software Availability: Dec-2021

Benchmark	Copies	base	peak
500.perlbench_r	224	535	542
502.gcc_r	224	531	691
505.mcf_r	224	853	942
520.omnetpp_r	224	320	
523.xalancbmk_r	224	608	957
525.x264_r	224	1470	
531.deepsjeng_r	224	649	649
541.leela_r	224	677	680
548.exchange2_r	224	1670	1670
557.xz_r	224	436	

SPECrate®2017_int_base (683)   SPECrate®2017_int_peak (743)

Hardware

CPU Name: AMD EPYC 7663
  Max MHz: 3500
  Nominal: 2000
Enabled: 112 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
  L2: 512 KB I+D on chip per core
  L3: 256 MB I+D on chip per chip, 32 MB shared / 7
       cores
  Other: None
Memory: 1 TB (32 x 32 GB 2Rx8 PC4-3200AA-R)
Storage: 1 x 480 GB SATA SSD
Other: None

Software

OS: Red Hat Enterprise Linux release 8.3 (Ootpa)
     4.18.0-240.el8.x86_64
Compiler: C/C++/Fortran: Version 3.2.0 of AOCC
Parallel: No
Firmware: Version 7.00.04 released May-2022
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc: jemalloc memory allocator library v5.1.0
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.

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Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
500.perlbench_r	224	664	537	**667**	**535**			224	**658**	**542**	657	543		
502.gcc_r	224	587	540	**598**	**531**			224	**459**	**691**	459	691		
505.mcf_r	224	424	854	**424**	**853**			224	381	949	**384**	**942**		
520.omnetpp_r	224	**919**	**320**	915	321			224	**919**	**320**	915	321		
523.xalancbmk_r	224	388	610	**389**	**608**			224	247	959	**247**	**957**		
525.x264_r	224	**267**	**1470**	266	1470			224	**267**	**1470**	266	1470		
531.deepsjeng_r	224	**396**	**649**	395	650			224	395	649	**396**	**649**		
541.leela_r	224	547	678	**548**	**677**			224	**545**	**680**	537	691		
548.exchange2_r	224	**351**	**1670**	351	1670			224	**350**	**1670**	350	1680		
557.xz_r	224	**554**	**436**	554	437			224	**554**	**436**	554	437		
SPECrate®2017_int_base =		683												
SPECrate®2017_int_peak =		743												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

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Operating System Notes (Continued)

```
Scaling_Governor set to Performance

To enable Transparent Hugepages (THP) only on request for base runs,
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root.
To enable THP for all allocations for peak runs,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
      "/home/CPU2017/amd_rate_aocc320_milanx_A_lib/lib;/home/CPU2017/amd_rate_
      aocc320_milanx_A_lib/lib32:"
MALLOC_CONF = "retain:true"

Environment variables set by runcpu during the 523.xalancbmk_r peak run:
MALLOC_CONF = "thp:never"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using OpenSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)
jemalloc 5.1.0 is available here:
https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2
```

Platform Notes

```
 BIOS configuration:
 SVM Mode = disable
 DRAM Scrub time = disable
 NUMA nodes per socket = NPS4
 APBDIS = 1
```

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Platform Notes (Continued)

```
Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
running on localhost.localdomain Tue Jul 26 18:42:35 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : AMD EPYC 7663 56-Core Processor
      2  "physical id"s (chips)
      224 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 56
      siblings  : 112
      physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
      28 29 30 32 33 34 35 36 37 38 40 41 42 43 44 45 46 48 49 50 51 52 53 54 56 57 58 59
      60 61 62
      physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
      28 29 30 32 33 34 35 36 37 38 40 41 42 43 44 45 46 48 49 50 51 52 53 54 56 57 58 59
      60 61 62

From lscpu from util-linux 2.32.1:
      Architecture:        x86_64
      CPU op-mode(s):      32-bit, 64-bit
      Byte Order:          Little Endian
      CPU(s):              224
      On-line CPU(s) list: 0-223
      Thread(s) per core:  2
      Core(s) per socket:  56
      Socket(s):           2
      NUMA node(s):        16
      Vendor ID:           AuthenticAMD
      CPU family:          25
      Model:               1
      Model name:          AMD EPYC 7663 56-Core Processor
      Stepping:            1
      CPU MHz:             2887.624
      CPU max MHz:         2000.0000
      CPU min MHz:         1500.0000
      BogoMIPS:            3992.51
      Virtualization:      AMD-V
      L1d cache:           32K
      L1i cache:           32K
      L2 cache:            512K
```

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CPU2017 License: 3358　　　　　　　　　　　　　　　　　　　　**Test Date:**　　　　Jul-2022
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Platform Notes (Continued)

```
        L3 cache:           32768K
        NUMA node0 CPU(s):  0-6,112-118
        NUMA node1 CPU(s):  7-13,119-125
        NUMA node2 CPU(s):  14-20,126-132
        NUMA node3 CPU(s):  21-27,133-139
        NUMA node4 CPU(s):  28-34,140-146
        NUMA node5 CPU(s):  35-41,147-153
        NUMA node6 CPU(s):  42-48,154-160
        NUMA node7 CPU(s):  49-55,161-167
        NUMA node8 CPU(s):  56-62,168-174
        NUMA node9 CPU(s):  63-69,175-181
        NUMA node10 CPU(s): 70-76,182-188
        NUMA node11 CPU(s): 77-83,189-195
        NUMA node12 CPU(s): 84-90,196-202
        NUMA node13 CPU(s): 91-97,203-209
        NUMA node14 CPU(s): 98-104,210-216
        NUMA node15 CPU(s): 105-111,217-223
        Flags:              fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
    pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
    constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf pni pclmulqdq
    monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand
    lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw
    ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
    cat_l3 cdp_l3 invpcid_single hw_pstate sme ssbd mba sev ibrs ibpb stibp vmmcall
    fsgsbase bmi1 avx2 smep bmi2 invpcid cqm rdt_a rdseed adx smap clflushopt clwb
    sha_ni xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
    cqm_mbm_local clzero irperf xsaveerptr wbnoinvd amd_ppin arat npt lbrv svm_lock
    nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold
    v_vmsave_vmload vgif umip pku ospke vaes vpclmulqdq rdpid overflow_recov succor smca

 /proc/cpuinfo cache data
    cache size : 512 KB

 From numactl --hardware
 WARNING: a numactl 'node' might or might not correspond to a physical chip.
   available: 16 nodes (0-15)
   node 0 cpus: 0 1 2 3 4 5 6 112 113 114 115 116 117 118
   node 0 size: 64242 MB
   node 0 free: 64079 MB
   node 1 cpus: 7 8 9 10 11 12 13 119 120 121 122 123 124 125
   node 1 size: 64428 MB
   node 1 free: 64302 MB
   node 2 cpus: 14 15 16 17 18 19 20 126 127 128 129 130 131 132
   node 2 size: 64458 MB
   node 2 free: 64258 MB
   node 3 cpus: 21 22 23 24 25 26 27 133 134 135 136 137 138 139
   node 3 size: 64432 MB
```

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Platform Notes (Continued)

```
     node 3 free: 64246 MB
     node 4 cpus: 28 29 30 31 32 33 34 140 141 142 143 144 145 146
     node 4 size: 64456 MB
     node 4 free: 64323 MB
     node 5 cpus: 35 36 37 38 39 40 41 147 148 149 150 151 152 153
     node 5 size: 64434 MB
     node 5 free: 64293 MB
     node 6 cpus: 42 43 44 45 46 47 48 154 155 156 157 158 159 160
     node 6 size: 64448 MB
     node 6 free: 64309 MB
     node 7 cpus: 49 50 51 52 53 54 55 161 162 163 164 165 166 167
     node 7 size: 64418 MB
     node 7 free: 64301 MB
     node 8 cpus: 56 57 58 59 60 61 62 168 169 170 171 172 173 174
     node 8 size: 64428 MB
     node 8 free: 64307 MB
     node 9 cpus: 63 64 65 66 67 68 69 175 176 177 178 179 180 181
     node 9 size: 64442 MB
     node 9 free: 64317 MB
     node 10 cpus: 70 71 72 73 74 75 76 182 183 184 185 186 187 188
     node 10 size: 64472 MB
     node 10 free: 64305 MB
     node 11 cpus: 77 78 79 80 81 82 83 189 190 191 192 193 194 195
     node 11 size: 64458 MB
     node 11 free: 64313 MB
     node 12 cpus: 84 85 86 87 88 89 90 196 197 198 199 200 201 202
     node 12 size: 64440 MB
     node 12 free: 64323 MB
     node 13 cpus: 91 92 93 94 95 96 97 203 204 205 206 207 208 209
     node 13 size: 64464 MB
     node 13 free: 64322 MB
     node 14 cpus: 98 99 100 101 102 103 104 210 211 212 213 214 215 216
     node 14 size: 64480 MB
     node 14 free: 64304 MB
     node 15 cpus: 105 106 107 108 109 110 111 217 218 219 220 221 222 223
     node 15 size: 64429 MB
     node 15 free: 64278 MB
     node distances:
     node   0   1   2   3   4   5   6   7   8   9  10  11  12  13  14  15
       0:  10  11  12  12  12  12  12  12  32  32  32  32  32  32  32  32
       1:  11  10  12  12  12  12  12  12  32  32  32  32  32  32  32  32
       2:  12  12  10  11  12  12  12  12  32  32  32  32  32  32  32  32
       3:  12  12  11  10  12  12  12  12  32  32  32  32  32  32  32  32
       4:  12  12  12  12  10  11  12  12  32  32  32  32  32  32  32  32
       5:  12  12  12  12  11  10  12  12  32  32  32  32  32  32  32  32
       6:  12  12  12  12  12  12  10  11  32  32  32  32  32  32  32  32
       7:  12  12  12  12  12  12  11  10  32  32  32  32  32  32  32  32
```

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Platform Notes (Continued)

```
    8:  32  32  32  32  32  32  32  32  10  11  12  12  12  12  12  12
    9:  32  32  32  32  32  32  32  32  11  10  12  12  12  12  12  12
   10:  32  32  32  32  32  32  32  32  12  12  10  11  12  12  12  12
   11:  32  32  32  32  32  32  32  32  12  12  11  10  12  12  12  12
   12:  32  32  32  32  32  32  32  32  12  12  12  12  10  11  12  12
   13:  32  32  32  32  32  32  32  32  12  12  12  12  11  10  12  12
   14:  32  32  32  32  32  32  32  32  12  12  12  12  12  12  10  11
   15:  32  32  32  32  32  32  32  32  12  12  12  12  12  12  11  10

 From /proc/meminfo
    MemTotal:       1056648192 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 /sbin/tuned-adm active
     Current active profile: throughput-performance

 /sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
    performance

 From /etc/*release* /etc/*version*
    os-release:
       NAME="Red Hat Enterprise Linux"
       VERSION="8.3 (Ootpa)"
       ID="rhel"
       ID_LIKE="fedora"
       VERSION_ID="8.3"
       PLATFORM_ID="platform:el8"
       PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
       ANSI_COLOR="0;31"
    redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
    system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

 uname -a:
    Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
    x86_64 x86_64 x86_64 GNU/Linux

 Kernel self-reported vulnerability status:

 CVE-2018-12207 (iTLB Multihit):                    Not affected
 CVE-2018-3620 (L1 Terminal Fault):                 Not affected
 Microarchitectural Data Sampling:                  Not affected
 CVE-2017-5754 (Meltdown):                          Not affected
 CVE-2018-3639 (Speculative Store Bypass):          Mitigation: Speculative Store
                                                    Bypass disabled via prctl and
                                                    seccomp
```

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Platform Notes (Continued)

```
CVE-2017-5753 (Spectre variant 1):                    Mitigation: usercopy/swapgs
                                                      barriers and __user pointer
                                                      sanitization
CVE-2017-5715 (Spectre variant 2):                    Mitigation: Full AMD retpoline,
                                                     IBPB: conditional, IBRS_FW, STIBP:
                                                      always-on, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort):              Not affected

 run-level 3 Jul 21 15:07

 SPEC is set to: /home/CPU2017
    Filesystem              Type  Size  Used Avail Use% Mounted on
    /dev/mapper/rhel-home xfs   372G  8.7G  363G   3% /home

 From /sys/devices/virtual/dmi/id
    Vendor:          Inspur
    Product:         NF5280A6
    Product Family:  Not specified
    Serial:          200857231

 Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you
 interpret this section. The 'dmidecode' program reads system data which is "intended to
 allow hardware to be accurately determined", but the intent may not be met, as there are
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   Memory:
     32x Samsung M393A4K40EB3-CWE 32 GB 2 rank 3200

 BIOS:
    BIOS Vendor:       American Megatrends Inc.
    BIOS Version:      7.00.04
    BIOS Date:         05/19/2022

 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C       | 502.gcc_r(peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------
```

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Compiler Version Notes (Continued)

```
==============================================================================
C        | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
         | 525.x264_r(base, peak) 557.xz_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------


==============================================================================
C        | 502.gcc_r(peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
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         | 525.x264_r(base, peak) 557.xz_r(base, peak)
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Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------


==============================================================================
C++      | 523.xalancbmk_r(peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------


==============================================================================
C++      | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
         | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
------------------------------------------------------------------------------
```

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Target: x86_64-unknown-linux-gnu
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C++      | 523.xalancbmk_r(peak)
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------------------------------------------------------------------------------


==============================================================================
C++      | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
         | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------


==============================================================================
Fortran | 548.exchange2_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

(Continued on next page)

SPEC CPU®2017 Integer Rate Result

Inspur Corporation

Inspur NF5280A6 (AMD EPYC 7663)

SPECrate®2017_int_base = 683

SPECrate®2017_int_peak = 743

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test Date: Jul-2022
Hardware Availability: Aug-2022
Software Availability: Dec-2021

Base Compiler Invocation (Continued)

Fortran benchmarks:
```
flang
```

Base Portability Flags

500.perlbench_r: `-DSPEC_LINUX_X64 -DSPEC_LP64`
502.gcc_r: `-DSPEC_LP64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LINUX -DSPEC_LP64`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
```
-m64 -Wl,-allow-multiple-definition -Wl,-mllvm -Wl,-enable-licm-vrp
-flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -z muldefs -lamdlibm -ljemalloc -lflang
```

C++ benchmarks:
```
-m64 -std=c++98 -flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
```

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https://www.spec.org/

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Base Optimization Flags (Continued)

C++ benchmarks (continued):
```
-mllvm -extra-vectorizer-passes -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -convert-pow-exp-to-int=false
-mllvm -enable-loop-fusion -z muldefs -fvirtual-function-elimination
-fvisibility=hidden -lamdlibm -ljemalloc -lflang
```

Fortran benchmarks:
```
-m64 -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -Wl,-mllvm -Wl,-enable-iv-split
-flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -z muldefs -mllvm -unroll-aggressive
-mllvm -unroll-threshold=500 -lamdlibm -ljemalloc -lflang
```

Base Other Flags

C benchmarks:
```
-Wno-unused-command-line-argument
```

C++ benchmarks:
```
-Wno-unused-command-line-argument
```

Peak Compiler Invocation

C benchmarks:
```
clang
```

C++ benchmarks:
```
clang++
```

Fortran benchmarks:
```
flang
```

Peak Portability Flags

500.perlbench_r: `-DSPEC_LINUX_X64 -DSPEC_LP64`

(Continued on next page)

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Peak Portability Flags (Continued)

502.gcc_r: `-D_FILE_OFFSET_BITS=64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LINUX -DSPEC_LP64`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Peak Optimization Flags

C benchmarks:

500.perlbench_r: `-m64 -Wl,-allow-multiple-definition`
`-Wl,-mllvm -Wl,-enable-licm-vrp -flto`
`-Wl,-mllvm -Wl,-function-specialize`
`-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
`-Wl,-mllvm -Wl,-reduce-array-computations=3`
`-fprofile-instr-generate`(pass 1)
`-fprofile-instr-use`(pass 2) `-Ofast -march=znver3`
`-fveclib=AMDLIBM -ffast-math -fstruct-layout=7`
`-mllvm -unroll-threshold=50 -fremap-arrays`
`-flv-function-specialization -mllvm -inline-threshold=1000`
`-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=false`
`-mllvm -function-specialize -mllvm -enable-licm-vrp`
`-mllvm -reduce-array-computations=3 -lamdlibm -ljemalloc`

502.gcc_r: `-m32 -Wl,-allow-multiple-definition`
`-Wl,-mllvm -Wl,-enable-licm-vrp -flto`
`-Wl,-mllvm -Wl,-function-specialize -Ofast -march=znver3`
`-fveclib=AMDLIBM -ffast-math -fstruct-layout=7`
`-mllvm -unroll-threshold=50 -fremap-arrays`
`-flv-function-specialization -mllvm -inline-threshold=1000`
`-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true`
`-mllvm -function-specialize -mllvm -enable-licm-vrp`
`-mllvm -reduce-array-computations=3 -fgnu89-inline`
`-ljemalloc`

505.mcf_r: `-m64 -Wl,-allow-multiple-definition`
`-Wl,-mllvm -Wl,-enable-licm-vrp -flto`
`-Wl,-mllvm -Wl,-function-specialize`
`-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
`-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast`

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Peak Optimization Flags (Continued)

505.mcf_r (continued):
```
-march=znver3 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -lamdlibm -ljemalloc
```

525.x264_r: basepeak = yes

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: `-m32 -Wl,-mllvm -Wl,-do-block-reorder=aggressive -flto`
```
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-flv-function-specialization -mllvm -enable-licm-vrp
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-ljemalloc
```

531.deepsjeng_r: `-m64 -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize`
```
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-flv-function-specialization -mllvm -enable-licm-vrp
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -ljemalloc
```

541.leela_r: Same as 531.deepsjeng_r

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Peak Optimization Flags (Continued)

Fortran benchmarks:
```
-m64 -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -Wl,-mllvm -Wl,-enable-iv-split
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -mllvm -unroll-aggressive
-mllvm -unroll-threshold=500 -lamdlibm -ljemalloc -lflang
```

Peak Other Flags

C benchmarks (except as noted below):
```
-Wno-unused-command-line-argument
```

502.gcc_r: `-L/usr/lib -Wno-unused-command-line-argument`
`-L/sppo/bin/cpu2017v118-aocc3-milanX/amd_rate_aocc320_milanx_A_lib/lib32`

C++ benchmarks (except as noted below):
```
-Wno-unused-command-line-argument
```

523.xalancbmk_r: `-L/usr/lib -Wno-unused-command-line-argument`
`-L/sppo/bin/cpu2017v118-aocc3-milanX/amd_rate_aocc320_milanx_A_lib/lib32`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/aocc320-flags-A1.html
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-amd-V1.3.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/aocc320-flags-A1.xml
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-amd-V1.3.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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