SPEC CPU®2017 Floating Point Speed Result

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Supermicro
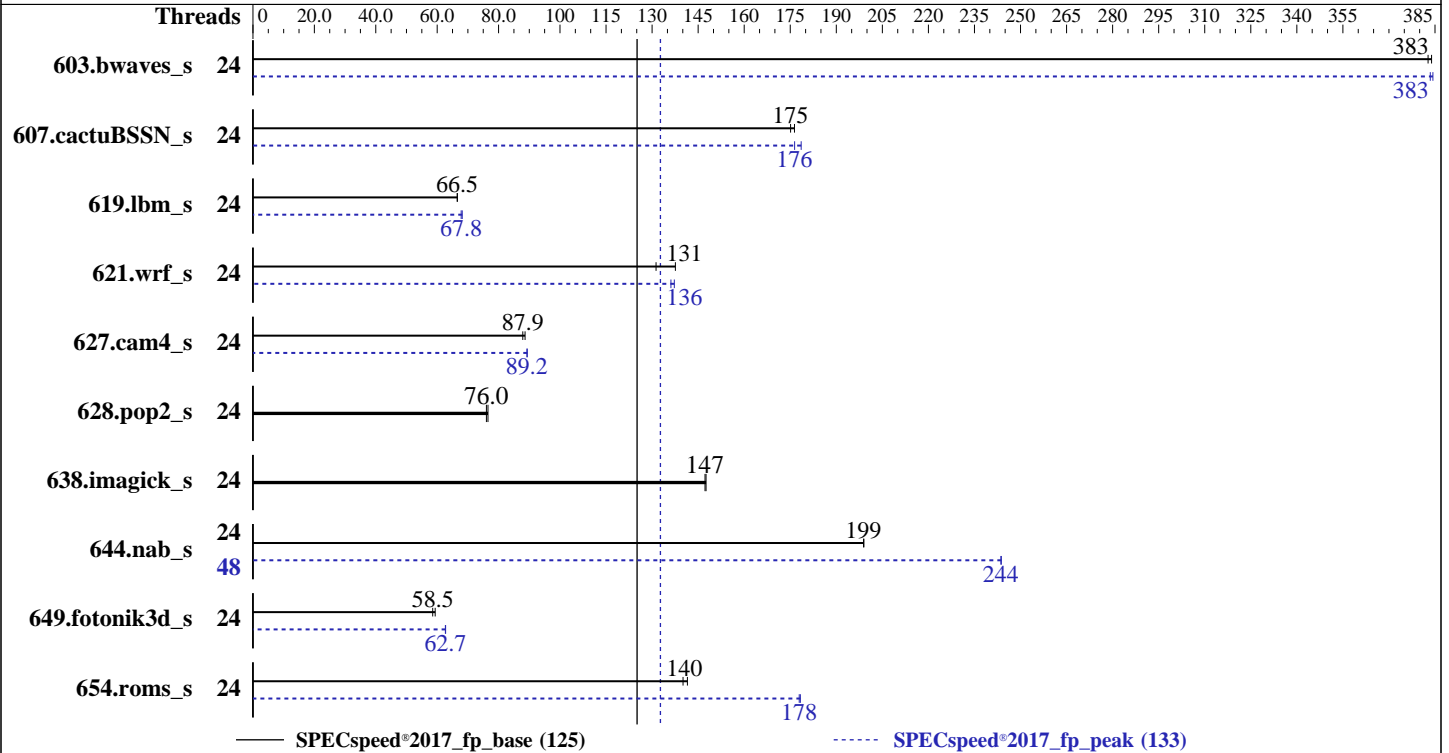
A+ Server 2014CS-TR
(H12SSW-AN6 , AMD EPYC 7473X)

$SPECspeed®2017\_fp\_base = 125$

$SPECspeed®2017\_fp\_peak = 133$

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Feb-2022

Hardware Availability: Mar-2021

Software Availability: Feb-2022

Threads		
603.bwaves_s	24	383 / 383
607.cactuBSSN_s	24	175 / 176
619.lbm_s	24	66.5 / 67.8
621.wrf_s	24	131 / 136
627.cam4_s	24	87.9 / 89.2
628.pop2_s	24	76.0
638.imagick_s	24	147
644.nab_s	24 / 48	199 / 244
649.fotonik3d_s	24	58.5 / 62.7
654.roms_s	24	140 / 178

SPECspeed®2017_fp_base (125) ------- SPECspeed®2017_fp_peak (133)

Hardware

CPU Name: AMD EPYC 7473X
Max MHz: 3700
Nominal: 2800
Enabled: 24 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 512 KB I+D on chip per core
 L3: 768 MB I+D on chip per chip, 96 MB shared / 3 cores
Other: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R, running at 2933)
Storage: 1 x 3 TB SATA III, 7200 RPM
Other: None

Software

OS: Ubuntu 20.04.3 LTS
Kernel 5.4.0-100-generic
Compiler: C/C++/Fortran: Version 3.2.0 of AOCC
Parallel: Yes
Firmware: Version 2.4 released Feb-2022
File System: ext4
System State: Run level 5 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc: jemalloc memory allocator library v5.1.0
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.

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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	24	154	384	**154**	**383**			24	154	384	**154**	**383**		
607.cactuBSSN_s	24	94.5	176	**95.2**	**175**			24	**94.5**	**176**	93.4	179		
619.lbm_s	24	**78.7**	**66.5**	78.7	66.6			24	76.8	68.2	**77.2**	**67.8**		
621.wrf_s	24	**101**	**131**	96.1	138			24	**97.1**	**136**	96.4	137		
627.cam4_s	24	100	88.6	**101**	**87.9**			24	**99.3**	**89.2**	99.2	89.3		
628.pop2_s	24	155	76.5	**156**	**76.0**			24	155	76.5	**156**	**76.0**		
638.imagick_s	24	97.8	148	**98.0**	**147**			24	97.8	148	**98.0**	**147**		
644.nab_s	24	87.8	199	**87.8**	**199**			48	**71.7**	**244**	71.7	244		
649.fotonik3d_s	24	**156**	**58.5**	154	59.4			24	145	62.8	**145**	**62.7**		
654.roms_s	24	111	142	**112**	**140**			24	**88.5**	**178**	88.3	178		
SPECspeed®2017_fp_base =	**125**													
SPECspeed®2017_fp_peak =	**133**													

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
```

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Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
To enable THP only on request for peak runs of 628.pop2_s:
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root.
To disable THP for peak runs of 627.cam4_s, 649.fotonik3d_s, and 654.roms_s,
'echo never > /sys/kernel/mm/transparent_hugepage/enabled' run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
GOMP_CPU_AFFINITY = "0-47"
LD_LIBRARY_PATH =
      "/home/cpu2017/amd_speed_aocc320_milanx_A_lib/lib;/home/cpu2017/amd_spee
      d_aocc320_milanx_A_lib/lib32:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "48"

Environment variables set by runcpu during the 603.bwaves_s peak run:
GOMP_CPU_AFFINITY = "0-23"

Environment variables set by runcpu during the 607.cactuBSSN_s peak run:
GOMP_CPU_AFFINITY = "0-23"

Environment variables set by runcpu during the 619.lbm_s peak run:
GOMP_CPU_AFFINITY = "0-23"

Environment variables set by runcpu during the 621.wrf_s peak run:
GOMP_CPU_AFFINITY = "0-23"

Environment variables set by runcpu during the 627.cam4_s peak run:
GOMP_CPU_AFFINITY = "0-23"

Environment variables set by runcpu during the 644.nab_s peak run:
GOMP_CPU_AFFINITY = "0-47"

Environment variables set by runcpu during the 649.fotonik3d_s peak run:
GOMP_CPU_AFFINITY = "0-23"
PGHPF_ZMEM = "yes"

Environment variables set by runcpu during the 654.roms_s peak run:
GOMP_CPU_AFFINITY = "0-23"
```

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General Notes

Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using openSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)
jemalloc 5.1.0 is available here:
https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2

Platform Notes

BIOS Settings:
Determinism Control = Manual
Determinism Slider = Power
cTDP Control = Manual
cTDP = 280
Package Power Limit Control = Manual
Package Power Limit = 280
APBDIS = 1
NUMA Nodes Per Socket = NPS4

 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
 running on h12ssw-an6-7473x Fri Feb 18 21:52:31 2022

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : AMD EPYC 7473X 24-Core Processor
       1 "physical id"s (chips)
       48 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
       cpu cores : 24
       siblings : 48
       physical 0: cores 0 1 2 4 5 6 8 9 10 12 13 14 16 17 18 20 21 22 24 25 26 28 29 30

 From lscpu from util-linux 2.34:
    Architecture: x86_64

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CPU2017 License: 001176   **Test Date:**   Feb-2022
Test Sponsor: Supermicro   **Hardware Availability:** Mar-2021
Tested by: Supermicro   **Software Availability:** Feb-2022

Platform Notes (Continued)

```
        CPU op-mode(s):                 32-bit, 64-bit
        Byte Order:                     Little Endian
        Address sizes:                  48 bits physical, 48 bits virtual
        CPU(s):                         48
        On-line CPU(s) list:            0-47
        Thread(s) per core:             2
        Core(s) per socket:             24
        Socket(s):                      1
        NUMA node(s):                   8
        Vendor ID:                      AuthenticAMD
        CPU family:                     25
        Model:                          1
        Model name:                     AMD EPYC 7473X 24-Core Processor
        Stepping:                       2
        Frequency boost:                enabled
        CPU MHz:                        1930.837
        CPU max MHz:                    2800.0000
        CPU min MHz:                    1500.0000
        BogoMIPS:                       5599.63
        Virtualization:                 AMD-V
        L1d cache:                      768 KiB
        L1i cache:                      768 KiB
        L2 cache:                       12 MiB
        L3 cache:                       768 MiB
        NUMA node0 CPU(s):              0-2,24-26
        NUMA node1 CPU(s):              3-5,27-29
        NUMA node2 CPU(s):              6-8,30-32
        NUMA node3 CPU(s):              9-11,33-35
        NUMA node4 CPU(s):              12-14,36-38
        NUMA node5 CPU(s):              15-17,39-41
        NUMA node6 CPU(s):              18-20,42-44
        NUMA node7 CPU(s):              21-23,45-47
        Vulnerability Itlb multihit:    Not affected
        Vulnerability L1tf:             Not affected
        Vulnerability Mds:              Not affected
        Vulnerability Meltdown:         Not affected
        Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via
        prctl and seccomp
        Vulnerability Spectre v1:        Mitigation; usercopy/swapgs barriers and __user
        pointer sanitization
        Vulnerability Spectre v2:        Mitigation; Full AMD retpoline, IBPB conditional,
        IBRS_FW, STIBP always-on, RSB filling
        Vulnerability Srbds:            Not affected
        Vulnerability Tsx async abort:  Not affected
        Flags:                          fpu vme de pse tsc msr pae mce cx8 apic sep mtrr
        pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt
        pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid
```

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Platform Notes (Continued)

```
     aperfmperf pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe popcnt aes
     xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a
     misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb
     bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 invpcid_single hw_pstate ssbd mba ibrs
     ibpb stibp vmmcall fsgsbase bmi1 avx2 smep bmi2 invpcid cqm rdt_a rdseed adx smap
     clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc
     cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr wbnoinvd arat npt lbrv svm_lock
     nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold
     v_vmsave_vmload vgif umip pku ospke vaes vpclmulqdq rdpid overflow_recov succor smca


 From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE          LEVEL
     L1d       32K     768K    8 Data             1
     L1i       32K     768K    8 Instruction      1
     L2       512K      12M    8 Unified          2
     L3        96M     768M   16 Unified          3


 /proc/cpuinfo cache data
    cache size : 512 KB


 From numactl --hardware
 WARNING: a numactl 'node' might or might not correspond to a physical chip.
   available: 8 nodes (0-7)
   node 0 cpus: 0 1 2 24 25 26
   node 0 size: 64387 MB
   node 0 free: 64081 MB
   node 1 cpus: 3 4 5 27 28 29
   node 1 size: 64508 MB
   node 1 free: 64284 MB
   node 2 cpus: 6 7 8 30 31 32
   node 2 size: 64510 MB
   node 2 free: 64009 MB
   node 3 cpus: 9 10 11 33 34 35
   node 3 size: 64509 MB
   node 3 free: 64400 MB
   node 4 cpus: 12 13 14 36 37 38
   node 4 size: 64510 MB
   node 4 free: 64278 MB
   node 5 cpus: 15 16 17 39 40 41
   node 5 size: 64480 MB
   node 5 free: 64292 MB
   node 6 cpus: 18 19 20 42 43 44
   node 6 size: 64510 MB
   node 6 free: 63058 MB
   node 7 cpus: 21 22 23 45 46 47
   node 7 size: 64496 MB
   node 7 free: 64304 MB
```

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Platform Notes (Continued)

```
  node distances:
  node    0    1    2    3    4    5    6    7
    0:   10   11   12   12   12   12   12   12
    1:   11   10   12   12   12   12   12   12
    2:   12   12   10   11   12   12   12   12
    3:   12   12   11   10   12   12   12   12
    4:   12   12   12   12   10   11   12   12
    5:   12   12   12   12   11   10   12   12
    6:   12   12   12   12   12   12   10   11
    7:   12   12   12   12   12   12   11   10

 From /proc/meminfo
    MemTotal:        528295992 kB
    HugePages_Total:         0
    Hugepagesize:         2048 kB

 /sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
    performance

 /usr/bin/lsb_release -d
    Ubuntu 20.04.3 LTS

 From /etc/*release* /etc/*version*
    debian_version: bullseye/sid
    os-release:
       NAME="Ubuntu"
       VERSION="20.04.3 LTS (Focal Fossa)"
       ID=ubuntu
       ID_LIKE=debian
       PRETTY_NAME="Ubuntu 20.04.3 LTS"
       VERSION_ID="20.04"
       HOME_URL="https://www.ubuntu.com/"
       SUPPORT_URL="https://help.ubuntu.com/"

 uname -a:
    Linux h12ssw-an6-7473x 5.4.0-100-generic #113-Ubuntu SMP Thu Feb 3 18:43:29 UTC 2022
    x86_64 x86_64 x86_64 GNU/Linux

 Kernel self-reported vulnerability status:

 CVE-2018-12207 (iTLB Multihit):                   Not affected
 CVE-2018-3620 (L1 Terminal Fault):                Not affected
 Microarchitectural Data Sampling:                 Not affected
 CVE-2017-5754 (Meltdown):                         Not affected
 CVE-2018-3639 (Speculative Store Bypass):         Mitigation: Speculative Store
                                                   Bypass disabled via prctl and
                                                   seccomp
```

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Platform Notes (Continued)

```
CVE-2017-5753 (Spectre variant 1):                    Mitigation: usercopy/swapgs
                                                      barriers and __user pointer
                                                      sanitization
CVE-2017-5715 (Spectre variant 2):                    Mitigation: Full AMD retpoline,
                                                   IBPB: conditional, IBRS_FW, STIBP:
                                                      always-on, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort):              Not affected

run-level 5 Feb 18 07:53

SPEC is set to: /home/cpu2017
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      ext4  2.7T   17G  2.6T   1% /

From /sys/devices/virtual/dmi/id
   Vendor:         Supermicro
   Product:        Super Server
   Serial:         0123456789

Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you
interpret this section. The 'dmidecode' program reads system data which is "intended to
allow hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  Memory:
    16x Micron Technology 36ASF4G72PZ-3G2J3 32 GB 2 rank 3200, configured at 2933

BIOS:
  BIOS Vendor:     American Megatrends Inc.
  BIOS Version:    2.4
  BIOS Date:       02/07/2022
  BIOS Revision:   5.22

(End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
                | 644.nab_s(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
```

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Compiler Version Notes (Continued)

```
--------------------------------------------------------------------------

==========================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
--------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
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  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
--------------------------------------------------------------------------

==========================================================================
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)
                | 654.roms_s(base, peak)
--------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
--------------------------------------------------------------------------

==========================================================================
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak)
                | 628.pop2_s(base, peak)
--------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
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Compiler Version Notes (Continued)

Base Compiler Invocation

C benchmarks:
```
clang
```

Fortran benchmarks:
```
flang
```

Benchmarks using both Fortran and C:
```
flang clang
```

Benchmarks using Fortran, C, and C++:
```
clang++ clang flang
```

Base Portability Flags

```
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:
```
-m64 -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -z muldefs
```

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A+ Server 2014CS-TR
(H12SSW-AN6 , AMD EPYC 7473X)

SPECspeed®2017_fp_base = 125

SPECspeed®2017_fp_peak = 133

CPU2017 License: 001176　　　　　　　　　　　　　**Test Date:** Feb-2022
Test Sponsor: Supermicro　　　　　　　　　　　　**Hardware Availability:** Mar-2021
Tested by: Supermicro　　　　　　　　　　　　　　**Software Availability:** Feb-2022

Base Optimization Flags (Continued)

C benchmarks (continued):
```
-DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

Fortran benchmarks:
```
-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Hz,1,0x1 -O3
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-loopinterchange
-mllvm -compute-interchange-order -z muldefs -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

Benchmarks using both Fortran and C:
```
-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -Hz,1,0x1
-Mrecursive -mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
-z muldefs -DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-lflang
```

Benchmarks using Fortran, C, and C++:
```
-m64 -Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-partial-unswitch -mllvm -unroll-threshold=100
-finline-aggressive -mllvm -loop-unswitch-threshold=200000
```

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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):
```
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -extra-vectorizer-passes -mllvm -convert-pow-exp-to-int=false
-Hz,1,0x1 -Mrecursive -mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -enable-loopinterchange
-mllvm -compute-interchange-order -z muldefs -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

Base Other Flags

C benchmarks:
```
-Wno-unused-command-line-argument -Wno-return-type
```

Fortran benchmarks:
```
-Wno-unused-command-line-argument -Wno-return-type
```

Benchmarks using both Fortran and C:
```
-Wno-unused-command-line-argument -Wno-return-type
```

Benchmarks using Fortran, C, and C++:
```
-Wno-unused-command-line-argument -Wno-return-type
```

Peak Compiler Invocation

C benchmarks:
```
clang
```

Fortran benchmarks:
```
flang
```

Benchmarks using both Fortran and C:
```
flang clang
```

Benchmarks using Fortran, C, and C++:
```
clang++ clang flang
```

Peak Portability Flags

```
Same as Base Portability Flags
```

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Peak Optimization Flags

C benchmarks:

619.lbm_s: -m64 -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=5 -mllvm -unroll-threshold=50
-fremap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang

638.imagick_s: basepeak = yes

644.nab_s: -m64 -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -do-block-reorder=aggressive -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang

Fortran benchmarks:

603.bwaves_s: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-Mrecursive -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp
-DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-lflang

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Peak Optimization Flags (Continued)

649.fotonik3d_s: `-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching`
`-Wl,-mllvm -Wl,-enable-licm-vrp`
`-Wl,-mllvm -Wl,-function-specialize`
`-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
`-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast`
`-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp`
`-flto -Mrecursive -mllvm -reduce-array-computations=3`
`-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp`
`-DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc`
`-lflang`

654.roms_s: `Same as 603.bwaves_s`

Benchmarks using both Fortran and C:

621.wrf_s: `-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching`
`-Wl,-mllvm -Wl,-enable-licm-vrp`
`-Wl,-mllvm -Wl,-function-specialize`
`-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
`-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast`
`-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp`
`-flto -fstruct-layout=5 -mllvm -unroll-threshold=50`
`-fremap-arrays -flv-function-specialization`
`-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist`
`-mllvm -global-vectorize-slp=true`
`-mllvm -function-specialize -mllvm -enable-licm-vrp`
`-mllvm -reduce-array-computations=3 -Hz,1,0x1 -Mrecursive`
`-mllvm -fuse-tile-inner-loop -funroll-loops`
`-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop`
`-mllvm -enable-loopinterchange`
`-mllvm -compute-interchange-order -DSPEC_OPENMP`
`-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang`

627.cam4_s: `Same as 621.wrf_s`

628.pop2_s: `basepeak = yes`

Benchmarks using Fortran, C, and C++:
`-m64 -Wl,-mllvm -Wl,-x86-use-vzeroupper=false`
`-Wl,-mllvm -Wl,-enable-licm-vrp`
`-Wl,-mllvm -Wl,-do-block-reorder=aggressive`
`-Wl,-mllvm -Wl,-function-specialize`
`-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
`-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver3`
`-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5`
`-mllvm -unroll-threshold=50 -fremap-arrays -flv-function-specialization`

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Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):
```
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true -mllvm -function-specialize
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-finline-aggressive -mllvm -unroll-threshold=100 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -Mrecursive
-mllvm -do-block-reorder=aggressive -DSPEC_OPENMP -fopenmp=libomp
-lomp -lamdlibm -ljemalloc -lflang
```

Peak Other Flags

C benchmarks:
```
-Wno-unused-command-line-argument -Wno-return-type
```

Fortran benchmarks:
```
-Wno-unused-command-line-argument -Wno-return-type
```

Benchmarks using both Fortran and C:
```
-Wno-unused-command-line-argument -Wno-return-type
```

Benchmarks using Fortran, C, and C++:
```
-Wno-unused-command-line-argument -Wno-return-type
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/aocc320-flags-A1.html
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Milan-revD.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/aocc320-flags-A1.xml
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Milan-revD.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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