



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

## Hewlett Packard Enterprise

(Test Sponsor: HPE)

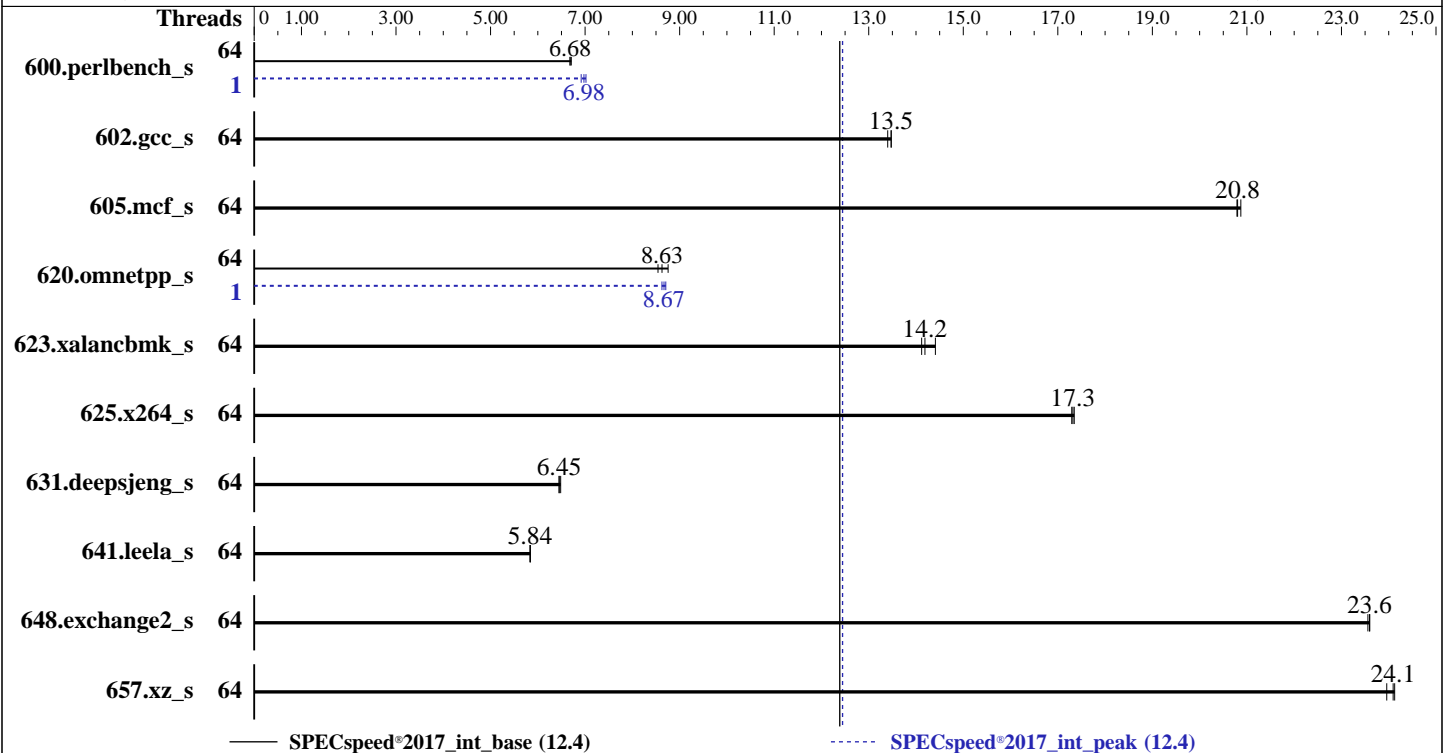
ProLiant DL325 Gen10 Plus v2  
(2.00 GHz, AMD EPYC 7713P)

SPECspeed®2017\_int\_base = 12.4

SPECspeed®2017\_int\_peak = 12.4

CPU2017 License: 3  
Test Sponsor: HPE  
Tested by: HPE

Test Date: Jul-2021  
Hardware Availability: Jun-2021  
Software Availability: Mar-2021



### Hardware

CPU Name: AMD EPYC 7713P  
 Max MHz: 3675  
 Nominal: 2000  
 Enabled: 64 cores, 1 chip  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 512 KB I+D on chip per core  
 L3: 256 MB I+D on chip per chip,  
 32 MB shared / 8 cores  
 Other: None  
 Memory: 1 TB (8 x 128 GB 4Rx4 PC4-3200AA-L)  
 Storage: 1 x 800 GB SAS SSD, RAID 0  
 Other: None

### Software

OS: Ubuntu 20.04.1 LTS (x86\_64)  
 Kernel 5.4.0-54-generic  
 Compiler: C/C++/Fortran: Version 3.0.0 of AOCC  
 Parallel: Yes  
 Firmware: HPE BIOS Version A43 v2.42 04/15/2021 released  
 Apr-2021  
 File System: ext4  
 System State: Run level 5 (multi-user, GUI disabled)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc: jemalloc memory allocator library v5.1.0  
 Power Management: BIOS set to prefer performance at the cost of  
 additional power usage



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL325 Gen10 Plus v2  
(2.00 GHz, AMD EPYC 7713P)

SPECspeed®2017\_int\_base = 12.4

SPECspeed®2017\_int\_peak = 12.4

CPU2017 License: 3  
Test Sponsor: HPE  
Tested by: HPE

Test Date: Jul-2021  
Hardware Availability: Jun-2021  
Software Availability: Mar-2021

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	64	265	6.71	266	6.68	<b><u>266</u></b>	<b><u>6.68</u></b>	1	257	6.92	253	7.02	<b><u>254</u></b>	<b><u>6.98</u></b>
602.gcc_s	64	297	13.4	295	13.5	<b><u>296</u></b>	<b><u>13.5</u></b>	64	297	13.4	295	13.5	<b><u>296</u></b>	<b><u>13.5</u></b>
605.mcf_s	64	227	20.8	<b><u>227</u></b>	<b><u>20.8</u></b>	226	20.9	64	227	20.8	<b><u>227</u></b>	<b><u>20.8</u></b>	226	20.9
620.omnetpp_s	64	186	8.76	191	8.54	<b><u>189</u></b>	<b><u>8.63</u></b>	1	<b><u>188</u></b>	<b><u>8.67</u></b>	187	8.71	189	8.63
623.xalancbmk_s	64	<b><u>99.9</u></b>	<b><u>14.2</u></b>	100	14.1	98.3	14.4	64	<b><u>99.9</u></b>	<b><u>14.2</u></b>	100	14.1	98.3	14.4
625.x264_s	64	<b><u>102</u></b>	<b><u>17.3</u></b>	102	17.3	102	17.3	64	<b><u>102</u></b>	<b><u>17.3</u></b>	102	17.3	102	17.3
631.deepsjeng_s	64	<b><u>222</u></b>	<b><u>6.45</u></b>	222	6.45	221	6.48	64	<b><u>222</u></b>	<b><u>6.45</u></b>	222	6.45	221	6.48
641.leela_s	64	<b><u>292</u></b>	<b><u>5.84</u></b>	292	5.84	292	5.84	64	<b><u>292</u></b>	<b><u>5.84</u></b>	292	5.84	292	5.84
648.exchange2_s	64	125	23.6	125	23.6	<b><u>125</u></b>	<b><u>23.6</u></b>	64	125	23.6	125	23.6	<b><u>125</u></b>	<b><u>23.6</u></b>
657.xz_s	64	256	24.1	<b><u>257</u></b>	<b><u>24.1</u></b>	258	24.0	64	256	24.1	<b><u>257</u></b>	<b><u>24.1</u></b>	258	24.0

SPECspeed®2017\_int\_base = 12.4

SPECspeed®2017\_int\_peak = 12.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
'echo 8 > /proc/sys/vm/dirty_ratio' run as root to limit dirty cache to 8% of
memory.
'echo 1 > /proc/sys/vm/swappiness' run as root to limit swap usage to minimum
necessary.
'echo 1 > /proc/sys/vm/zone_reclaim_mode' run as root to free node-local memory
and avoid remote memory usage.
'sync; echo 3 > /proc/sys/vm/drop_caches' run as root to reset filesystem caches.
'sysctl -w kernel.randomize_va_space=0' run as root to disable address space layout
randomization (ASLR) to reduce run-to-run variability.
```

The real test date is Jul-2021. The clock was mistakenly set to 2020 before the benchmark was run.

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL325 Gen10 Plus v2  
(2.00 GHz, AMD EPYC 7713P)

SPECspeed®2017\_int\_base = 12.4

SPECspeed®2017\_int\_peak = 12.4

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jul-2021  
**Hardware Availability:** Jun-2021  
**Software Availability:** Mar-2021

## Operating System Notes (Continued)

To enable Transparent Hugepages (THP) for all allocations,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.  
To enable THP only on request for peak runs of 628.pop2\_s, and 638.imagick\_s,  
'echo madvise > /sys/kernel/mm/transparent\_hugepage/enabled' run as root.  
To disable THP for peak runs of 627.cam4\_s, 644.nab\_s, 649.fotonik3d\_s, and 654.roms\_s,  
'echo never > /sys/kernel/mm/transparent\_hugepage/enabled' run as root.

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
GOMP\_CPU\_AFFINITY = "0-63"  
LD\_LIBRARY\_PATH =  
"/cpu2017/amd\_speed\_aocc300\_milan\_B\_lib/64;/cpu2017/amd\_speed\_aocc300\_mi  
lan\_B\_lib/32:"  
MALLOC\_CONF = "retain:true"  
OMP\_DYNAMIC = "false"  
OMP\_SCHEDULE = "static"  
OMP\_STACKSIZE = "128M"  
OMP\_THREAD\_LIMIT = "64"

Environment variables set by runcpu during the 600.perlbench\_s peak run:  
GOMP\_CPU\_AFFINITY = "0"

Environment variables set by runcpu during the 620.omnetpp\_s peak run:  
GOMP\_CPU\_AFFINITY = "0"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 512GiB Memory using OpenSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)  
jemalloc 5.1.0 is available here:  
<https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2>

Submitted\_by: "Bucek, James" <james.bucek@hpe.com>  
Submitted: Wed Jul 14 18:08:06 EDT 2021

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL325 Gen10 Plus v2  
(2.00 GHz, AMD EPYC 7713P)

SPECspeed®2017\_int\_base = 12.4

SPECspeed®2017\_int\_peak = 12.4

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jul-2021  
**Hardware Availability:** Jun-2021  
**Software Availability:** Mar-2021

## General Notes (Continued)

Submission: cpu2017-20210714-28138.sub

## Platform Notes

The system ROM used for this result contains AMD microcode version 0xa00111d for the AMD EPYC 7713P processor.

BIOS Configuration

Workload Profile set to General Throughput Compute  
AMD SMT Option set to Disabled  
Determinism Control set to Manual  
Performance Determinism set to Power Deterministic  
Last-Level Cache (LLC) as NUMA Node set to Enabled  
Memory PStates set to Disabled  
Data Fabric C-State Enable set to Force Enabled  
Thermal Configuration set to Maximum Cooling  
Workload Profile set to Custom  
Infinity Fabric Power Management set to Disabled  
Infinity Fabric Performance State set to P0  
L1 HW Prefetcher set to Disabled

Sysinfo program /cpu2017/bin/sysinfo  
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d  
running on dl325gen10plus Wed Apr 1 16:10:19 2020

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : AMD EPYC 7713P 64-Core Processor
 1 "physical id"s (chips)
 64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 64
siblings : 64
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55 56 57 58 59 60 61 62 63
```

From lscpu from util-linux 2.34:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 48 bits physical, 48 bits virtual
CPU(s): 64
```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL325 Gen10 Plus v2**  
(2.00 GHz, AMD EPYC 7713P)

SPECspeed®2017\_int\_base = 12.4

SPECspeed®2017\_int\_peak = 12.4

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jul-2021  
**Hardware Availability:** Jun-2021  
**Software Availability:** Mar-2021

## Platform Notes (Continued)

```

On-line CPU(s) list:          0-63
Thread(s) per core:          1
Core(s) per socket:          64
Socket(s):                    1
NUMA node(s):                8
Vendor ID:                    AuthenticAMD
CPU family:                   25
Model:                        1
Model name:                   AMD EPYC 7713P 64-Core Processor
Stepping:                     1
Frequency boost:              enabled
CPU MHz:                      1800.119
CPU max MHz:                  2000.0000
CPU min MHz:                  1500.0000
BogoMIPS:                     3992.31
Virtualization:               AMD-V
L1d cache:                    2 MiB
L1i cache:                    2 MiB
L2 cache:                     32 MiB
L3 cache:                     256 MiB
NUMA node0 CPU(s):           0-7
NUMA node1 CPU(s):           8-15
NUMA node2 CPU(s):           16-23
NUMA node3 CPU(s):           24-31
NUMA node4 CPU(s):           32-39
NUMA node5 CPU(s):           40-47
NUMA node6 CPU(s):           48-55
NUMA node7 CPU(s):           56-63
Vulnerability Itlb multihit:  Not affected
Vulnerability L1tf:          Not affected
Vulnerability Mds:           Not affected
Vulnerability Meltdown:      Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:     Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:     Mitigation; Full AMD retpoline, IBPB conditional, IBRS_FW, STIBP disabled, RSB filling
Vulnerability Srbds:         Not affected
Vulnerability Tsx async abort: Not affected
Flags:                        fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 invpcid_single hw_pstate ssbd mba ibrs

```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL325 Gen10 Plus v2**  
(2.00 GHz, AMD EPYC 7713P)

SPECspeed®2017\_int\_base = 12.4

SPECspeed®2017\_int\_peak = 12.4

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jul-2021  
**Hardware Availability:** Jun-2021  
**Software Availability:** Mar-2021

## Platform Notes (Continued)

ibpb stibp vmcall fsgsbase bml avx2 smep bmi2 invpcid cqm rdt\_a rdseed adx smap  
clflushopt clwb sha\_ni xsaveopt xsavec xgetbv1 xsaves cqm\_llc cqm\_occup\_llc  
cqm\_mbm\_total cqm\_mbm\_local clzero irperf xsaveerptr wbnoinvd arat npt lbrv svm\_lock  
nrip\_save tsc\_scale vmcb\_clean flushbyasid decodeassists pausefilter pfthreshold  
v\_umsave\_vmload vgif umip pku ospke vaes vpcmulqdq rdpid overflow\_recov succor smca

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL
L1d	32K	2M	8	Data	1
L1i	32K	2M	8	Instruction	1
L2	512K	32M	8	Unified	2
L3	32M	256M	16	Unified	3

/proc/cpuinfo cache data  
cache size : 512 KB

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0 1 2 3 4 5 6 7
node 0 size: 128775 MB
node 0 free: 128582 MB
node 1 cpus: 8 9 10 11 12 13 14 15
node 1 size: 129022 MB
node 1 free: 128829 MB
node 2 cpus: 16 17 18 19 20 21 22 23
node 2 size: 129022 MB
node 2 free: 128906 MB
node 3 cpus: 24 25 26 27 28 29 30 31
node 3 size: 129022 MB
node 3 free: 128899 MB
node 4 cpus: 32 33 34 35 36 37 38 39
node 4 size: 129022 MB
node 4 free: 128751 MB
node 5 cpus: 40 41 42 43 44 45 46 47
node 5 size: 129022 MB
node 5 free: 128652 MB
node 6 cpus: 48 49 50 51 52 53 54 55
node 6 size: 128997 MB
node 6 free: 128749 MB
node 7 cpus: 56 57 58 59 60 61 62 63
node 7 size: 116908 MB
node 7 free: 116764 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10 11 11 11 11 11 11 11
1:  11 10 11 11 11 11 11 11

```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL325 Gen10 Plus v2**  
(2.00 GHz, AMD EPYC 7713P)

SPECspeed®2017\_int\_base = 12.4

SPECspeed®2017\_int\_peak = 12.4

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jul-2021  
**Hardware Availability:** Jun-2021  
**Software Availability:** Mar-2021

## Platform Notes (Continued)

2:	11	11	10	11	11	11	11	11
3:	11	11	11	10	11	11	11	11
4:	11	11	11	11	10	11	11	11
5:	11	11	11	11	11	10	11	11
6:	11	11	11	11	11	11	10	11
7:	11	11	11	11	11	11	11	10

From /proc/meminfo

MemTotal: 1044267296 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/sys/devices/system/cpu/cpu\*/cpufreq/scaling\_governor has performance

/usr/bin/lsb\_release -d  
Ubuntu 20.04.1 LTS

From /etc/\*release\* /etc/\*version\*

debian\_version: bullseye/sid  
os-release:  
NAME="Ubuntu"  
VERSION="20.04.1 LTS (Focal Fossa)"  
ID=ubuntu  
ID\_LIKE=debian  
PRETTY\_NAME="Ubuntu 20.04.1 LTS"  
VERSION\_ID="20.04"  
HOME\_URL="https://www.ubuntu.com/"  
SUPPORT\_URL="https://help.ubuntu.com/"

uname -a:

Linux dl325gen10plus 5.4.0-54-generic #60-Ubuntu SMP Fri Nov 6 10:37:59 UTC 2020  
x86\_64 x86\_64 x86\_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Full AMD retpoline,

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL325 Gen10 Plus v2**  
(2.00 GHz, AMD EPYC 7713P)

**SPECspeed®2017\_int\_base = 12.4**

**SPECspeed®2017\_int\_peak = 12.4**

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jul-2021  
**Hardware Availability:** Jun-2021  
**Software Availability:** Mar-2021

## Platform Notes (Continued)

IBPB: conditional, IBRS\_FW, STIBP:  
disabled, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected

CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 5 Apr 1 12:23

SPEC is set to: /cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sdb2	ext4	733G	27G	668G	4%	/

From /sys/devices/virtual/dmi/id

Vendor: HPE  
Product: ProLiant DL325 Gen10 Plus  
Product Family: ProLiant  
Serial: CN79290FKQ

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

8x Samsung M386AAG40AM3-CWE 128 GB 4 rank 3200  
8x UNKNOWN NOT AVAILABLE

BIOS:

BIOS Vendor: HPE  
BIOS Version: A43  
BIOS Date: 04/15/2021  
BIOS Revision: 2.42  
Firmware Revision: 2.40

(End of data from sysinfo program)

## Compiler Version Notes

```

=====
C      | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base,
      | peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
=====

```

AMD clang version 12.0.0 (CLANG: AOCC\_3.0.0-Build#78 2020\_12\_10) (based on LLVM Mirror.Version.12.0.0)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin

(Continued on next page)





# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL325 Gen10 Plus v2  
(2.00 GHz, AMD EPYC 7713P)

SPECspeed®2017\_int\_base = 12.4

SPECspeed®2017\_int\_peak = 12.4

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jul-2021  
**Hardware Availability:** Jun-2021  
**Software Availability:** Mar-2021

## Compiler Version Notes (Continued)

=====  
C++ | 620.omnetpp\_s(base, peak) 623.xalancbmk\_s(base, peak)  
| 631.deepsjeng\_s(base, peak) 641.leela\_s(base, peak)  
=====

AMD clang version 12.0.0 (CLANG: AOCC\_3.0.0-Build#78 2020\_12\_10) (based on LLVM Mirror.Version.12.0.0)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin  
=====

=====  
Fortran | 648.exchange2\_s(base, peak)  
=====

AMD clang version 12.0.0 (CLANG: AOCC\_3.0.0-Build#78 2020\_12\_10) (based on LLVM Mirror.Version.12.0.0)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin  
=====

## Base Compiler Invocation

C benchmarks:  
clang

C++ benchmarks:  
clang++

Fortran benchmarks:  
flang

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LINUX\_X64 -DSPEC\_LP64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LINUX -DSPEC\_LP64  
625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL325 Gen10 Plus v2**  
(2.00 GHz, AMD EPYC 7713P)

SPECspeed®2017\_int\_base = 12.4

SPECspeed®2017\_int\_peak = 12.4

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jul-2021  
**Hardware Availability:** Jun-2021  
**Software Availability:** Mar-2021

## Base Portability Flags (Continued)

648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

```
-m64 -mno-adx -mno-sse4a -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -z muldefs
-DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-lflang -lflangrti
```

C++ benchmarks:

```
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -flto -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -extra-vectorizer-passes -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -convert-pow-exp-to-int=false
-z muldefs -mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden -DSPEC_OPENMP
-fopenmp -fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
-lflangrti
```

Fortran benchmarks:

```
-m64 -mno-adx -mno-sse4a -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -Wl,-mllvm -Wl,-enable-iv-split
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -flto -z muldefs
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -DSPEC_OPENMP
```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL325 Gen10 Plus v2  
(2.00 GHz, AMD EPYC 7713P)

SPECspeed®2017\_int\_base = 12.4

SPECspeed®2017\_int\_peak = 12.4

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jul-2021  
**Hardware Availability:** Jun-2021  
**Software Availability:** Mar-2021

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

-fopenmp -fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang  
-lflangrti

## Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

C++ benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

Fortran benchmarks:

-Wno-return-type

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

600.perlbench\_s: -m64 -mno-adx -mno-sse4a -Wl,-allow-multiple-definition  
-Wl,-mllvm -Wl,-enable-licm-vrp  
-Wl,-mllvm -Wl,-function-specialize

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL325 Gen10 Plus v2**  
(2.00 GHz, AMD EPYC 7713P)

SPECspeed®2017\_int\_base = 12.4

SPECspeed®2017\_int\_peak = 12.4

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Jul-2021  
**Hardware Availability:** Jun-2021  
**Software Availability:** Mar-2021

## Peak Optimization Flags (Continued)

600.perlbench\_s (continued):

```
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=5 -mllvm -unroll-threshold=50
-fremap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -fopenmp
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

602.gcc\_s: basepeak = yes

605.mcf\_s: basepeak = yes

625.x264\_s: basepeak = yes

657.xz\_s: basepeak = yes

C++ benchmarks:

```
620.omnetpp_s: -m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -flto
-finline-aggressive -mllvm -unroll-threshold=100
-flv-function-specialization -mllvm -enable-licm-vrp
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lamdlibm
-ljemalloc -lflang
```

623.xalancbmk\_s: basepeak = yes

631.deepsjeng\_s: basepeak = yes

641.leela\_s: basepeak = yes

Fortran benchmarks:

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL325 Gen10 Plus v2  
(2.00 GHz, AMD EPYC 7713P)

SPECspeed®2017\_int\_base = 12.4

SPECspeed®2017\_int\_peak = 12.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jul-2021

Hardware Availability: Jun-2021

Software Availability: Mar-2021

## Peak Optimization Flags (Continued)

648.exchange2\_s: basepeak = yes

## Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

C++ benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

Fortran benchmarks:

-Wno-return-type

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc300-flags-A1.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-V1.2-EPYC-revQ.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc300-flags-A1.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-V1.2-EPYC-revQ.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.8 on 2020-04-01 17:10:18-0400.

Report generated on 2021-08-04 18:39:35 by CPU2017 PDF formatter v6442.

Originally published on 2021-08-03.