



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

## Fujitsu

PRIMERGY RX2540 M6, Intel Xeon Platinum 8380,  
2.30GHz

SPECrate®2017\_fp\_base = 462

SPECrate®2017\_fp\_energy\_base = 633

SPECrate®2017\_fp\_peak = Not Run

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CPU2017 License: 19

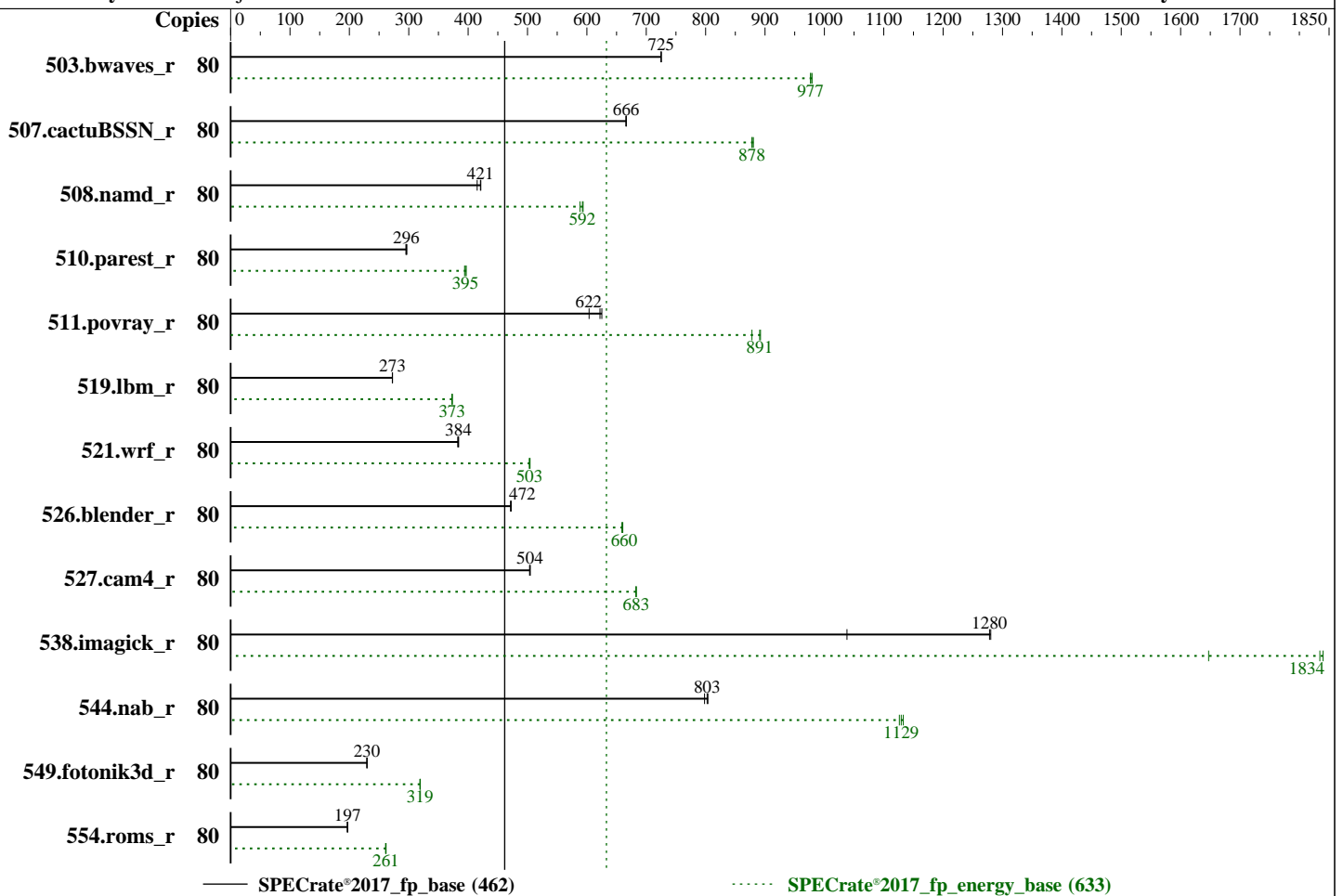
Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Mar-2021

Hardware Availability: May-2021

Software Availability: Feb-2021



### Hardware

CPU Name: Intel Xeon Platinum 8380  
 Max MHz: 3400  
 Nominal: 2300  
 Enabled: 80 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1.25 MB I+D on chip per core  
 L3: 60 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (32 x 32 GB 2Rx4 PC4-3200AA-R)  
 Storage: 1 x SATA M.2 SSD, 480GB  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP2  
 5.3.18-22-default  
 Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++  
 Compiler Build 20201113 for Linux;  
 C/C++: Version 2021.1 of Intel C/C++  
 Compiler Classic Build 20201112 for Linux;  
 Fortran: Version 2021.1 of Intel Fortran  
 Compiler Classic Build 20201112 for Linux  
 Parallel: No  
 Firmware: Fujitsu BIOS Version V1.0.0.0 R1.4.0 for  
 D3891-A1x. Released May-2021  
 tested as V1.0.0.0 R1.2.0 for D3891-A1x Apr-2021  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 (Continued on next page)



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### Software (Continued)

Peak Pointers: Not Applicable  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS set to prefer performance at the cost of additional power usage

### Power

Max. Power (W): 875.4  
 Idle Power (W): 269.07  
 Min. Temperature (C): 24.69  
 Elevation (m): 11  
 Line Standard: 200 V / 50 Hz / 1 phase / 2 wires  
 Provisioning: Line-powered

### Power Settings

Management FW: Version 3.20i for D3891-A1x of Fujitsu BMC Firmware  
 Memory Mode: Normal

### Power-Relevant Hardware

Power Supply: 1 x 1600 W (non-redundant)  
 Details: Standard power supply part of base unit S26113-E630-V50-1  
 Backplane: 24 x 2.5inch HDD back plan  
 Other Storage: Embedded SATA Controller  
 Storage Model #s: S26361-F5706  
 NICs Installed: 1 x Intel I350-T4 @ 1 Gb  
 NICs Enabled (FW/OS): 4 / 4  
 NICs Connected/Speed: 1 @ 1 Gb  
 Other HW Model #s: None

### Power Analyzer

Power Analyzer: 10.26.120.153:8888  
 Hardware Vendor: Hioki  
 Model: Hioki PW3336:1-Channel  
 Serial Number: 170213562  
 Input Connection: USB via USB-Serial CH340  
 Metrology Institute: NICT  
 Calibration By: HIOKI E.E. CORPORATION  
 Calibration Label: H06400088  
 Calibration Date: 25-Jun-2020  
 PTDaemon® Version: 1.9.1 (a2d19f26; 2019-07-17)  
 Setup Description: Connected to PSU 1  
 Current Ranges Used: 5A  
 Voltage Range Used: 300V

### Temperature Meter

Temperature Meter: 10.26.120.153:8889  
 Hardware Vendor: Digi International Inc.  
 Model: DigiWATCHPORT\_H  
 Serial Number: W 613 66209  
 Input Connection: USB  
 PTDaemon Version: 1.9.1 (a2d19f26; 2019-07-17)  
 Setup Description: 5 mm in front of SUT main air intake

## Base Results Table

Benchmark	Copies	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power
503.bwaves_r	80	1107	725	893	979	807	821	1106	725	895	977	809	822	<b>1107</b>	<b>725</b>	<b>895</b>	<b>977</b>	<b>809</b>	<b>822</b>
507.cactuBSSN_r	80	152	666	126	880	831	849	152	666	127	878	834	846	<b>152</b>	<b>666</b>	<b>127</b>	<b>878</b>	<b>834</b>	<b>849</b>
508.namd_r	80	183	415	141	588	769	830	180	421	140	594	774	832	<b>181</b>	<b>421</b>	<b>140</b>	<b>592</b>	<b>774</b>	<b>831</b>
510.parest_r	80	<b>707</b>	<b>296</b>	<b>576</b>	<b>395</b>	<b>815</b>	<b>829</b>	709	295	578	394	815	828	705	297	574	397	814	828

Table continues on next page. Results appear in the order in which they were run. Bold underlined text indicates a median measurement.



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Benchmark	Copies	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power
511.povray_r	80	<b>300</b>	<b>622</b>	<b>227</b>	<b>891</b>	<b>757</b>	<b>815</b>	299	625	227	892	760	821	309	604	231	878	746	822
519.lbm_r	80	<b>309</b>	<b>273</b>	<b>257</b>	<b>373</b>	<b>830</b>	<b>865</b>	310	272	257	373	829	865	309	273	257	373	830	858
521.wrf_r	80	469	382	389	503	830	861	467	384	388	504	831	863	<b>467</b>	<b>384</b>	<b>389</b>	<b>503</b>	<b>833</b>	<b>860</b>
526.blender_r	80	258	472	200	660	775	833	<b>258</b>	<b>472</b>	<b>200</b>	<b>660</b>	<b>775</b>	<b>836</b>	258	472	200	659	775	845
527.cam4_r	80	<b>278</b>	<b>504</b>	<b>223</b>	<b>683</b>	<b>804</b>	<b>869</b>	277	505	223	683	804	859	278	503	223	683	803	875
538.imagick_r	80	192	1040	131	1650	682	859	<b>156</b>	<b>1280</b>	<b>117</b>	<b>1830</b>	<b>755</b>	<b>859</b>	155	1280	117	1840	754	859
544.nab_r	80	167	804	129	1130	769	807	169	798	130	1130	768	820	<b>168</b>	<b>803</b>	<b>129</b>	<b>1130</b>	<b>770</b>	<b>815</b>
549.fotonik3d_r	80	1357	230	1090	319	802	816	<b>1358</b>	<b>230</b>	<b>1090</b>	<b>319</b>	<b>802</b>	<b>816</b>	1358	230	1090	319	801	816
554.roms_r	80	<b>645</b>	<b>197</b>	<b>536</b>	<b>261</b>	<b>831</b>	<b>843</b>	644	197	536	262	831	845	648	196	537	261	830	844

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## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
    "/home/Benchmark/speccpu-1.1.5_fprate/lib/intel64:/home/Benchmark/speccpu-1.1.5_fprate/je5.0.1-64"
MALLOCONF = "retain:true"
```

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7940X CPU + 64GB RAM memory using openSUSE Leap 15.2  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

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Hardware Availability: May-2021

Software Availability: Feb-2021

### General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

### Platform Notes

BIOS configuration:

Hyper Threading = Disabled

Adjacent Cache Line Prefetch = Disabled

DCU Streamer Prefetcher = Disabled

Intel Virtualization Technology = Disabled

Override OS Energy Performance = Enabled

Energy Performance = Performance

CPU ClE Support = Disabled

Patrol Scrub = Enabled

SNC = Enable SNC2

sysinfo program /home/Benchmark/speccpu-1.1.5\_fprate/bin/sysinfo

Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c

running on localhost Thu Apr 8 20:09:49 2021

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Platinum 8380 CPU @ 2.30GHz

2 "physical id"s (chips)

80 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 40

siblings : 40

physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24  
25 26 27 28 29 30 31 32 33 34 35 36 37 38 39

physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24  
25 26 27 28 29 30 31 32 33 34 35 36 37 38 39

From lscpu:

Architecture: x86\_64

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### Platform Notes (Continued)

```

CPU op-mode(s):      32-bit, 64-bit
Byte Order:          Little Endian
Address sizes:       46 bits physical, 57 bits virtual
CPU(s):              80
On-line CPU(s) list: 0-79
Thread(s) per core: 1
Core(s) per socket: 40
Socket(s):           2
NUMA node(s):       4
Vendor ID:           GenuineIntel
CPU family:          6
Model:               106
Model name:          Intel(R) Xeon(R) Platinum 8380 CPU @ 2.30GHz
Stepping:            6
CPU MHz:             799.933
CPU max MHz:         3400.0000
CPU min MHz:         800.0000
BogoMIPS:            4600.00
Virtualization:     VT-x
L1d cache:           48K
L1i cache:           32K
L2 cache:            1280K
L3 cache:            61440K
NUMA node0 CPU(s):  0-19
NUMA node1 CPU(s):  20-39
NUMA node2 CPU(s):  40-59
NUMA node3 CPU(s):  60-79
Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpelt rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx fl16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd
mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad
fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f
avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp
hwp_pkg_req avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d
arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 61440 KB

```

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### Platform Notes (Continued)

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19
node 0 size: 257648 MB
node 0 free: 257309 MB
node 1 cpus: 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39
node 1 size: 258043 MB
node 1 free: 257680 MB
node 2 cpus: 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59
node 2 size: 258009 MB
node 2 free: 257603 MB
node 3 cpus: 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79
node 3 size: 257814 MB
node 3 free: 257511 MB
node distances:
node  0  1  2  3
  0:  10  11  20  20
  1:  11  10  20  20
  2:  20  20  10  11
  3:  20  20  11  10

```

```

From /proc/meminfo
MemTotal:      1056271952 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

/sys/devices/system/cpu/cpu\*/cpufreq/scaling\_governor has powersave

```

From /etc/*release* /etc/*version*
os-release:
NAME="SLES"
VERSION="15-SP2"
VERSION_ID="15.2"
PRETTY_NAME="SUSE Linux Enterprise Server 15 SP2"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15:sp2"

```

```

uname -a:
Linux localhost 5.3.18-22-default #1 SMP Wed Jun 3 12:16:43 UTC 2020 (720aeba) x86_64
x86_64 x86_64 GNU/Linux

```

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### Platform Notes (Continued)

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling):	Not affected
CVE-2019-11135 (TSX Asynchronous Abort):	Not affected

run-level 3 Apr 8 13:36

```
SPEC is set to: /home/Benchmark/speccpu-1.1.5_fprate
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdb3       xfs   376G  149G  228G  40% /home
```

```
From /sys/devices/virtual/dmi/id
Vendor:          FUJITSU
Product:         PRIMERGY RX2540 M6
Product Family: SERVER
Serial:          EWAAxxxxxx
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
Memory:
 32x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200
```

```
BIOS:
 BIOS Vendor:      FUJITSU
 BIOS Version:     V1.0.0.0 R1.2.0 for D3891-A1x
 BIOS Date:        04/01/2021
 BIOS Revision:    1.2
 Firmware Revision: 3.20
```

(End of data from sysinfo program)



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### Power Settings Notes

PTDaemon to measure power and temperature was run on a PRIMERGY RX2530 M5 as a controller with 2x Intel Xeon Platinum 8280 CPU and 768 GB of memory using Windows Server 2012 R2. Power management in the BIOS was default except for any settings mentioned in BIOS Configuration. No power management settings were set in the management firmware. The optional optical drive was not installed. The run was started and observed through the management firmware.

### Compiler Version Notes

=====  
C | 519.lbm\_r(base) 538.imagick\_r(base) 544.nab\_r(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
=====

=====  
C++ | 508.namd\_r(base) 510.parest\_r(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
=====

=====  
C++, C | 511.povray\_r(base) 526.blender\_r(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
=====

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
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Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
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## Compiler Version Notes (Continued)

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====  
Fortran | 503.bwaves\_r(base) 549.fotonik3d\_r(base) 554.roms\_r(base)  
=====

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====  
Fortran, C | 521.wrf\_r(base) 527.cam4\_r(base)  
=====

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icx

Benchmarks using both C and C++:

icpx icx

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## Base Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:  
icpx icx ifort

## Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

C++ benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-mbranches-within-32B-boundaries -ljemalloc
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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## Fujitsu

PRIMERGY RX2540 M6, Intel Xeon Platinum 8380,  
2.30GHz

SPECrate®2017_fp_base =	462
SPECrate®2017_fp_energy_base =	633
SPECrate®2017_fp_peak =	Not Run
SPECrate®2017_fp_energy_peak =	Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Mar-2021

Hardware Availability: May-2021

Software Availability: Feb-2021

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-align array32byte -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-align array32byte -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-ICL-RevA.html>

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html)

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-ICL-RevA.xml>

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml)

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