SPEC CPU®2017 Integer Rate Result

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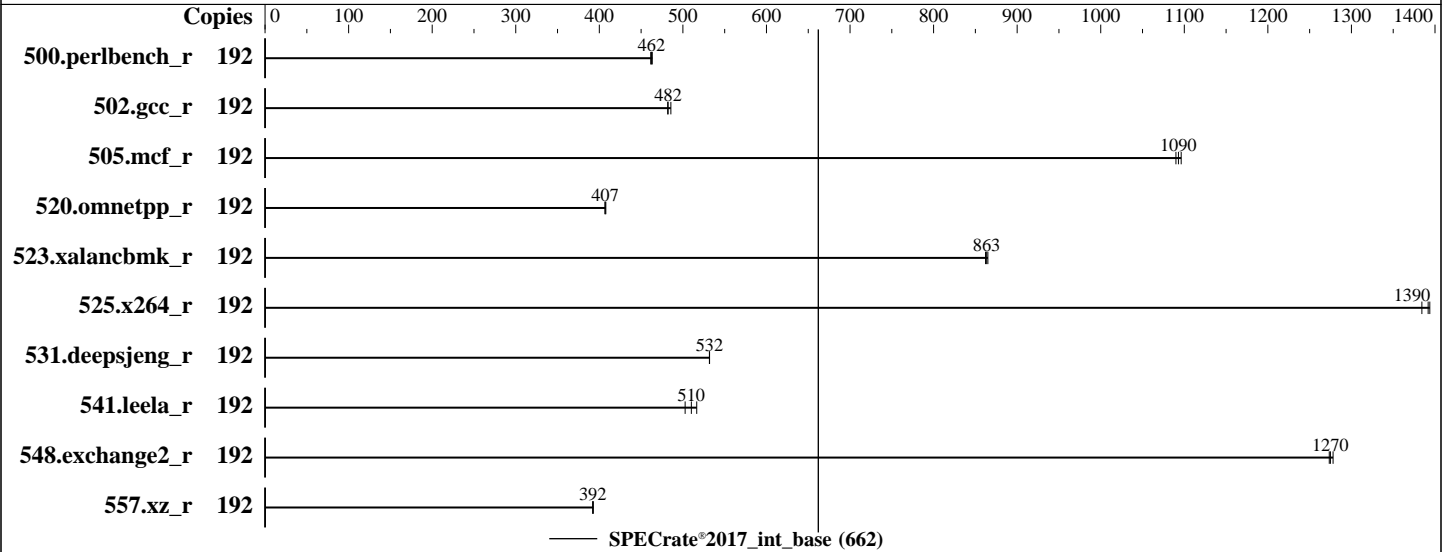## Fujitsu

PRIMERGY RX4770 M6, Intel Xeon Platinum 8360H, 3.00GHz

$SPECrate^{®}2017\_int\_base =$ 662

$SPECrate^{®}2017\_int\_peak =$ Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Nov-2020

Hardware Availability: Nov-2020

Software Availability: Jul-2020

Benchmark	Copies	Result
500.perlbench_r	192	462
502.gcc_r	192	482
505.mcf_r	192	1090
520.omnetpp_r	192	407
523.xalancbmk_r	192	863
525.x264_r	192	1390
531.deepsjeng_r	192	532
541.leela_r	192	510
548.exchange2_r	192	1270
557.xz_r	192	392

—— SPECrate®2017_int_base (662)

Hardware

CPU Name: Intel Xeon Platinum 8360H
 Max MHz: 4200
 Nominal: 3000
Enabled: 96 cores, 4 chips, 2 threads/core
Orderable: 2,4 chips
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 33 MB I+D on chip per chip
 Other: None
Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-3200AA-R)
Storage: 1 x SATA M.2 SSD, 480GB
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP2
5.3.18-22-default
Compiler: C/C++: Version 19.1.1.217 of Intel C/C++ Compiler for Linux;
Fortran: Version 19.1.1.217 of Intel Fortran Compiler for Linux
Parallel: No
Firmware: Fujitsu BIOS Version V1.0.0.0 R1.8.0 for D3892-A1x. Released Jan-2021 tested as V1.0.0.0 R1.1.1 for D3892-A1x Sep-2020
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: binutils-gold 2.32-7.11.4
Power Management: BIOS set to prefer performance at the cost of additional power usage

Standard Performance Evaluation Corporation (info@spec.org)

https://www.spec.org/

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Results Table

Benchmark	Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
500.perlbench_r	192	**661**	**462**	660	463	662	461							
502.gcc_r	192	560	486	564	482	**564**	**482**							
505.mcf_r	192	**284**	**1090**	285	1090	283	1100							
520.omnetpp_r	192	619	407	618	408	**619**	**407**							
523.xalancbmk_r	192	**235**	**863**	234	865	235	862							
525.x264_r	192	241	1390	243	1380	**242**	**1390**							
531.deepsjeng_r	192	414	532	**414**	**532**	414	532							
541.leela_r	192	615	517	633	503	**623**	**510**							
548.exchange2_r	192	395	1270	**395**	**1270**	394	1280							
557.xz_r	192	529	392	528	393	**529**	**392**							
SPECrate®2017_int_base =		**662**												
SPECrate®2017_int_peak =		**Not Run**												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler.
The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux
The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux
```

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Kernel Boot Parameter set with : nohz_full=1-191
echo 15000000 > /proc/sys/kernel/sched_min_granularity_ns
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
    "/home/Benchmark/speccpu-ic19_1u1/lib/intel64:/home/Benchmark/speccpu-ic
    19_1u1/lib/ia32:/home/Benchmark/speccpu-ic19_1u1/je5.0.1-32"
MALLOC_CONF = "retain:true"
```

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General Notes

Binaries compiled on a system with 4x Intel Xeon Platinum 8360H CPU + 1536GB RAM
memory using SUSE Linux Enterprise Server 15 SP2
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>   /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
binutils-gold, an ELF linker
package available from https://software.opensuse.org/package/binutils-gold as follows:
- Click "Show bunutils-gold for other distributions"
- Click "Show community packages" of SUSE SLE-15-SP2
- Click "Expert Download" selecting package corresponding to 2.32 release identified by "home:gabrielftg:ulp-preview"
- Click "Grab binary packages directly"
- Click "binutils-gold-xxxx.rpm"
package was installed with rpm command i.e.:
rpm -i --nodeps bunutils-gold-xxxx.rpm

Platform Notes

BIOS configuration:
DCU Streamer Prefetcher = Disabled
Intel Virtualization Technology = Disabled
Utilization Profile = Unbalanced
Stale AtoS = Disabled
LLC Deadline Alloc = Disabled
XPT Prefetch = Enabled
Patrol Scrub = Enabled
SNC = Enabled
FAN Control = Full

Sysinfo program /home/Benchmark/speccpu-ic19_1u1/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011
running on localhost Fri Nov 6 02:05:13 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

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CPU2017 License: 19		**Test Date:**	Nov-2020
Test Sponsor:	Fujitsu	**Hardware Availability:**	Nov-2020
Tested by:	Fujitsu	**Software Availability:**	Jul-2020

Platform Notes (Continued)

```
    model name : Intel(R) Xeon(R) Platinum 8360H CPU @ 3.00GHz
       4  "physical id"s (chips)
       192 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 24
       siblings  : 48
       physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
       physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
       physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
       physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

  From lscpu:
       Architecture:        x86_64
       CPU op-mode(s):      32-bit, 64-bit
       Byte Order:          Little Endian
       Address sizes:       46 bits physical, 48 bits virtual
       CPU(s):              192
       On-line CPU(s) list: 0-191
       Thread(s) per core:  2
       Core(s) per socket:  24
       Socket(s):           4
       NUMA node(s):        8
       Vendor ID:           GenuineIntel
       CPU family:          6
       Model:               85
       Model name:          Intel(R) Xeon(R) Platinum 8360H CPU @ 3.00GHz
       Stepping:            11
       CPU MHz:             1201.057
       CPU max MHz:         4200.0000
       CPU min MHz:         1200.0000
       BogoMIPS:            6000.00
       Virtualization:      VT-x
       L1d cache:           32K
       L1i cache:           32K
       L2 cache:            1024K
       L3 cache:            33792K
       NUMA node0 CPU(s):   0-2,6-8,12-14,18-20,96-98,102-104,108-110,114-116
       NUMA node1 CPU(s):   3-5,9-11,15-17,21-23,99-101,105-107,111-113,117-119
       NUMA node2 CPU(s):   24-26,30-32,36-38,42-44,120-122,126-128,132-134,138-140
       NUMA node3 CPU(s):   27-29,33-35,39-41,45-47,123-125,129-131,135-137,141-143
       NUMA node4 CPU(s):   48-50,54-56,60-62,66-68,144-146,150-152,156-158,162-164
       NUMA node5 CPU(s):   51-53,57-59,63-65,69-71,147-149,153-155,159-161,165-167
       NUMA node6 CPU(s):   72-74,78-80,84-86,90-92,168-170,174-176,180-182,186-188
       NUMA node7 CPU(s):   75-77,81-83,87-89,93-95,171-173,177-179,183-185,189-191
       Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
       pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
```

(Continued on next page)

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Platform Notes (Continued)

```
     lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
     aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
     xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
     avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
     invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
     flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms
     invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt
     avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc
     cqm_mbm_total cqm_mbm_local avx512_bf16 dtherm ida arat pln pts hwp hwp_act_window
     hwp_epp hwp_pkg_req pku ospke avx512_vnni md_clear flush_l1d arch_capabilities

  /proc/cpuinfo cache data
     cache size : 33792 KB

  From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
     available: 8 nodes (0-7)
     node 0 cpus: 0 1 2 6 7 8 12 13 14 18 19 20 96 97 98 102 103 104 108 109 110 114 115 116
     node 0 size: 192127 MB
     node 0 free: 191753 MB
     node 1 cpus: 3 4 5 9 10 11 15 16 17 21 22 23 99 100 101 105 106 107 111 112 113 117 118
     119
     node 1 size: 193530 MB
     node 1 free: 193318 MB
     node 2 cpus: 24 25 26 30 31 32 36 37 38 42 43 44 120 121 122 126 127 128 132 133 134
     138 139 140
     node 2 size: 193530 MB
     node 2 free: 193345 MB
     node 3 cpus: 27 28 29 33 34 35 39 40 41 45 46 47 123 124 125 129 130 131 135 136 137
     141 142 143
     node 3 size: 193496 MB
     node 3 free: 193201 MB
     node 4 cpus: 48 49 50 54 55 56 60 61 62 66 67 68 144 145 146 150 151 152 156 157 158
     162 163 164
     node 4 size: 193530 MB
     node 4 free: 193344 MB
     node 5 cpus: 51 52 53 57 58 59 63 64 65 69 70 71 147 148 149 153 154 155 159 160 161
     165 166 167
     node 5 size: 193530 MB
     node 5 free: 193319 MB
     node 6 cpus: 72 73 74 78 79 80 84 85 86 90 91 92 168 169 170 174 175 176 180 181 182
     186 187 188
     node 6 size: 193530 MB
     node 6 free: 193327 MB
     node 7 cpus: 75 76 77 81 82 83 87 88 89 93 94 95 171 172 173 177 178 179 183 184 185
     189 190 191
     node 7 size: 193303 MB
```

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Platform Notes (Continued)

```
   node 7 free: 193120 MB
   node distances:
   node   0   1   2   3   4   5   6   7
     0:  10  11  20  20  20  20  20  20
     1:  11  10  20  20  20  20  20  20
     2:  20  20  10  11  20  20  20  20
     3:  20  20  11  10  20  20  20  20
     4:  20  20  20  20  10  11  20  20
     5:  20  20  20  20  11  10  20  20
     6:  20  20  20  20  20  20  10  11
     7:  20  20  20  20  20  20  11  10

 From /proc/meminfo
    MemTotal:       1583696956 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 From /etc/*release* /etc/*version*
    os-release:
       NAME="SLES"
       VERSION="15-SP2"
       VERSION_ID="15.2"
       PRETTY_NAME="SUSE Linux Enterprise Server 15 SP2"
       ID="sles"
       ID_LIKE="suse"
       ANSI_COLOR="0;32"
       CPE_NAME="cpe:/o:suse:sles:15:sp2"

 uname -a:
    Linux localhost 5.3.18-22-default #1 SMP Wed Jun 3 12:16:43 UTC 2020 (720aeba) x86_64
    x86_64 x86_64 GNU/Linux

 Kernel self-reported vulnerability status:

 itlb_multihit:                         Not affected
 CVE-2018-3620 (L1 Terminal Fault):     Not affected
 Microarchitectural Data Sampling:      Not affected
 CVE-2017-5754 (Meltdown):              Not affected
 CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
                                        via prctl and seccomp
 CVE-2017-5753 (Spectre variant 1):     Mitigation: usercopy/swapgs barriers and __user
                                        pointer sanitization
 CVE-2017-5715 (Spectre variant 2):     Mitigation: Enhanced IBRS, IBPB: conditional,
                                        RSB filling
 srbds:                                 Not affected
 tsx_async_abort:                       Not affected
```

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Platform Notes (Continued)

```
 run-level 3 Nov 6 01:22

 SPEC is set to: /home/Benchmark/speccpu-ic19_1u1
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/sdb3       xfs   376G   92G  284G  25% /home

 From /sys/devices/virtual/dmi/id
    BIOS:    FUJITSU V1.0.0.0 R1.1.1 for D3892-A1x          09/25/2020
    Vendor:  FUJITSU
    Product: PRIMERGY RX4770 M6
    Product Family: SERVER
    Serial:  MABTxxxxxx

 Additional information from dmidecode follows.  WARNING: Use caution when you interpret
 this section. The 'dmidecode' program reads system data which is "intended to allow
 hardware to be accurately determined", but the intent may not be met, as there are
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   Memory:
    48x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200

 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C      | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base)
       | 525.x264_r(base) 557.xz_r(base)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
  NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C++    | 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
       | 541.leela_r(base)
------------------------------------------------------------------------------
Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
  NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------


==============================================================================
Fortran | 548.exchange2_r(base)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
```

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Compiler Version Notes (Continued)

```
  64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Fortran benchmarks:
```
ifort
```

Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:
```
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-fuse-ld=gold -qopt-mem-layout-trans=4
-L/opt/intel/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:
```
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse
```

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Base Optimization Flags (Continued)

C++ benchmarks (continued):
```
-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4
-L/opt/intel/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:
```
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries
-L/opt/intel/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-CPL-RevA.html
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-CPL-RevA.xml
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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