



# SPEC CPU®2017 Floating Point Rate Result

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## Supermicro

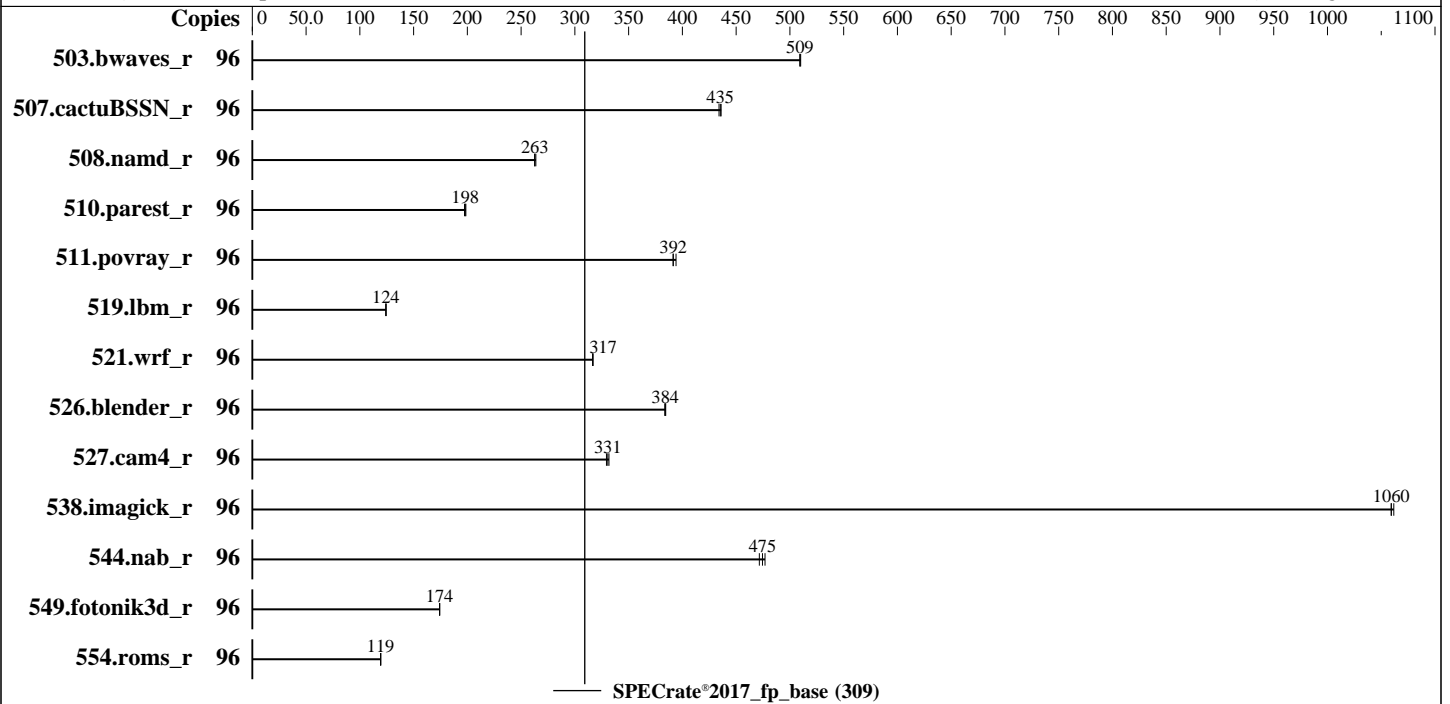
A+ Server 2124BT-HTR  
(H12DST-B , AMD EPYC 7402)

SPECrate®2017\_fp\_base = 309

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 001176  
Test Sponsor: Supermicro  
Tested by: Supermicro

Test Date: Oct-2020  
Hardware Availability: Aug-2019  
Software Availability: Aug-2019



### Hardware

CPU Name: AMD EPYC 7402  
Max MHz: 3350  
Nominal: 2800  
Enabled: 48 cores, 2 chips, 2 threads/core  
Orderable: 1,2 chips  
Cache L1: 32 KB I + 32 KB D on chip per core  
L2: 512 KB I+D on chip per core  
L3: 128 MB I+D on chip per chip, 16 MB shared / 3 cores  
Other: None  
Memory: 256 GB (16 x 16 GB 1Rx4 PC4-3200AA-R)  
Storage: 1 x 200 Gb SATA III SSD  
Other: None

### Software

OS: Ubuntu 19.04  
kernel 5.0.0-25-generic  
Compiler: C/C++/Fortran: Version 2.0.0 of AOCC  
Parallel: No  
Firmware: Version 1.2 released Aug-2020  
File System: ext4  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: Not Applicable  
Other: jemalloc: jemalloc memory allocator library v5.2.0  
Power Management: BIOS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	96	1888	510	<b>1890</b>	<b>509</b>	1890	509							
507.cactuBSSN_r	96	<b>279</b>	<b>435</b>	279	436	280	434							
508.namd_r	96	346	264	<b>347</b>	<b>263</b>	347	263							
510.parest_r	96	<b>1268</b>	<b>198</b>	1272	197	1265	199							
511.povray_r	96	569	394	<b>572</b>	<b>392</b>	573	391							
519.lbm_r	96	814	124	815	124	<b>814</b>	<b>124</b>							
521.wrf_r	96	<b>679</b>	<b>317</b>	679	317	679	317							
526.blender_r	96	380	384	<b>381</b>	<b>384</b>	381	384							
527.cam4_r	96	506	332	<b>508</b>	<b>331</b>	510	329							
538.imagick_r	96	225	1060	<b>225</b>	<b>1060</b>	225	1060							
544.nab_r	96	<b>340</b>	<b>475</b>	339	477	343	472							
549.fotonik3d_r	96	<b>2147</b>	<b>174</b>	2149	174	2147	174							
554.roms_r	96	1276	120	<b>1279</b>	<b>119</b>	1279	119							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

Set dirty\_ratio=8 to limit dirty cache to 8% of memory  
Set swappiness=1 to swap only if necessary  
Set zone\_reclaim\_mode=1 to free local node memory and avoid remote memory  
sync then drop\_caches=3 to reset caches before invoking runcpu

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## Operating System Notes (Continued)

dirty\_ratio, swappiness, zone\_reclaim\_mode and drop\_caches were all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH =  
"/home/cpu2017/amd\_rate\_aocc200\_rome\_C\_lib/64;/home/cpu2017/amd\_rate\_aoc  
c200\_rome\_C\_lib/32:"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flto  
jemalloc 5.2.0 is available here:  
<https://github.com/jemalloc/jemalloc/releases/download/5.2.0/jemalloc-5.2.0.tar.bz2>

## Platform Notes

BIOS Settings:  
Determinism Control = Manual  
Determinism Slider = Power  
cTDP Control = Manual  
cTDP = 200  
Package Power Limit Control = Manual  
Package Power Limit = 200  
APBDIS = 1  
NUMA Nodes Per Socket = NPS4

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011  
running on h12dst Wed Oct 14 20:39:45 2020

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### Platform Notes (Continued)

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : AMD EPYC 7402 24-Core Processor
 2 "physical id"s (chips)
 96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
physical 0: cores 0 1 2 4 5 6 8 9 10 12 13 14 16 17 18 20 21 22 24 25 26 28 29 30
physical 1: cores 0 1 2 4 5 6 8 9 10 12 13 14 16 17 18 20 21 22 24 25 26 28 29 30
```

```
From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 43 bits physical, 48 bits virtual
CPU(s): 96
On-line CPU(s) list: 0-95
Thread(s) per core: 2
Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 8
Vendor ID: AuthenticAMD
CPU family: 23
Model: 49
Model name: AMD EPYC 7402 24-Core Processor
Stepping: 0
CPU MHz: 1799.873
CPU max MHz: 2800.0000
CPU min MHz: 1500.0000
BogoMIPS: 5600.02
Virtualization: AMD-V
L1d cache: 32K
L1i cache: 32K
L2 cache: 512K
L3 cache: 16384K
NUMA node0 CPU(s): 0-5,48-53
NUMA node1 CPU(s): 6-11,54-59
NUMA node2 CPU(s): 12-17,60-65
NUMA node3 CPU(s): 18-23,66-71
NUMA node4 CPU(s): 24-29,72-77
NUMA node5 CPU(s): 30-35,78-83
```

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### Platform Notes (Continued)

```

NUMA node6 CPU(s): 36-41,84-89
NUMA node7 CPU(s): 42-47,90-95
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl xtopology nonstop_tsc cpuid extd_apicid aperfmperf pni
pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx f16c
rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
cat_l3 cdp_l3 hw_pstate sme ssbd mba sev ibpb stibp vmmcall fsgsbase bml avx2
smep bmi2 cqm rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1
xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr
wbnoinvd arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid
decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif umip rdpid
overflow_recov succor smca

```

```

/proc/cpuinfo cache data
cache size : 512 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0 1 2 3 4 5 48 49 50 51 52 53
node 0 size: 32131 MB
node 0 free: 31744 MB
node 1 cpus: 6 7 8 9 10 11 54 55 56 57 58 59
node 1 size: 32252 MB
node 1 free: 31943 MB
node 2 cpus: 12 13 14 15 16 17 60 61 62 63 64 65
node 2 size: 32252 MB
node 2 free: 31723 MB
node 3 cpus: 18 19 20 21 22 23 66 67 68 69 70 71
node 3 size: 32240 MB
node 3 free: 31940 MB
node 4 cpus: 24 25 26 27 28 29 72 73 74 75 76 77
node 4 size: 32252 MB
node 4 free: 31788 MB
node 5 cpus: 30 31 32 33 34 35 78 79 80 81 82 83
node 5 size: 32228 MB
node 5 free: 31918 MB
node 6 cpus: 36 37 38 39 40 41 84 85 86 87 88 89
node 6 size: 32252 MB
node 6 free: 31941 MB
node 7 cpus: 42 43 44 45 46 47 90 91 92 93 94 95
node 7 size: 32251 MB
node 7 free: 31938 MB
node distances:
node 0 1 2 3 4 5 6 7

```

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### Platform Notes (Continued)

0:	10	12	12	12	32	32	32	32
1:	12	10	12	12	32	32	32	32
2:	12	12	10	12	32	32	32	32
3:	12	12	12	10	32	32	32	32
4:	32	32	32	32	10	12	12	12
5:	32	32	32	32	12	10	12	12
6:	32	32	32	32	12	12	10	12
7:	32	32	32	32	12	12	12	10

```
From /proc/meminfo
MemTotal:          264051444 kB
HugePages_Total:      0
Hugepagesize:       2048 kB
```

```
/usr/bin/lsb_release -d
Ubuntu 19.04
```

```
From /etc/*release* /etc/*version*
debian_version: buster/sid
os-release:
NAME="Ubuntu"
VERSION="19.04 (Disco Dingo)"
ID=ubuntu
ID_LIKE=debian
PRETTY_NAME="Ubuntu 19.04"
VERSION_ID="19.04"
HOME_URL="https://www.ubuntu.com/"
SUPPORT_URL="https://help.ubuntu.com/"
```

```
uname -a:
Linux h12dst 5.0.0-25-generic #26-Ubuntu SMP Thu Aug 1 12:04:58 UTC 2019 x86_64 x86_64
x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2018-3620 (L1 Terminal Fault):      Not affected
Microarchitectural Data Sampling:      Not affected
CVE-2017-5754 (Meltdown):              Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):      Mitigation: usercopy/swaps barriers and __user
pointer sanitization
CVE-2017-5715 (Spectre variant 2):      Mitigation: Full AMD retpoline, IBPB:
conditional, IBRS_FW, STIBP: conditional, RSB
filling
```

run-level 3 Oct 14 16:42

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### Platform Notes (Continued)

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	ext4	183G	32G	142G	19%	/

From /sys/devices/virtual/dmi/id

BIOS: American Megatrends Inc. 1.2 08/10/2020  
Vendor: Supermicro  
Product: Super Server  
Serial: 0123456789

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x Samsung M393A2K40DB3-CWE 16 kB 1 rank 3200

(End of data from sysinfo program)

### Compiler Version Notes

=====  
C | 519.lbm\_r(base) 538.imagick\_r(base) 544.nab\_r(base)  
=====

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
=====

=====  
C++ | 508.namd\_r(base) 510.parest\_r(base)  
=====

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
=====

=====  
C++, C | 511.povray\_r(base) 526.blender\_r(base)  
=====

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
=====

(Continued on next page)



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### Compiler Version Notes (Continued)

```

AOC2_2_0_0-Build#191) (based on LLVM AOC2.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
AOC2.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOC2_2_0_0-Build#191) (based on LLVM AOC2.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

```

```

=====
C++, C, Fortran | 507.cactuBSSN_r(base)
=====

```

```

AOC2.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOC2_2_0_0-Build#191) (based on LLVM AOC2.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
AOC2.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOC2_2_0_0-Build#191) (based on LLVM AOC2.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

```

```

=====
Fortran | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
=====

```

```

AOC2.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOC2_2_0_0-Build#191) (based on LLVM AOC2.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

```

```

=====
Fortran, C | 521.wrf_r(base) 527.cam4_r(base)
=====

```

```

AOC2.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
AOC2_2_0_0-Build#191) (based on LLVM AOC2.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu

```

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### Compiler Version Notes (Continued)

Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
-----

### Base Compiler Invocation

C benchmarks:  
clang

C++ benchmarks:  
clang++

Fortran benchmarks:  
flang

Benchmarks using both Fortran and C:  
flang clang

Benchmarks using both C and C++:  
clang++ clang

Benchmarks using Fortran, C, and C++:  
clang++ clang flang

### Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
526.blender\_r: -funsigned-char -D\_\_BOOL\_DEFINED -DSPEC\_LP64  
527.cam4\_r: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64

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## Base Portability Flags (Continued)

554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-freemap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -lmvec -lamdlibm -ljemalloc
-lflang
```

C++ benchmarks:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-partial-unswitch -z muldefs -lmvec -lamdlibm
-ljemalloc -lflang
```

Fortran benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using both Fortran and C:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-freemap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -funroll-loops -Mrecursive -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

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# SPEC CPU®2017 Floating Point Rate Result

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## Supermicro

A+ Server 2124BT-HTR  
(H12DST-B , AMD EPYC 7402)

SPECrate®2017\_fp\_base = 309

SPECrate®2017\_fp\_peak = Not Run

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Oct-2020  
**Hardware Availability:** Aug-2019  
**Software Availability:** Aug-2019

## Base Optimization Flags (Continued)

Benchmarks using both C and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch -z muldefs
-lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch
-funroll-loops -Mrecursive -z muldefs -Kieee -fno-finite-math-only
-lmvec -lamdlibm -ljemalloc -lflang
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc200-flags-C4.html>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Rome-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc200-flags-C4.xml>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Rome-revB.xml>

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Tested with SPEC CPU®2017 v1.1.0 on 2020-10-14 16:39:44-0400.

Report generated on 2020-11-10 15:23:56 by CPU2017 PDF formatter v6255.

Originally published on 2020-11-10.