SPEC CPU®2017 Integer Speed Result

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Lenovo Global Technology
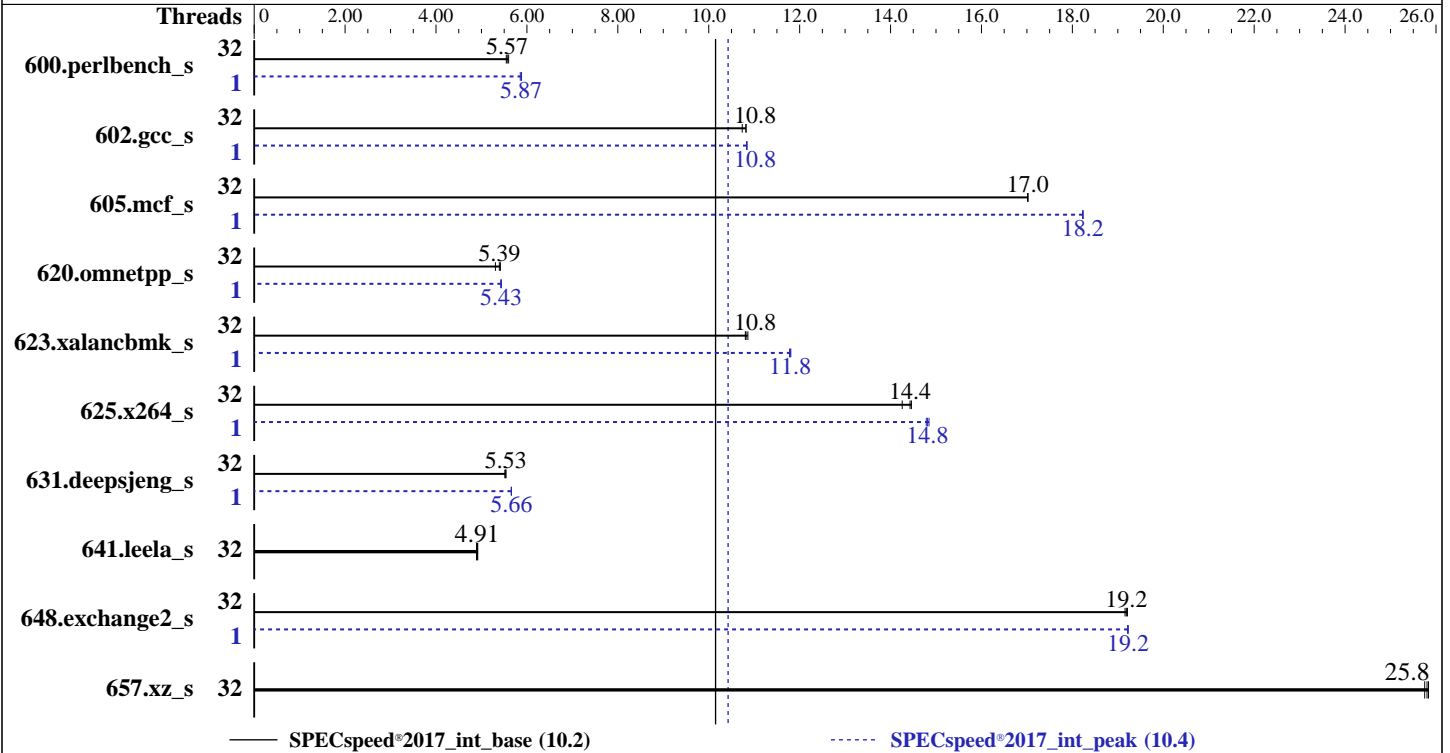
ThinkSystem SR645
3.50 GHz, AMD EPYC 7F52

$SPECspeed^{®}2017\_int\_base = 10.2$

$SPECspeed^{®}2017\_int\_peak = 10.4$

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jul-2020
Hardware Availability: Jun-2020
Software Availability: Nov-2019

Threads

Benchmark	Threads	Result
600.perlbench_s	32	5.57
	1	5.87
602.gcc_s	32	10.8
	1	10.8
605.mcf_s	32	17.0
	1	18.2
620.omnetpp_s	32	5.39
	1	5.43
623.xalancbmk_s	32	10.8
	1	11.8
625.x264_s	32	14.4
	1	14.8
631.deepsjeng_s	32	5.53
	1	5.66
641.leela_s	32	4.91
648.exchange2_s	32	19.2
	1	19.2
657.xz_s	32	25.8

——— SPECspeed®2017_int_base (10.2)   ----- SPECspeed®2017_int_peak (10.4)

Hardware

CPU Name: AMD EPYC 7F52
 Max MHz: 3900
 Nominal: 3500
Enabled: 32 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 512 KB I+D on chip per core
 L3: 256 MB I+D on chip per chip,
  16 MB per core
 Other: None
Memory: 1 TB (32 x 32 GB 2Rx8 PC4-3200AA-R)
Storage: 1 x 960 GB SATA SSD
Other: None

Software

OS: Red Hat Enterprise Linux 8.1
 (Ootpa)
 Kernel 4.18.0-147.el8.x86_64
Compiler: C/C++/Fortran: Version 2.0.0 of AOCC
Parallel: Yes
Firmware: Lenovo BIOS Version D8E105P 1.00 released May-2020
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc: jemalloc memory allocator library v5.1.0
Power Management: BIOS set to prefer performance at the cost of additional power usage

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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	32	317	5.60	320	5.55	**319**	**5.57**	1	303	5.85	302	5.88	**302**	**5.87**
602.gcc_s	32	371	10.7	368	10.8	**368**	**10.8**	1	368	10.8	367	10.8	**367**	**10.8**
605.mcf_s	32	277	17.0	**277**	**17.0**	277	17.0	1	**259**	**18.2**	259	18.2	259	18.2
620.omnetpp_s	32	301	5.42	307	5.31	**302**	**5.39**	1	300	5.44	301	5.43	**300**	**5.43**
623.xalancbmk_s	32	130	10.9	**131**	**10.8**	131	10.8	1	120	11.8	120	11.8	**120**	**11.8**
625.x264_s	32	**122**	**14.4**	122	14.5	124	14.3	1	119	14.8	**119**	**14.8**	119	14.8
631.deepsjeng_s	32	260	5.51	259	5.54	**259**	**5.53**	1	**253**	**5.66**	253	5.66	253	5.66
641.leela_s	32	349	4.89	347	4.92	**348**	**4.91**	32	349	4.89	347	4.92	**348**	**4.91**
648.exchange2_s	32	**153**	**19.2**	153	19.2	153	19.2	1	**153**	**19.2**	153	19.2	153	19.2
657.xz_s	32	239	25.8	**240**	**25.8**	240	25.8	32	239	25.8	**240**	**25.8**	240	25.8
SPECspeed®2017_int_base =	**10.2**													
SPECspeed®2017_int_peak =	**10.4**													

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory
sync then drop_caches=3 to reset caches before invoking runcpu

dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were
all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)
```

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Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
GOMP_CPU_AFFINITY = "0-63"
LD_LIBRARY_PATH =
      "/home/cpu2017-1.1.0-amd-rome-aocc200-C1/amd_speed_aocc200_rome_C_lib/64
      ;/home/cpu2017-1.1.0-amd-rome-aocc200-C1/amd_speed_aocc200_rome_C_lib/32
      :"
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "64"

Environment variables set by runcpu during the 600.perlbench_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 602.gcc_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 605.mcf_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 620.omnetpp_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 623.xalancbmk_s peak run:
GOMP_CPU_AFFINITY = "0"
OMP_STACKSIZE = "128M"

Environment variables set by runcpu during the 625.x264_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 631.deepsjeng_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 648.exchange2_s peak run:
GOMP_CPU_AFFINITY = "0"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
```

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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jul-2020
Hardware Availability: Jun-2020
Software Availability: Nov-2019

General Notes (Continued)

```
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flto
jemalloc 5.1.0 is available here:
https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2
```

Platform Notes

```
 BIOS settings:
 Choose Operating Mode set to Maximum Performance and then set it to Custom Mode
 NUMA nodes per socket set to NPS4

 Sysinfo program /home/cpu2017-1.1.0-amd-rome-aocc200-C1/bin/sysinfo
 Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011
 running on localhost.localdomain Sat Jul  4 17:04:36 2020

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : AMD EPYC 7F52 16-Core Processor
       2  "physical id"s (chips)
       64 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 16
       siblings  : 32
       physical 0: cores 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60
       physical 1: cores 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60

 From lscpu:
      Architecture:        x86_64
      CPU op-mode(s):      32-bit, 64-bit
      Byte Order:          Little Endian
      CPU(s):              64
      On-line CPU(s) list: 0-63
      Thread(s) per core:  2
      Core(s) per socket:  16
      Socket(s):           2
      NUMA node(s):        8
      Vendor ID:           AuthenticAMD
      CPU family:          23
      Model:               49
      Model name:          AMD EPYC 7F52 16-Core Processor
```

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Platform Notes (Continued)

```
      Stepping:              0
      CPU MHz:               1796.493
      CPU max MHz:           3500.0000
      CPU min MHz:           2500.0000
      BogoMIPS:              6987.11
      Virtualization:        AMD-V
      L1d cache:             32K
      L1i cache:             32K
      L2 cache:              512K
      L3 cache:              16384K
      NUMA node0 CPU(s):     0-3,32-35
      NUMA node1 CPU(s):     4-7,36-39
      NUMA node2 CPU(s):     8-11,40-43
      NUMA node3 CPU(s):     12-15,44-47
      NUMA node4 CPU(s):     16-19,48-51
      NUMA node5 CPU(s):     20-23,52-55
      NUMA node6 CPU(s):     24-27,56-59
      NUMA node7 CPU(s):     28-31,60-63
      Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
    pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
    constant_tsc rep_good nopl xtopology nonstop_tsc cpuid extd_apicid aperfmperf pni
    pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx f16c
    rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
    osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
    cat_l3 cdp_l3 hw_pstate sme ssbd mba sev ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2
    smep bmi2 cqm rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1
    xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr
    wbnoinvd arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid
    decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif umip rdpid
    overflow_recov succor smca

 /proc/cpuinfo cache data
    cache size : 512 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
   available: 8 nodes (0-7)
   node 0 cpus: 0 1 2 3 32 33 34 35
   node 0 size: 128801 MB
   node 0 free: 128634 MB
   node 1 cpus: 4 5 6 7 36 37 38 39
   node 1 size: 129020 MB
   node 1 free: 128824 MB
   node 2 cpus: 8 9 10 11 40 41 42 43
   node 2 size: 129020 MB
   node 2 free: 128865 MB
   node 3 cpus: 12 13 14 15 44 45 46 47
```

(Continued on next page)

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Platform Notes (Continued)

```
   node 3 size: 129008 MB
   node 3 free: 128682 MB
   node 4 cpus: 16 17 18 19 48 49 50 51
   node 4 size: 129020 MB
   node 4 free: 128926 MB
   node 5 cpus: 20 21 22 23 52 53 54 55
   node 5 size: 129020 MB
   node 5 free: 128906 MB
   node 6 cpus: 24 25 26 27 56 57 58 59
   node 6 size: 129020 MB
   node 6 free: 128925 MB
   node 7 cpus: 28 29 30 31 60 61 62 63
   node 7 size: 129020 MB
   node 7 free: 128929 MB
   node distances:
   node   0   1   2   3   4   5   6   7
     0:  10  12  12  12  32  32  32  32
     1:  12  10  12  12  32  32  32  32
     2:  12  12  10  12  32  32  32  32
     3:  12  12  12  10  32  32  32  32
     4:  32  32  32  32  10  12  12  12
     5:  32  32  32  32  12  10  12  12
     6:  32  32  32  32  12  12  10  12
     7:  32  32  32  32  12  12  12  10

 From /proc/meminfo
    MemTotal:       1056697428 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 From /etc/*release* /etc/*version*
    os-release:
       NAME="Red Hat Enterprise Linux"
       VERSION="8.1 (Ootpa)"
       ID="rhel"
       ID_LIKE="fedora"
       VERSION_ID="8.1"
       PLATFORM_ID="platform:el8"
       PRETTY_NAME="Red Hat Enterprise Linux 8.1 (Ootpa)"
       ANSI_COLOR="0;31"
    redhat-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
    system-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:8.1:ga

 uname -a:
    Linux localhost.localdomain 4.18.0-147.el8.x86_64 #1 SMP Thu Sep 26 15:52:44 UTC 2019
    x86_64 x86_64 x86_64 GNU/Linux
```

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Platform Notes (Continued)

```
  Kernel self-reported vulnerability status:

  CVE-2018-3620 (L1 Terminal Fault):       Not affected
  Microarchitectural Data Sampling:        Not affected
  CVE-2017-5754 (Meltdown):                Not affected
  CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
                                        via prctl and seccomp
  CVE-2017-5753 (Spectre variant 1):       Mitigation: usercopy/swapgs barriers and __user
                                        pointer sanitization
  CVE-2017-5715 (Spectre variant 2):       Mitigation: Full AMD retpoline, IBPB:
                                        conditional, IBRS_FW, STIBP: conditional, RSB
                                        filling


  run-level 3 Jul 4 17:01

  SPEC is set to: /home/cpu2017-1.1.0-amd-rome-aocc200-C1
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/sda2       xfs   838G   33G  806G   4% /home

  From /sys/devices/virtual/dmi/id
      BIOS:    Lenovo D8E105P-1.00 05/08/2020
      Vendor:  Lenovo
      Product: ThinkSystem SR645 MB
      Product Family: ThinkSystem
      Serial:  1234567890


  Additional information from dmidecode follows.  WARNING: Use caution when you interpret
  this section. The 'dmidecode' program reads system data which is "intended to allow
  hardware to be accurately determined", but the intent may not be met, as there are
  frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
    Memory:
      32x Samsung M393A4G43AB3-CWE 32 kB 2 rank 3200


  (End of data from sysinfo program)
This system support 16 DIMMs per processor, total 32 DIMMs.
32 DIMM slots installed with 32 GB DIMM for this run.
```

Compiler Version Notes

```
==============================================================================
C         | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base,
          | peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
```

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Compiler Version Notes (Continued)

```
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
--------------------------------------------------------------------


==============================================================================
C++     | 623.xalancbmk_s(peak)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
--------------------------------------------------------------------


==============================================================================
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base)
        | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
--------------------------------------------------------------------


==============================================================================
C++     | 623.xalancbmk_s(peak)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
--------------------------------------------------------------------


==============================================================================
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base)
        | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
--------------------------------------------------------------------
```

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SPECspeed®2017_int_base = 10.2

SPECspeed®2017_int_peak = 10.4

Compiler Version Notes (Continued)

```
==============================================================================
Fortran | 648.exchange2_s(base, peak)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Base Portability Flags

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50

(Continued on next page)

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CPU2017 License: 9017		**Test Date:**	Jul-2020
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Base Optimization Flags (Continued)

C benchmarks (continued):
```
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -DSPEC_OPENMP -fopenmp
-DUSE_OPENMP -fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm
-ljemalloc -lflang
```

C++ benchmarks:
```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-partial-unswitch -z muldefs -DSPEC_OPENMP -fopenmp
-DUSE_OPENMP -fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm
-ljemalloc -lflang
```

Fortran benchmarks:
```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -ffast-math
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver2 -funroll-loops
-Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs
-mllvm -disable-indvar-simplify -mllvm -unroll-aggressive
-mllvm -unroll-threshold=150 -DSPEC_OPENMP -fopenmp -DUSE_OPENMP
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc
-lflang
```

Base Other Flags

C benchmarks:
```
-Wno-return-type
```

C++ benchmarks:
```
-Wno-return-type
```

Fortran benchmarks:
```
-Wno-return-type
```

SPEC CPU®2017 Integer Speed Result
Copyright 2017-2020 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645
3.50 GHz, AMD EPYC 7F52

SPECspeed®2017_int_base = 10.2

SPECspeed®2017_int_peak = 10.4

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jul-2020

Hardware Availability: Jun-2020

Software Availability: Nov-2019

Peak Compiler Invocation

C benchmarks:
```
clang
```

C++ benchmarks:
```
clang++
```

Fortran benchmarks:
```
flang
```

Peak Portability Flags

600.perlbench_s: `-DSPEC_LINUX_X64 -DSPEC_LP64`
602.gcc_s: `-DSPEC_LP64`
605.mcf_s: `-DSPEC_LP64`
620.omnetpp_s: `-DSPEC_LP64`
623.xalancbmk_s: `-DSPEC_LINUX -D_FILE_OFFSET_BITS=64`
625.x264_s: `-DSPEC_LP64`
631.deepsjeng_s: `-DSPEC_LP64`
641.leela_s: `-DSPEC_LP64`
648.exchange2_s: `-DSPEC_LP64`
657.xz_s: `-DSPEC_LP64`

Peak Optimization Flags

C benchmarks:

600.perlbench_s: `-flto -Wl,-mllvm -Wl,-function-specialize`
`-Wl,-mllvm -Wl,-region-vectorize`
`-Wl,-mllvm -Wl,-vector-library=LIBMVEC`
`-Wl,-mllvm -Wl,-reduce-array-computations=3`
`-fprofile-instr-generate`(pass 1)
`-fprofile-instr-use`(pass 2) `-Ofast -march=znver2`
`-mno-sse4a -fstruct-layout=5`
`-mllvm -vectorize-memory-aggressively`
`-mllvm -function-specialize -mllvm -enable-gvn-hoist`
`-mllvm -unroll-threshold=50 -fremap-arrays`
`-mllvm -vector-library=LIBMVEC`
`-mllvm -reduce-array-computations=3`
`-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000`
`-flv-function-specialization -DSPEC_OPENMP -fopenmp`
`-DUSE_OPENMP -lmvec -lamdlibm -fopenmp=libomp -lomp`
`-lpthread -ldl -ljemalloc -lflang`

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Peak Optimization Flags (Continued)

602.gcc_s: `-flto -Wl,-mllvm -Wl,-function-specialize`
`-Wl,-mllvm -Wl,-region-vectorize`
`-Wl,-mllvm -Wl,-vector-library=LIBMVEC`
`-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast`
`-march=znver2 -mno-sse4a -fstruct-layout=5`
`-mllvm -vectorize-memory-aggressively`
`-mllvm -function-specialize -mllvm -enable-gvn-hoist`
`-mllvm -unroll-threshold=50 -fremap-arrays`
`-mllvm -vector-library=LIBMVEC`
`-mllvm -reduce-array-computations=3`
`-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000`
`-flv-function-specialization -z muldefs -DSPEC_OPENMP`
`-fopenmp -DUSE_OPENMP -fgnu89-inline -fopenmp=libomp`
`-lomp -lpthread -ldl -ljemalloc`

605.mcf_s: `-flto -Wl,-mllvm -Wl,-function-specialize`
`-Wl,-mllvm -Wl,-region-vectorize`
`-Wl,-mllvm -Wl,-vector-library=LIBMVEC`
`-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast`
`-march=znver2 -mno-sse4a -fstruct-layout=5`
`-mllvm -vectorize-memory-aggressively`
`-mllvm -function-specialize -mllvm -enable-gvn-hoist`
`-mllvm -unroll-threshold=50 -fremap-arrays`
`-mllvm -vector-library=LIBMVEC`
`-mllvm -reduce-array-computations=3`
`-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000`
`-flv-function-specialization -DSPEC_OPENMP -fopenmp`
`-DUSE_OPENMP -lmvec -lamdlibm -fopenmp=libomp -lomp`
`-lpthread -ldl -ljemalloc -lflang`

625.x264_s: `Same as 600.perlbench_s`

657.xz_s: `basepeak = yes`

C++ benchmarks:

620.omnetpp_s: `-flto -Wl,-mllvm -Wl,-function-specialize`
`-Wl,-mllvm -Wl,-region-vectorize`
`-Wl,-mllvm -Wl,-vector-library=LIBMVEC`
`-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast`
`-march=znver2 -flv-function-specialization`
`-mllvm -unroll-threshold=100`
`-mllvm -enable-partial-unswitch`
`-mllvm -loop-unswitch-threshold=200000`

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CPU2017 License: 9017	**Test Date:** Jul-2020
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Peak Optimization Flags (Continued)

620.omnetpp_s (continued):
```
-mllvm -vector-library=LIBMVEC
-mllvm -inline-threshold=1000 -DSPEC_OPENMP -fopenmp
-DUSE_OPENMP -fopenmp=libomp -lomp -lpthread -ldl
-lmvec -lamdlibm -ljemalloc -lflang
```

623.xalancbmk_s:
```
-m32 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -flv-function-specialization
-mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000
-mllvm -vector-library=LIBMVEC
-mllvm -inline-threshold=1000 -DSPEC_OPENMP -fopenmp
-DUSE_OPENMP -fopenmp=libomp -lomp -lpthread -ldl
-ljemalloc
```

631.deepsjeng_s: Same as 620.omnetpp_s

641.leela_s: basepeak = yes

Fortran benchmarks:
```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -ffast-math
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver2 -funroll-loops
-Mrecursive -mllvm -vector-library=LIBMVEC
-mllvm -disable-indvar-simplify -mllvm -unroll-aggressive
-mllvm -unroll-threshold=150 -DSPEC_OPENMP -fopenmp -DUSE_OPENMP
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc
-lflang
```

Peak Other Flags

C benchmarks:
```
-Wno-return-type
```

C++ benchmarks (except as noted below):
```
-Wno-return-type
```

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Peak Other Flags (Continued)

623.xalancbmk_s: `-Wno-return-type`
`-L/sppo/dev/cpu2017/v110/amd_speed_aocc200_rome_C_lib/32`

Fortran benchmarks:
`-Wno-return-type`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/aocc200-flags-B1-1.html`
`http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Rome2P-K.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/aocc200-flags-B1-1.xml`
`http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Rome2P-K.xml`

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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