



# SPEC CPU®2017 Integer Speed Result

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## Dell Inc.

SPECspeed®2017\_int\_base = 9.92

PowerEdge C6525 (AMD EPYC 7F52, 3.50 GHz)

SPECspeed®2017\_int\_peak = 10.2

CPU2017 License: 55

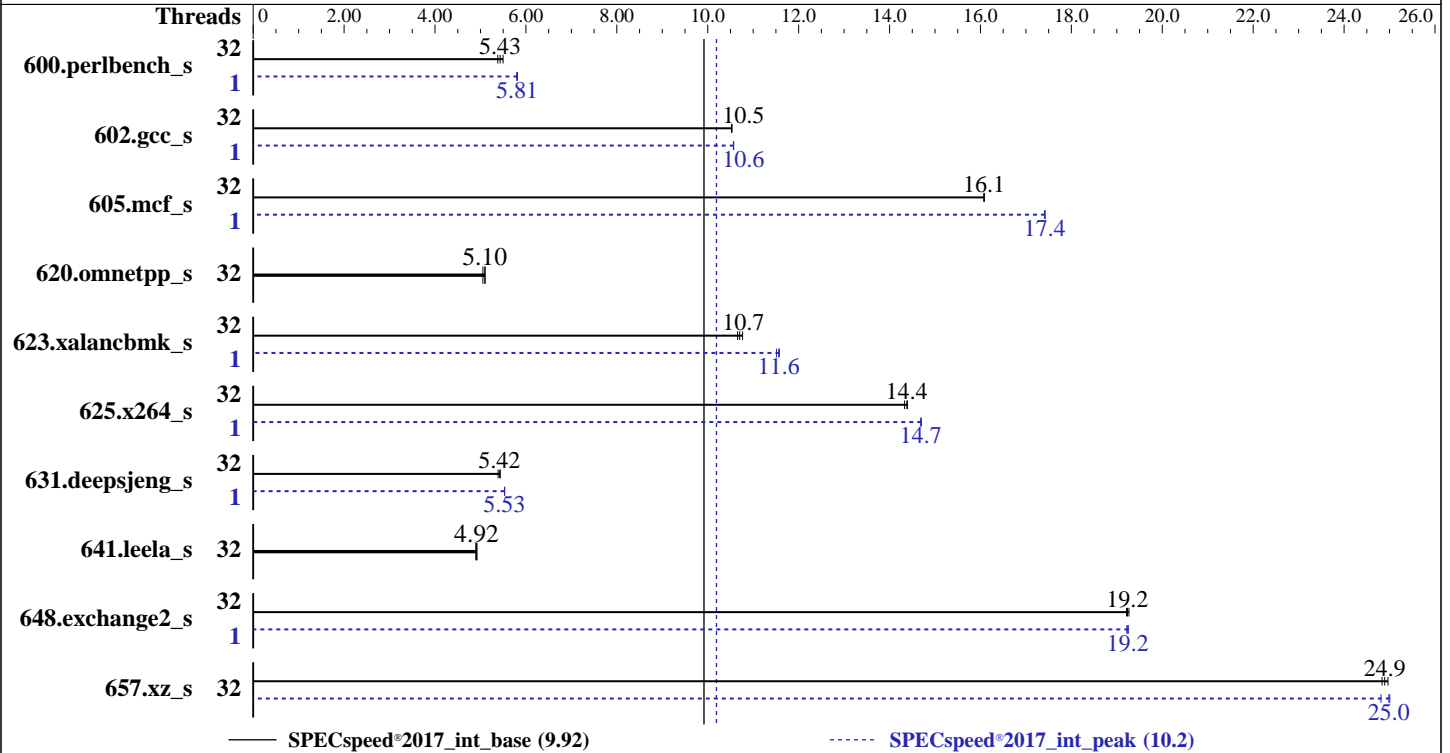
Test Date: Jan-2020

Test Sponsor: Dell Inc.

Hardware Availability: Apr-2020

Tested by: Dell Inc.

Software Availability: Aug-2019



### Hardware

CPU Name: AMD EPYC 7F52  
 Max MHz: 3900  
 Nominal: 3500  
 Enabled: 32 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 512 KB I+D on chip per core  
 L3: 256 MB I+D on chip per chip, 16 MB per core  
 Other: None  
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R, running at 3200)  
 Storage: 1 x 480 GB SATA SSD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP1  
 kernel 4.12.14-197.7-default  
 Compiler: C/C++/Fortran: Version 2.0.0 of AOCC  
 Parallel: Yes  
 Firmware: Version 1.2.5 released Dec-2019  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc: jemalloc memory allocator library v5.2.0  
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	32	<b>327</b>	<b>5.43</b>	323	5.50	330	5.38	1	306	5.80	305	5.81	<b>306</b>	<b>5.81</b>
602.gcc_s	32	378	10.5	<b>378</b>	<b>10.5</b>	378	10.5	1	<b>377</b>	<b>10.6</b>	377	10.6	377	10.6
605.mcf_s	32	<b>294</b>	<b>16.1</b>	294	16.1	294	16.1	1	271	17.4	271	17.4	<b>271</b>	<b>17.4</b>
620.omnetpp_s	32	320	5.10	323	5.05	<b>320</b>	<b>5.10</b>	32	320	5.10	323	5.05	<b>320</b>	<b>5.10</b>
623.xalancbmk_s	32	<b>132</b>	<b>10.7</b>	133	10.7	132	10.8	1	123	11.5	122	11.6	<b>123</b>	<b>11.6</b>
625.x264_s	32	123	14.4	123	14.3	<b>123</b>	<b>14.4</b>	1	120	14.7	120	14.7	<b>120</b>	<b>14.7</b>
631.deepsjeng_s	32	<b>264</b>	<b>5.42</b>	266	5.39	264	5.44	1	259	5.53	259	5.53	<b>259</b>	<b>5.53</b>
641.leela_s	32	347	4.92	348	4.90	<b>347</b>	<b>4.92</b>	32	347	4.92	348	4.90	<b>347</b>	<b>4.92</b>
648.exchange2_s	32	153	19.3	153	19.2	<b>153</b>	<b>19.2</b>	1	153	19.3	153	19.2	<b>153</b>	<b>19.2</b>
657.xz_s	32	<b>248</b>	<b>24.9</b>	249	24.8	248	25.0	32	247	25.0	<b>247</b>	<b>25.0</b>	249	24.8

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

```
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
```

Set dirty\_ratio=8 to limit dirty cache to 8% of memory  
Set swappiness=1 to swap only if necessary  
Set zone\_reclaim\_mode=1 to free local node memory and avoid remote memory  
sync then drop\_caches=3 to reset caches before invoking runcpu

dirty\_ratio, swappiness, zone\_reclaim\_mode and drop\_caches were  
all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)



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## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP\_CPU\_AFFINITY = "0-63"

LD\_LIBRARY\_PATH =

"/root/cpu2017-1.1.0/amd\_speed\_aocc200\_rome\_C\_lib/64;/root/cpu2017-1.1.0/amd\_speed\_aocc200\_rome\_C\_lib/32:"

MALLOC\_CONF = "retain:true"

OMP\_DYNAMIC = "false"

OMP\_SCHEDULE = "static"

OMP\_STACKSIZE = "128M"

OMP\_THREAD\_LIMIT = "64"

Environment variables set by runcpu during the 600.perlbench\_s peak run:

GOMP\_CPU\_AFFINITY = "0"

Environment variables set by runcpu during the 602.gcc\_s peak run:

GOMP\_CPU\_AFFINITY = "0"

Environment variables set by runcpu during the 605.mcf\_s peak run:

GOMP\_CPU\_AFFINITY = "0"

Environment variables set by runcpu during the 623.xalancbmk\_s peak run:

GOMP\_CPU\_AFFINITY = "0"

OMP\_STACKSIZE = "128M"

Environment variables set by runcpu during the 625.x264\_s peak run:

GOMP\_CPU\_AFFINITY = "0"

Environment variables set by runcpu during the 631.deepsjeng\_s peak run:

GOMP\_CPU\_AFFINITY = "0"

Environment variables set by runcpu during the 648.exchange2\_s peak run:

GOMP\_CPU\_AFFINITY = "0"

Environment variables set by runcpu during the 657.xz\_s peak run:

GOMP\_CPU\_AFFINITY = "0-31"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

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## General Notes (Continued)

is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flto  
jemalloc 5.2.0 is available here:  
<https://github.com/jemalloc/jemalloc/releases/download/5.2.0/jemalloc-5.2.0.tar.bz2>

## Platform Notes

BIOS settings:

NUMA Nodes Per Socket set to 4  
CCX as NUMA Domain set to Enabled  
System Profile set to Custom  
CPU Power Management set to Maximum Performance  
Memory Frequency set to Maximum Performance  
Turbo Boost Enabled  
Cstates set to Enabled  
Memory Patrol Scrub Disabled  
Memory Refresh Rate set to 1x  
PCI ASPM L1 Link Power Management Disabled  
Determinism Slider set to Power Determinism  
Efficiency Optimized Mode Disabled  
Memory Interleaving set to Disabled  
Logical Processor Disabled

Sysinfo program /root/cpu2017-1.1.0/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011  
running on suse15-spl Mon Feb 3 13:24:01 2020

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : AMD EPYC 7F52 16-Core Processor
 2 "physical id"s (chips)
32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings  : 16
physical 0: cores 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60
physical 1: cores 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60
```

From lscpu:

Architecture: x86\_64

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## Platform Notes (Continued)

```

CPU op-mode(s):      32-bit, 64-bit
Byte Order:          Little Endian
Address sizes:       43 bits physical, 48 bits virtual
CPU(s):              32
On-line CPU(s) list: 0-31
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s):           2
NUMA node(s):       32
Vendor ID:           AuthenticAMD
CPU family:          23
Model:               49
Model name:          AMD EPYC 7F52 16-Core Processor
Stepping:            0
CPU MHz:             3493.586
BogoMIPS:            6987.17
Virtualization:     AMD-V
L1d cache:           32K
L1i cache:           32K
L2 cache:            512K
L3 cache:            16384K
NUMA node0 CPU(s):  0
NUMA node1 CPU(s):  1
NUMA node2 CPU(s):  2
NUMA node3 CPU(s):  3
NUMA node4 CPU(s):  4
NUMA node5 CPU(s):  5
NUMA node6 CPU(s):  6
NUMA node7 CPU(s):  7
NUMA node8 CPU(s):  8
NUMA node9 CPU(s):  9
NUMA node10 CPU(s): 10
NUMA node11 CPU(s): 11
NUMA node12 CPU(s): 12
NUMA node13 CPU(s): 13
NUMA node14 CPU(s): 14
NUMA node15 CPU(s): 15
NUMA node16 CPU(s): 16
NUMA node17 CPU(s): 17
NUMA node18 CPU(s): 18
NUMA node19 CPU(s): 19
NUMA node20 CPU(s): 20
NUMA node21 CPU(s): 21
NUMA node22 CPU(s): 22
NUMA node23 CPU(s): 23
NUMA node24 CPU(s): 24
NUMA node25 CPU(s): 25

```

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## Platform Notes (Continued)

```

NUMA node26 CPU(s): 26
NUMA node27 CPU(s): 27
NUMA node28 CPU(s): 28
NUMA node29 CPU(s): 29
NUMA node30 CPU(s): 30
NUMA node31 CPU(s): 31

```

```

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf pni pclmulqdq
monitor ssse3 fma cx16 sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand
lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw
ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_l2 mwaitx cpb
cat_l3 cdp_l3 hw_pstate sme ssbd sev ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2 smep
bmi2 cqm rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves
cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr arat npt
lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
pfthreshold avic v_vmsave_vmload vgif umip rdpid overflow_recov succor smca

```

```

/proc/cpuinfo cache data
cache size : 512 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 32 nodes (0-31)
node 0 cpus: 0
node 0 size: 15677 MB
node 0 free: 15614 MB
node 1 cpus: 1
node 1 size: 16127 MB
node 1 free: 16094 MB
node 2 cpus: 2
node 2 size: 16127 MB
node 2 free: 16102 MB
node 3 cpus: 3
node 3 size: 16126 MB
node 3 free: 16102 MB
node 4 cpus: 4
node 4 size: 16127 MB
node 4 free: 16102 MB
node 5 cpus: 5
node 5 size: 16127 MB
node 5 free: 16093 MB
node 6 cpus: 6
node 6 size: 16098 MB
node 6 free: 16075 MB
node 7 cpus: 7
node 7 size: 16126 MB

```

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## Platform Notes (Continued)

```
node 7 free: 16089 MB
node 8 cpus: 8
node 8 size: 16127 MB
node 8 free: 16102 MB
node 9 cpus: 9
node 9 size: 16127 MB
node 9 free: 16098 MB
node 10 cpus: 10
node 10 size: 16127 MB
node 10 free: 16108 MB
node 11 cpus: 11
node 11 size: 16126 MB
node 11 free: 16101 MB
node 12 cpus: 12
node 12 size: 16127 MB
node 12 free: 16068 MB
node 13 cpus: 13
node 13 size: 16127 MB
node 13 free: 16101 MB
node 14 cpus: 14
node 14 size: 16127 MB
node 14 free: 15990 MB
node 15 cpus: 15
node 15 size: 16114 MB
node 15 free: 16016 MB
node 16 cpus: 16
node 16 size: 16127 MB
node 16 free: 16110 MB
node 17 cpus: 17
node 17 size: 16127 MB
node 17 free: 16110 MB
node 18 cpus: 18
node 18 size: 16127 MB
node 18 free: 16104 MB
node 19 cpus: 19
node 19 size: 16126 MB
node 19 free: 16108 MB
node 20 cpus: 20
node 20 size: 16127 MB
node 20 free: 16103 MB
node 21 cpus: 21
node 21 size: 16127 MB
node 21 free: 16111 MB
node 22 cpus: 22
node 22 size: 16127 MB
node 22 free: 16103 MB
node 23 cpus: 23
```

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## Platform Notes (Continued)

```

node 23 size: 16126 MB
node 23 free: 16106 MB
node 24 cpus: 24
node 24 size: 16127 MB
node 24 free: 16113 MB
node 25 cpus: 25
node 25 size: 16127 MB
node 25 free: 16111 MB
node 26 cpus: 26
node 26 size: 16127 MB
node 26 free: 16107 MB
node 27 cpus: 27
node 27 size: 16126 MB
node 27 free: 16110 MB
node 28 cpus: 28
node 28 size: 16127 MB
node 28 free: 16109 MB
node 29 cpus: 29
node 29 size: 16127 MB
node 29 free: 16108 MB
node 30 cpus: 30
node 30 size: 16127 MB
node 30 free: 16108 MB
node 31 cpus: 31
node 31 size: 16126 MB
node 31 free: 16108 MB

```

node distances:

node	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19
20	0:	10	11	11	11	12	12	12	12	12	12	12	12	12	12	12	32	32	32	32
21	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
22	1:	11	10	11	11	12	12	12	12	12	12	12	12	12	12	12	32	32	32	32
23	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
24	2:	11	11	10	11	12	12	12	12	12	12	12	12	12	12	12	32	32	32	32
25	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
26	3:	11	11	11	10	12	12	12	12	12	12	12	12	12	12	12	32	32	32	32
27	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
28	4:	12	12	12	12	10	11	11	11	12	12	12	12	12	12	12	32	32	32	32
29	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
30	5:	12	12	12	12	11	10	11	11	12	12	12	12	12	12	12	32	32	32	32
31	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
32	6:	12	12	12	12	11	11	10	11	12	12	12	12	12	12	12	32	32	32	32
33	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
34	7:	12	12	12	12	11	11	11	10	12	12	12	12	12	12	12	32	32	32	32
35	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32
36	8:	12	12	12	12	12	12	12	12	10	11	11	11	12	12	12	32	32	32	32
37	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32

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9:	12	12	12	12	12	12	12	12	12	11	10	11	11	12	12	12	12	32	32	32	32	
	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	
10:	12	12	12	12	12	12	12	12	12	11	11	10	11	12	12	12	12	32	32	32	32	
	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	
11:	12	12	12	12	12	12	12	12	12	11	11	11	10	12	12	12	12	32	32	32	32	
	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	
12:	12	12	12	12	12	12	12	12	12	12	12	12	12	12	10	11	11	11	32	32	32	32
	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	
13:	12	12	12	12	12	12	12	12	12	12	12	12	12	12	11	10	11	11	32	32	32	32
	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	
14:	12	12	12	12	12	12	12	12	12	12	12	12	12	11	11	10	11	32	32	32	32	
	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	
15:	12	12	12	12	12	12	12	12	12	12	12	12	12	11	11	11	10	32	32	32	32	
	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	
16:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	10	11	11	11	
	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	
17:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	11	10	11	11	
	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	
18:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	11	11	10	11	
	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	
19:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	11	11	11	10	
	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	
20:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	
	10	11	11	11	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	
21:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	
	11	10	11	11	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	
22:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	
	11	11	10	11	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	
23:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	
	11	11	11	10	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	12	
24:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	
	12	12	12	12	10	11	11	11	12	12	12	12	12	12	12	12	12	12	12	12	12	
25:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	
	12	12	12	12	11	10	11	11	12	12	12	12	12	12	12	12	12	12	12	12	12	
26:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	
	12	12	12	12	11	11	10	11	12	12	12	12	12	12	12	12	12	12	12	12	12	
27:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	
	12	12	12	12	11	11	11	10	12	12	12	12	12	12	12	12	12	12	12	12	12	
28:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	
	12	12	12	12	12	12	12	12	10	11	11	11	11	11	11	11	11	12	12	12	12	
29:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	
	12	12	12	12	12	12	12	12	11	10	11	11	11	11	11	11	11	12	12	12	12	
30:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	
	12	12	12	12	12	12	12	12	11	11	10	11	11	11	11	11	11	12	12	12	12	
31:	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	32	12	12	12	12	
	12	12	12	12	12	12	12	12	11	11	11	11	11	11	11	11	11	12	12	12	12	

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# SPEC CPU®2017 Integer Speed Result

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Dell Inc.

SPECspeed®2017\_int\_base = 9.92

PowerEdge C6525 (AMD EPYC 7F52, 3.50 GHz)

SPECspeed®2017\_int\_peak = 10.2

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Platform Notes (Continued)

From /proc/meminfo

MemTotal: 527958580 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="SLES"

VERSION="15-SP1"

VERSION\_ID="15.1"

PRETTY\_NAME="SUSE Linux Enterprise Server 15 SP1"

ID="sles"

ID\_LIKE="suse"

ANSI\_COLOR="0;32"

CPE\_NAME="cpe:/o:suse:sles:15:sp1"

uname -a:

Linux suse15-sp1 4.12.14-197.7-default #1 SMP Mon Jun 24 08:33:54 UTC 2019 (650fd32)  
x86\_64 x86\_64 x86\_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault):

Not affected

Microarchitectural Data Sampling:

Not affected

CVE-2017-5754 (Meltdown):

Not affected

CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp

CVE-2017-5753 (Spectre variant 1):

Mitigation: \_\_user pointer sanitization

CVE-2017-5715 (Spectre variant 2):

Mitigation: Full AMD retpoline, IBPB: conditional, IBRS\_FW, STIBP: disabled, RSB filling

run-level 3 Feb 3 13:22

SPEC is set to: /root/cpu2017-1.1.0

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	xfs	444G	13G	432G	3%	/

From /sys/devices/virtual/dmi/id

BIOS: Dell Inc. 1.2.5 12/11/2019

Vendor: Dell Inc.

Product: PowerEdge C6525

Product Family: PowerEdge

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are

(Continued on next page)



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## Platform Notes (Continued)

frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x 802C869D802C 36ASF4G72PZ-3G2E2 32 GB 2 rank 3200

(End of data from sysinfo program)

## Compiler Version Notes

```

=====
C          | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base,
          | peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
=====

```

```

-----
AOCCLLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCCLLVM.2.0.0-Build#191) (based on LLVM AOCCLLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

```

```

=====
C++       | 623.xalanbmk_s(peak)
=====

```

```

-----
AOCCLLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCCLLVM.2.0.0-Build#191) (based on LLVM AOCCLLVM.2.0.0.B191.2019_07_19)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

```

```

=====
C++       | 620.omnetpp_s(base, peak) 623.xalanbmk_s(base)
          | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
=====

```

```

-----
AOCCLLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCCLLVM.2.0.0-Build#191) (based on LLVM AOCCLLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

```

```

=====
C++       | 623.xalanbmk_s(peak)
=====

```

```

-----
AOCCLLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCCLLVM.2.0.0-Build#191) (based on LLVM AOCCLLVM.2.0.0.B191.2019_07_19)
Target: i386-unknown-linux-gnu

```

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PowerEdge C6525 (AMD EPYC 7F52, 3.50 GHz)

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CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Compiler Version Notes (Continued)

Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====  
C++ | 620.omnetpp\_s(base, peak) 623.xalancbmk\_s(base)  
| 631.deepsjeng\_s(base, peak) 641.leela\_s(base, peak)  
=====

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====  
Fortran | 648.exchange2\_s(base, peak)  
=====

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

## Base Compiler Invocation

C benchmarks:  
clang

C++ benchmarks:  
clang++

Fortran benchmarks:  
flang

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LINUX\_X64 -DSPEC\_LP64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LINUX -DSPEC\_LP64

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

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SPECspeed®2017\_int\_peak = 10.2

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Base Portability Flags (Continued)

625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64

## Base Optimization Flags

### C benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-freemap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -DSPEC_OPENMP -fopenmp
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc
-lflang
```

### C++ benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-partial-unswitch -z muldefs -DSPEC_OPENMP -fopenmp
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc
-lflang
```

### Fortran benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -ffast-math
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver2 -funroll-loops
-Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs
-mllvm -disable-indvar-simplify -mllvm -unroll-aggressive
-mllvm -unroll-threshold=150 -DSPEC_OPENMP -fopenmp -fopenmp=libomp
-lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc -lflang
```



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PowerEdge C6525 (AMD EPYC 7F52, 3.50 GHz)

SPECspeed®2017\_int\_peak = 10.2

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Base Other Flags

C benchmarks:

-Wno-return-type -DUSE\_OPENMP

C++ benchmarks:

-Wno-return-type -DUSE\_OPENMP

Fortran benchmarks:

-DUSE\_OPENMP -Wno-return-type

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Peak Portability Flags

600.perlbench\_s: -DSPEC\_LINUX\_X64 -DSPEC\_LP64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LINUX -D\_FILE\_OFFSET\_BITS=64  
625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64

## Peak Optimization Flags

C benchmarks:

600.perlbench\_s: -flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize  
-Wl,-mllvm -Wl,-vector-library=LIBMVEC

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CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Peak Optimization Flags (Continued)

600.perlbench\_s (continued):

```
-Wl,-mllvm -Wl,-reduce-array-computations=3
-fprofile-instr-generate(pass 1)
-fprofile-instr-use(pass 2) -Ofast -march=znver2
-mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -DSPEC_OPENMP -fopenmp
-lmvec -lamdlibm -fopenmp=libomp -lomp -lpthread -ldl
-ljemalloc -lflang
```

602.gcc\_s: -flto -Wl,-mllvm -Wl,-function-specialize

```
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -DSPEC_OPENMP
-fopenmp -fgnu89-inline -fopenmp=libomp -lomp -lpthread
-ldl -ljemalloc
```

605.mcf\_s: -flto -Wl,-mllvm -Wl,-function-specialize

```
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -DSPEC_OPENMP -fopenmp
-lmvec -lamdlibm -fopenmp=libomp -lomp -lpthread -ldl
-ljemalloc -lflang
```

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CPU2017 License: 55

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Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Peak Optimization Flags (Continued)

625.x264\_s: Same as 600.perlbench\_s

```

657.xz_s: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -DSPEC_OPENMP -fopenmp
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm
-ljemalloc -lflang

```

C++ benchmarks:

620.omnetpp\_s: basepeak = yes

```

623.xalancbmk_s: -m32 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -flv-function-specialization
-mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000
-mllvm -vector-library=LIBMVEC
-mllvm -inline-threshold=1000 -DSPEC_OPENMP -fopenmp
-fopenmp=libomp -lomp -lpthread -ldl -ljemalloc

```

```

631.deepsjeng_s: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -flv-function-specialization
-mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000
-mllvm -vector-library=LIBMVEC
-mllvm -inline-threshold=1000 -DSPEC_OPENMP -fopenmp
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm
-ljemalloc -lflang

```

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CPU2017 License: 55

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Tested by: Dell Inc.

Test Date: Jan-2020

Hardware Availability: Apr-2020

Software Availability: Aug-2019

## Peak Optimization Flags (Continued)

641.leela\_s: basepeak = yes

Fortran benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -ffast-math
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver2 -funroll-loops
-Mrecursive -mllvm -vector-library=LIBMVEC
-mllvm -disable-indvar-simplify -mllvm -unroll-aggressive
-mllvm -unroll-threshold=150 -DSPEC_OPENMP -fopenmp -fopenmp=libomp
-lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc -lflang
```

## Peak Other Flags

C benchmarks:

-Wno-return-type -DUSE\_OPENMP

C++ benchmarks (except as noted below):

-Wno-return-type -DUSE\_OPENMP

623.xalancbmk\_s: -Wno-return-type -DUSE\_OPENMP

-L/spo/dev/cpu2017/v110/amd\_speed\_aocc200\_rome\_C\_lib/32

Fortran benchmarks:

-DUSE\_OPENMP -Wno-return-type

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc200-flags-B1.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE9.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc200-flags-B1.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE9.xml>

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