



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS500A-E10(KRPA-U16) Server System
2.25 GHz, AMD EPYC 7742

SPECrate®2017_int_base = 353

SPECrate®2017_int_peak = 385

CPU2017 License: 9016

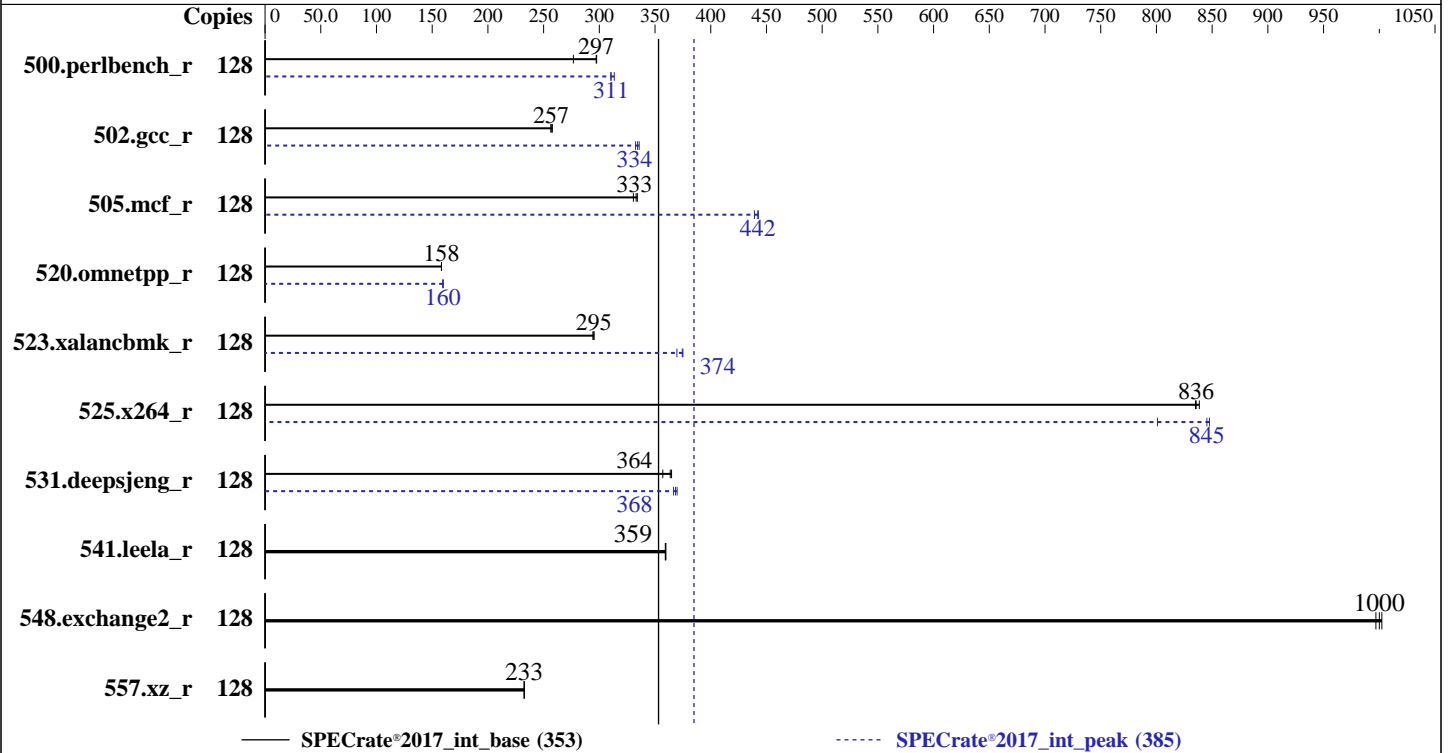
Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Aug-2019

Hardware Availability: Aug-2019

Software Availability: Aug-2019



Hardware

CPU Name: AMD EPYC 7742
 Max MHz: 3400
 Nominal: 2250
 Enabled: 64 cores, 1 chip, 2 threads/core
 Orderable: 1 chip
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 512 KB I+D on chip per core
 L3: 256 MB I+D on chip per chip,
 16 MB shared / 4 cores
 Other: None
 Memory: 256 GB (8 x 32 GB 2Rx4 PC4-3200AA-R)
 Storage: 1 x 240 GB SATA SSD
 Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP1 (x86_64)
 Kernel 4.12.14-195-default
 Compiler: C/C++/Fortran: Version 2.0.0 of AOCC
 Parallel: No
 Firmware: Version 0302 released Aug-2019
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other: jemalloc: jemalloc memory allocator library v5.2.0
 Power Management: --



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	128	686	297	685	298	736	277	128	650	313	656	311	657	310
502.gcc_r	128	707	257	706	257	703	258	128	540	336	545	333	542	334
505.mcf_r	128	621	333	626	331	619	334	128	468	442	471	439	467	443
520.omnetpp_r	128	1061	158	1061	158	1061	158	128	1052	160	1051	160	1053	160
523.xalancbmk_r	128	458	295	459	294	458	295	128	366	370	361	374	360	375
525.x264_r	128	268	835	268	836	267	838	128	280	801	264	848	265	845
531.deepsjeng_r	128	411	357	403	364	402	365	128	397	370	398	368	400	367
541.leela_r	128	590	359	590	359	589	360	128	590	359	590	359	589	360
548.exchange2_r	128	336	997	335	1000	335	1000	128	336	997	335	1000	335	1000
557.xz_r	128	595	232	594	233	594	233	128	595	232	594	233	594	233

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory
sync then drop_caches=3 to reset caches before invoking runcpu

dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were
all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

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Operating System Notes (Continued)

Transparent huge pages set to 'always' for this run (OS default)

General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/spec2017amd/amd_rate_aocc200_rome_A_lib/64:/spec2017amd/  
amd_rate_aocc200_rome_A_lib/32:"
```

```
MALLOC_CONF = "retain:true"
```

Binaries were compiled on a system with 2p AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flt0
jemalloc 5.2.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.2.0/jemalloc-5.2.0.tar.bz2>

Platform Notes

BIOS Configuration:

Power phase shedding = Disabled

SVM Mode = Disabled

SR-IOV support = Disabled

DRAM Scrub time = Disabled

NUMA nodes per socket = NPS4

Determinism Slider = Power

Sysinfo program /spec2017amd/bin/sysinfo

Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9

running on linux-k4o7 Thu Aug 22 17:14:37 2019

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : AMD EPYC 7742 64-Core Processor
```

```
1 "physical id"s (chips)
```

```
128 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 64
```

```
siblings : 128
```

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Platform Notes (Continued)

physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55 56 57 58 59 60 61 62 63

From lscpu:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:             Little Endian
Address sizes:          43 bits physical, 48 bits virtual
CPU(s):                 128
On-line CPU(s) list:   0-127
Thread(s) per core:     2
Core(s) per socket:    64
Socket(s):              1
NUMA node(s):          4
Vendor ID:              AuthenticAMD
CPU family:             23
Model:                  49
Model name:             AMD EPYC 7742 64-Core Processor
Stepping:               0
CPU MHz:                2250.000
CPU max MHz:           2250.0000
CPU min MHz:           1500.0000
BogoMIPS:               4545.10
Virtualization:        AMD-V
L1d cache:              32K
L1i cache:              32K
L2 cache:               512K
L3 cache:               16384K
NUMA node0 CPU(s):     0-15,64-79
NUMA node1 CPU(s):     16-31,80-95
NUMA node2 CPU(s):     32-47,96-111
NUMA node3 CPU(s):     48-63,112-127
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl xtopology nonstop_tsc cpuid extd_apicid aperfmperf pni
pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx f16c
rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_l2 mwaitx cpb
cat_l3 cdp_l3 hw_pstate sme ssbd sev ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2 smep
bmi2 cqm rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves
cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr arat npt
lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
pfthreshold avic v_vmsave_vmload vgif umip rdpid overflow_recov succor smca

```

```
/proc/cpuinfo cache data
cache size : 512 KB
```

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Platform Notes (Continued)

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)

node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79

node 0 size: 64313 MB

node 0 free: 63847 MB

node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95

node 1 size: 64499 MB

node 1 free: 64234 MB

node 2 cpus: 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 96 97 98 99 100 101 102 103 104 105 106 107 108 109 110 111

node 2 size: 64469 MB

node 2 free: 64208 MB

node 3 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 112 113 114 115 116 117 118 119 120 121 122 123 124 125 126 127

node 3 size: 64484 MB

node 3 free: 64213 MB

node distances:

```
node  0  1  2  3
0:  10  12  12  12
1:  12  10  12  12
2:  12  12  10  12
3:  12  12  12  10
```

From /proc/meminfo

MemTotal: 263953168 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

os-release:

NAME="SLES"

VERSION="15-SP1"

VERSION_ID="15.1"

PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"

ID="sles"

ID_LIKE="suse"

ANSI_COLOR="0;32"

CPE_NAME="cpe:/o:suse:sles:15:sp1"

uname -a:

Linux linux-k4o7 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)

x86_64 x86_64 x86_64 GNU/Linux

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Platform Notes (Continued)

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full AMD retpoline, IBPB: conditional, IBRS_FW, STIBP: conditional, RSB filling

run-level 3 Aug 22 17:13

SPEC is set to: /spec2017amd

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	199G	62G	138G	31%	/

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 0302 08/07/2019

Memory:

8x Micron Technology 36ASF4G72PZ-3G2E2 32 kB 2 rank 3200
8x Unknown Unknown

(End of data from sysinfo program)

Compiler Version Notes

C | 502.gcc_r(peak)

AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
| 525.x264_r(base, peak) 557.xz_r(base, peak)

AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin

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Compiler Version Notes (Continued)

=====
C | 502.gcc_r(peak)
=====

AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
=====

=====
C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
| 525.x264_r(base, peak) 557.xz_r(base, peak)
=====

AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
=====

=====
C++ | 523.xalancbmk_r(peak)
=====

AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
=====

=====
C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
| 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
=====

AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin
=====

=====
C++ | 523.xalancbmk_r(peak)
=====

AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
=====

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AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin

=====
C++ | 520.omnetpp_r(base, peak) 523.xalanbmk_r(base)
| 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
=====

AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin

=====
Fortran | 548.exchange2_r(base, peak)
=====

AOCC.LLVM.2.0.0.B179.2019_06_12 clang version 8.0.0 (CLANG: Jenkins
AOCC_2_0_0-Build#179) (based on LLVM AOCC.LLVM.2.0.0.B179.2019_06_12)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-2.0.0/bin

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Base Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64

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Base Portability Flags (Continued)

520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math  
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50  
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist  
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp  
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000  
-flv-function-specialization -z muldefs -lmvec -lamdlibm -ljemalloc  
-lflang
```

C++ benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2  
-mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC  
-mllvm -unroll-threshold=100 -flv-function-specialization  
-mllvm -enable-partial-unswitch -z muldefs -lmvec -lamdlibm  
-ljemalloc -lflang
```

Fortran benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -ffast-math  
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop  
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver2 -funroll-loops  
-Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs  
-mllvm -disable-indvar-simplify -mllvm -unroll-aggressive  
-mllvm -unroll-threshold=150 -lmvec -lamdlibm -ljemalloc -lflang
```



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Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Peak Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -D_FILE_OFFSET_BITS=64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-fprofile-instr-generate(pass 1)
-fprofile-instr-use(pass 2) -Ofast -march=znver2
-mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -lmvec -lamdlibm -ljemalloc
-lflang
```

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Peak Optimization Flags (Continued)

```
502.gcc_r: -m32 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -fgnu89-inline -ljemalloc
```

```
505.mcf_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -lmvec -lamdlibm -ljemalloc
-lflang
```

525.x264_r: Same as 500.perlbench_r

557.xz_r: basepeak = yes

C++ benchmarks:

```
520.omnetpp_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -flv-function-specialization
-mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000
-mllvm -vector-library=LIBMVEC
-mllvm -inline-threshold=1000 -lmvec -lamdlibm -ljemalloc
-lflang
```

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

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ASUSTeK Computer Inc.

ASUS RS500A-E10(KRPA-U16) Server System
2.25 GHz, AMD EPYC 7742

SPECrate®2017_int_base = 353

SPECrate®2017_int_peak = 385

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Aug-2019

Hardware Availability: Aug-2019

Software Availability: Aug-2019

Peak Optimization Flags (Continued)

```

523.xalancbmk_r: -m32 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -flv-function-specialization
-mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000
-mllvm -vector-library=LIBMVEC
-mllvm -inline-threshold=1000 -ljemalloc

```

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

Peak Other Flags

C benchmarks:

502.gcc_r: -L/sppo/dev/cpu2017/amd_rate_aocc200_rome/amd_rate_aocc200_rome_A_lib/32

C++ benchmarks:

523.xalancbmk_r: -L/sppo/dev/cpu2017/amd_rate_aocc200_rome/amd_rate_aocc200_rome_A_lib/32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc200-flags-A1.html>

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-AMD-Rome-V1.0-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc200-flags-A1.xml>

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-AMD-Rome-V1.0-revA.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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