SPEC CPU®2017 Floating Point Rate Result

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New H3C Technologies Co., Ltd.

$SPECrate®2017\_fp\_base = 135$

H3C UniServer R4900 G3 (Intel Xeon Gold 5118)

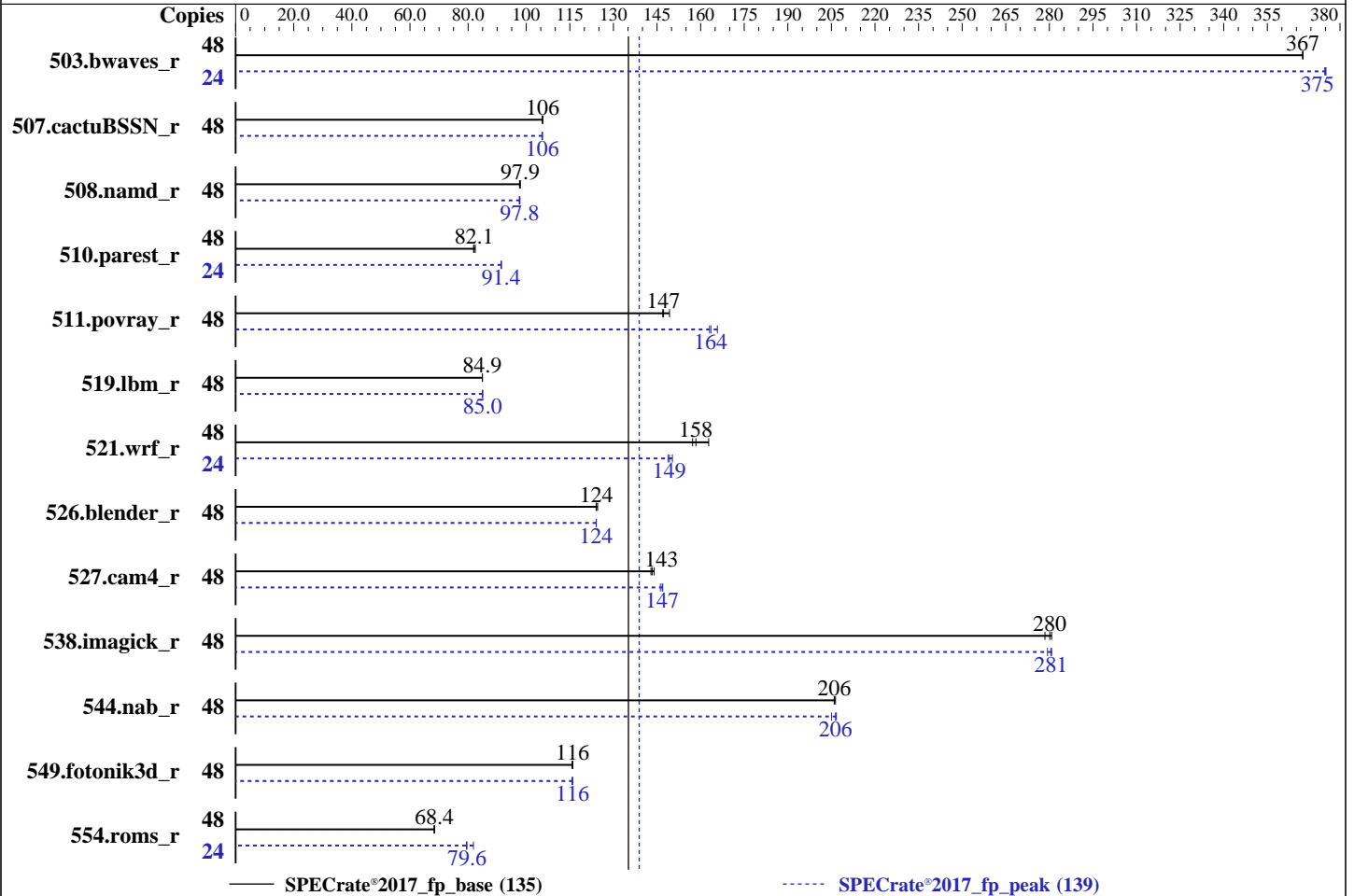$SPECrate®2017\_fp\_peak = 139$

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2019
Hardware Availability: Jul-2017
Software Availability: May-2019

Copies scale: 0 20.0 40.0 60.0 80.0 100 115 130 145 160 175 190 205 220 235 250 265 280 295 310 325 340 355 380

Benchmark	Copies	Base	Peak
503.bwaves_r	48 / 24	367	375
507.cactuBSSN_r	48	106	106
508.namd_r	48	97.9	97.8
510.parest_r	48 / 24	82.1	91.4
511.povray_r	48	147	164
519.lbm_r	48	84.9	85.0
521.wrf_r	48 / 24	158	149
526.blender_r	48	124	124
527.cam4_r	48	143	147
538.imagick_r	48	280	281
544.nab_r	48	206	206
549.fotonik3d_r	48	116	116
554.roms_r	48 / 24	68.4	79.6

—— SPECrate®2017_fp_base (135)   - - - - SPECrate®2017_fp_peak (139)

Hardware

CPU Name: Intel Xeon Gold 5118
 Max MHz: 3200
 Nominal: 2300
Enabled: 24 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 16.5 MB I+D on chip per chip
 Other: None
Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)
Storage: 1 x 1TB 7200RPM SATA HDD
Other: None

Software

OS: Red Hat Enterprise Linux Server release 7.5 (Maipo)
3.10.0-957.el7.x86_64
Compiler: C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;
Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux
Parallel: No
Firmware: Version 2.00.31 released Jul-2019 BIOS
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: --

Standard Performance Evaluation Corporation (info@spec.org)   https://www.spec.org/

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New H3C Technologies Co., Ltd.	SPECrate®2017_fp_base = 135
H3C UniServer R4900 G3 (Intel Xeon Gold 5118)	SPECrate®2017_fp_peak = 139

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
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Test Date: Aug-2019
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Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
503.bwaves_r	48	1311	367	**1311**	**367**	1311	367	24	641	375	642	375	**642**	**375**
507.cactuBSSN_r	48	575	106	**575**	**106**	576	106	48	576	105	576	106	**576**	**106**
508.namd_r	48	467	97.7	**466**	**97.9**	466	97.9	48	467	97.6	**466**	**97.8**	466	97.8
510.parest_r	48	1523	82.5	1534	81.9	**1530**	**82.1**	24	687	91.4	**687**	**91.4**	687	91.4
511.povray_r	48	763	147	751	149	**761**	**147**	48	**685**	**164**	687	163	676	166
519.lbm_r	48	596	84.9	596	84.9	**596**	**84.9**	48	596	84.9	595	85.0	**595**	**85.0**
521.wrf_r	48	684	157	**679**	**158**	661	163	24	**360**	**149**	358	150	361	149
526.blender_r	48	587	125	**589**	**124**	589	124	48	**589**	**124**	589	124	589	124
527.cam4_r	48	583	144	**586**	**143**	587	143	48	**572**	**147**	574	146	571	147
538.imagick_r	48	**426**	**280**	425	281	429	278	48	427	279	425	281	**426**	**281**
544.nab_r	48	392	206	**392**	**206**	391	206	48	391	207	394	205	**391**	**206**
549.fotonik3d_r	48	**1613**	**116**	1613	116	1615	116	48	1613	116	**1614**	**116**	1615	116
554.roms_r	48	1115	68.4	1116	68.3	**1115**	**68.4**	24	466	81.8	480	79.4	**479**	**79.6**
SPECrate®2017_fp_base =	**135**													
SPECrate®2017_fp_peak =	**139**													

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>   /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)

SPEC CPU®2017 Floating Point Rate Result

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New H3C Technologies Co., Ltd.

H3C UniServer R4900 G3 (Intel Xeon Gold 5118)

$SPECrate^{®}2017\_fp\_base = 135$

$SPECrate^{®}2017\_fp\_peak = 139$

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2019

Hardware Availability: Jul-2017

Software Availability: May-2019

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Platform Notes

```
BIOS Settings:
 Set SNC to Enabled
 Set IMC Interleaving to 1-way Interleave
 Set Stale AtoS to Enabled
 Set XPT Prefetch to Enabled
 Set DCU Streamer Prefetch to Disabled
 Set LLC dead line alloc to Disabled
 Set Package C State to C0/C1 state
 Sysinfo program /home/speccpu/bin/sysinfo
 Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
 running on localhost.localdomain Wed Aug  7 02:10:56 2019

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 5118 CPU @ 2.30GHz
       2  "physical id"s (chips)
       48 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 12
       siblings  : 24
       physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
       physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13

 From lscpu:
      Architecture:          x86_64
      CPU op-mode(s):        32-bit, 64-bit
      Byte Order:            Little Endian
      CPU(s):                48
      On-line CPU(s) list:   0-47
      Thread(s) per core:    2
      Core(s) per socket:    12
      Socket(s):             2
      NUMA node(s):          4
      Vendor ID:             GenuineIntel
      CPU family:            6
```

Standard Performance Evaluation Corporation (info@spec.org) https://www.spec.org/

New H3C Technologies Co., Ltd.

H3C UniServer R4900 G3 (Intel Xeon Gold 5118)

SPECrate®2017_fp_base = 135

SPECrate®2017_fp_peak = 139

CPU2017 License: 9066　　　　　　　　　　　　　　　　**Test Date:** Aug-2019
Test Sponsor: New H3C Technologies Co., Ltd.　　　　　**Hardware Availability:** Jul-2017
Tested by: New H3C Technologies Co., Ltd.　　　　　　**Software Availability:** May-2019

Platform Notes (Continued)

```
          Model:               85
          Model name:          Intel(R) Xeon(R) Gold 5118 CPU @ 2.30GHz
          Stepping:            4
          CPU MHz:             2301.000
          CPU max MHz:         2301.0000
          CPU min MHz:         1000.0000
          BogoMIPS:            4600.00
          Virtualization:      VT-x
          L1d cache:           32K
          L1i cache:           32K
          L2 cache:            1024K
          L3 cache:            16896K
          NUMA node0 CPU(s):   0-2,6-8,24-26,30-32
          NUMA node1 CPU(s):   3-5,9-11,27-29,33-35
          NUMA node2 CPU(s):   12-14,18-20,36-38,42-44
          NUMA node3 CPU(s):   15-17,21-23,39-41,45-47
          Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
      pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
      lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
      aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
      fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
      xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 intel_ppin
      intel_pt ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase
      tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq
      rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1
      cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts pku ospke
      spec_ctrl intel_stibp flush_l1d

 /proc/cpuinfo cache data
    cache size : 16896 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
    available: 4 nodes (0-3)
    node 0 cpus: 0 1 2 6 7 8 24 25 26 30 31 32
    node 0 size: 96919 MB
    node 0 free: 88524 MB
    node 1 cpus: 3 4 5 9 10 11 27 28 29 33 34 35
    node 1 size: 98304 MB
    node 1 free: 95433 MB
    node 2 cpus: 12 13 14 18 19 20 36 37 38 42 43 44
    node 2 size: 98304 MB
    node 2 free: 95996 MB
    node 3 cpus: 15 16 17 21 22 23 39 40 41 45 46 47
    node 3 size: 98304 MB
    node 3 free: 95946 MB
    node distances:
```

(Continued on next page)

New H3C Technologies Co., Ltd.

$SPECrate^{®}2017\_fp\_base = 135$

H3C UniServer R4900 G3 (Intel Xeon Gold 5118)

$SPECrate^{®}2017\_fp\_peak = 139$

Platform Notes (Continued)

```
   node   0   1   2   3
     0:  10  11  21  21
     1:  11  10  21  21
     2:  21  21  10  11
     3:  21  21  11  10

From /proc/meminfo
   MemTotal:        394623568 kB
   HugePages_Total:         0
   Hugepagesize:         2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.5 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VARIANT="Server"
      VARIANT_ID="server"
      VERSION_ID="7.5"
      PRETTY_NAME="OpenShift Enterprise"
   redhat-release: Red Hat Enterprise Linux Server release 7.5 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.5 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.5:ga:server

uname -a:
   Linux localhost.localdomain 3.10.0-957.el7.x86_64 #1 SMP Thu Oct 4 20:48:51 UTC 2018
   x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown):          Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS (kernel)

run-level 3 Aug 7 02:08

SPEC is set to: /home/speccpu
   Filesystem              Type  Size  Used Avail Use% Mounted on
   /dev/mapper/rhel-home xfs   876G   47G  830G   6% /home

Additional information from dmidecode follows.  WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   BIOS American Megatrends Inc. 2.00.31 07/04/2019
   Memory:
```

(Continued on next page)

New H3C Technologies Co., Ltd.

H3C UniServer R4900 G3 (Intel Xeon Gold 5118)

SPECrate®2017_fp_base = 135

SPECrate®2017_fp_peak = 139

CPU2017 License: 9066		**Test Date:**	Aug-2019
Test Sponsor: New H3C Technologies Co., Ltd.		**Hardware Availability:**	Jul-2017
Tested by: New H3C Technologies Co., Ltd.		**Software Availability:**	May-2019

Platform Notes (Continued)

```
     12x Micron 36ASF4G72PZ-2G6D1 32 GB 2 rank 2666, configured at 2400
     12x NO DIMM NO DIMM

   (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C                  | 519.lbm_r(base, peak) 538.imagick_r(base, peak)
                   | 544.nab_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C++                | 508.namd_r(base, peak) 510.parest_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C++, C             | 511.povray_r(base, peak) 526.blender_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.4.227 Build 20190416
```

(Continued on next page)

New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 135

H3C UniServer R4900 G3 (Intel Xeon Gold 5118)

SPECrate®2017_fp_peak = 139

CPU2017 License: 9066　　　　　　　　　　　　　　　　　　　　**Test Date:** Aug-2019
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Tested by: New H3C Technologies Co., Ltd.　　　　　　　　　　**Software Availability:** May-2019

Compiler Version Notes (Continued)

```
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak)
                | 554.roms_r(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
```
icc -m64 -std=c11
```

C++ benchmarks:
```
icpc -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using both C and C++:
```
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:
```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

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Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

C++ benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -auto
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -auto
-nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -auto
-nostandard-realloc-lhs -align array32byte

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SPECrate®2017_fp_base = 135

SPECrate®2017_fp_peak = 139

Peak Compiler Invocation

C benchmarks:
```
icc -m64 -std=c11
```

C++ benchmarks:
```
icpc -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using both C and C++:
```
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:
```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

519.lbm_r: `-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4`

538.imagick_r: `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4`

544.nab_r: Same as 538.imagick_r

C++ benchmarks:

508.namd_r: `-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4`

(Continued on next page)

New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 135

H3C UniServer R4900 G3 (Intel Xeon Gold 5118)

SPECrate®2017_fp_peak = 139

CPU2017 License: 9066
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Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2019
Hardware Availability: Jul-2017
Software Availability: May-2019

Peak Optimization Flags (Continued)

510.parest_r: `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4`

Fortran benchmarks:

503.bwaves_r: `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs -align array32byte`

549.fotonik3d_r: `Same as 503.bwaves_r`

554.roms_r: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs -align array32byte`

Benchmarks using both Fortran and C:
`-prof-gen`(pass 1) `-prof-use`(pass 2) `-ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs -align array32byte`

Benchmarks using both C and C++:

511.povray_r: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4`

526.blender_r: `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4`

Benchmarks using Fortran, C, and C++:
`-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs -align array32byte`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic19.0u1-official-linux64.2019-07-09.html
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.3-SKL-RevD.2019-08-21.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.0u1-official-linux64.2019-07-09.xml
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.3-SKL-RevD.2019-08-21.xml

SPEC CPU®2017 Floating Point Rate Result

New H3C Technologies Co., Ltd.

SPECrate®2017_fp_base = 135

H3C UniServer R4900 G3 (Intel Xeon Gold 5118)

SPECrate®2017_fp_peak = 139

CPU2017 License: 9066		**Test Date:**	Aug-2019
Test Sponsor:	New H3C Technologies Co., Ltd.	**Hardware Availability:**	Jul-2017
Tested by:	New H3C Technologies Co., Ltd.	**Software Availability:**	May-2019

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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