



# SPEC® CPU2017 Floating Point Speed Result

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## Supermicro

SuperWorkstation 5039C-I (X11SCL-F , Intel Pentium Gold G5500T)

SPECspeed2017\_fp\_base = 12.2

SPECspeed2017\_fp\_peak = 13.2

CPU2017 License: 001176

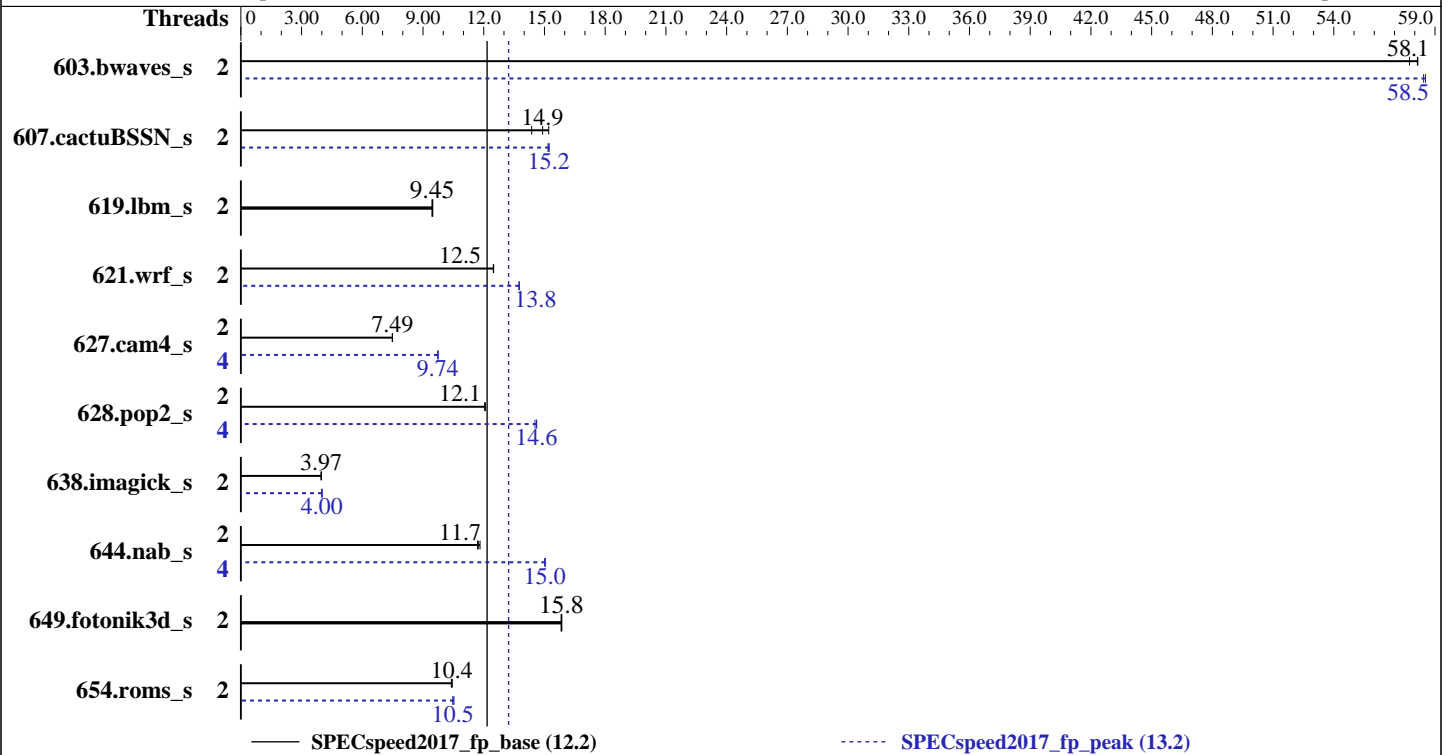
Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Apr-2019

Hardware Availability: Nov-2018

Software Availability: Sep-2018



### Hardware

CPU Name: Intel Pentium Gold G5500T  
 Max MHz.: 3200  
 Nominal: 3200  
 Enabled: 2 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 256 KB I+D on chip per core  
 L3: 4 MB I+D on chip per chip  
 Other: None  
 Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2666V-E, running at 2400)  
 Storage: 1 x 200 GB SATA III SSD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 12 SP3 (x86\_64)  
 Kernel 4.4.114-94.11-default  
 Compiler: C/C++: Version 19.0.0.117 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 19.0.0.117 of Intel Fortran Compiler for Linux  
 Parallel: Yes  
 Firmware: Version 1.0a released Feb-2019  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None



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## Results Table

| Benchmark       | Base    |             |             |             |             |             |             | Peak    |             |             |             |             |            |             |
|-----------------|---------|-------------|-------------|-------------|-------------|-------------|-------------|---------|-------------|-------------|-------------|-------------|------------|-------------|
|                 | Threads | Seconds     | Ratio       | Seconds     | Ratio       | Seconds     | Ratio       | Threads | Seconds     | Ratio       | Seconds     | Ratio       | Seconds    | Ratio       |
| 603.bwaves_s    | 2       | 1015        | 58.2        | 1022        | 57.7        | <b>1015</b> | <b>58.1</b> | 2       | 1010        | 58.4        | <b>1008</b> | <b>58.5</b> | 1008       | 58.5        |
| 607.cactuBSSN_s | 2       | 1160        | 14.4        | <b>1118</b> | <b>14.9</b> | 1096        | 15.2        | 2       | <b>1095</b> | <b>15.2</b> | 1095        | 15.2        | 1099       | 15.2        |
| 619.lbm_s       | 2       | 552         | 9.48        | 554         | 9.45        | <b>554</b>  | <b>9.45</b> | 2       | 552         | 9.48        | 554         | 9.45        | <b>554</b> | <b>9.45</b> |
| 621.wrf_s       | 2       | 1060        | 12.5        | <b>1059</b> | <b>12.5</b> | 1059        | 12.5        | 2       | <b>962</b>  | <b>13.8</b> | 962         | 13.8        | 962        | 13.7        |
| 627.cam4_s      | 2       | <b>1184</b> | <b>7.49</b> | 1184        | 7.48        | 1183        | 7.49        | 4       | <b>910</b>  | <b>9.74</b> | 910         | 9.74        | 910        | 9.74        |
| 628.pop2_s      | 2       | <b>984</b>  | <b>12.1</b> | 984         | 12.1        | 985         | 12.1        | 4       | 812         | 14.6        | <b>813</b>  | <b>14.6</b> | 817        | 14.5        |
| 638.imagick_s   | 2       | <b>3633</b> | <b>3.97</b> | 3637        | 3.97        | 3628        | 3.98        | 2       | 3597        | 4.01        | <b>3602</b> | <b>4.00</b> | 3604       | 4.00        |
| 644.nab_s       | 2       | 1479        | 11.8        | <b>1489</b> | <b>11.7</b> | 1494        | 11.7        | 4       | <b>1163</b> | <b>15.0</b> | 1161        | 15.0        | 1164       | 15.0        |
| 649.fotonik3d_s | 2       | 576         | 15.8        | 575         | 15.9        | <b>576</b>  | <b>15.8</b> | 2       | 576         | 15.8        | 575         | 15.9        | <b>576</b> | <b>15.8</b> |
| 654.roms_s      | 2       | 1514        | 10.4        | <b>1511</b> | <b>10.4</b> | 1508        | 10.4        | 2       | 1503        | 10.5        | <b>1503</b> | <b>10.5</b> | 1495       | 10.5        |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact,1,0"  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64"  
OMP\_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.5  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9

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**Tested by:** Supermicro

**Test Date:** Apr-2019  
**Hardware Availability:** Nov-2018  
**Software Availability:** Sep-2018

### Platform Notes (Continued)

running on linux-65nv Fri Apr 19 17:46:20 2019

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Pentium(R) Gold G5500T CPU @ 3.20GHz

1 "physical id"s (chips)  
4 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 2  
siblings : 4  
physical 0: cores 0 1

From lscpu:

Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 4  
On-line CPU(s) list: 0-3  
Thread(s) per core: 2  
Core(s) per socket: 2  
Socket(s): 1  
NUMA node(s): 1  
Vendor ID: GenuineIntel  
CPU family: 6  
Model: 158  
Model name: Intel(R) Pentium(R) Gold G5500T CPU @ 3.20GHz  
Stepping: 11  
CPU MHz: 3200.013  
CPU max MHz: 3200.0000  
CPU min MHz: 800.0000  
BogoMIPS: 6383.99  
Virtualization: VT-x  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 256K  
L3 cache: 4096K  
NUMA node0 CPU(s): 0-3

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant\_tsc art arch\_perfmon pebs bts rep\_good nopl xtopology nonstop\_tsc aperfperf eagerfpu pni pclmulqdq dtes64 monitor ds\_cpl vmx est tm2 ssse3 sdbg cx16 xtpr pdcm pcid sse4\_1 sse4\_2 x2apic movbe popcnt tsc\_deadline\_timer aes xsave rdrand lahf\_lm abm 3dnowprefetch arat epb invpcid\_single pln pts dtherm hwp hwp\_notify

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### Platform Notes (Continued)

```
hwp_act_window hwp_epp intel_pt rsb_ctxsw spec_ctrl retpoline kaiser tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust smep erms invpcid mpx rdseed smap
clflushopt xsaveopt xsavec xgetbv1
```

```
/proc/cpuinfo cache data
cache size : 4096 KB
```

```
From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
```

```
available: 1 nodes (0)
node 0 cpus: 0 1 2 3
node 0 size: 64333 MB
node 0 free: 44161 MB
node distances:
node 0
0: 10
```

```
From /proc/meminfo
```

```
MemTotal: 65877944 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
From /etc/*release* /etc/*version*
```

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
```

```
os-release:
NAME="SLES"
VERSION="12-SP3"
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

```
uname -a:
```

```
Linux linux-65nv 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB
```

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### Platform Notes (Continued)

run-level 3 Apr 18 11:33

SPEC is set to: /home/cpu2017

| Filesystem | Type | Size | Used | Avail | Use% | Mounted on |
|------------|------|------|------|-------|------|------------|
| /dev/sda3  | xfs  | 145G | 27G  | 118G  | 19%  | /home      |

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0a 02/14/2019

Memory:

4x Micron 18ADF2G72AZ-2G6H1R 16 GB 2 rank 2667, configured at 2400

(End of data from sysinfo program)

### Compiler Version Notes

=====  
CC 619.lbm\_s(base) 638.imagick\_s(base, peak) 644.nab\_s(base, peak)  
=====

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.0.117 Build 20180804  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
CC 619.lbm\_s(peak)  
=====

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.0.117 Build 20180804  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
FC 607.cactuBSSN\_s(base, peak)  
=====

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.0.117 Build 20180804  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.0.117 Build 20180804  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.0.117 Build 20180804

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### Compiler Version Notes (Continued)

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=====  
FC 603.bwaves\_s(base) 649.fotonik3d\_s(base) 654.roms\_s(base, peak)

-----  
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.0.117 Build 20180804

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
FC 603.bwaves\_s(peak) 649.fotonik3d\_s(peak)

-----  
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.0.117 Build 20180804

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
CC 621.wrf\_s(base) 627.cam4\_s(base, peak) 628.pop2\_s(base)

-----  
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.0.117 Build 20180804

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.0.117 Build 20180804

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
CC 621.wrf\_s(peak) 628.pop2\_s(peak)

-----  
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.0.117 Build 20180804

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.0.117 Build 20180804

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

### Base Compiler Invocation

C benchmarks:  
icc -m64 -std=c11

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## Base Compiler Invocation (Continued)

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

## Base Portability Flags

```
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
```

Fortran benchmarks:

```
-DSPEC_OPENMP -xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte
```

Benchmarks using both Fortran and C:

```
-xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte
```

Benchmarks using Fortran, C, and C++:

```
-xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
```

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## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):  
-nostandard-realloc-lhs -align array32byte

## Peak Compiler Invocation

C benchmarks:  
icc -m64 -std=c11

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:  
icpc -m64 icc -m64 -std=c11 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

619.lbm\_s: basepeak = yes

638.imagick\_s: -xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp  
-DSPEC\_OPENMP

644.nab\_s: Same as 638.imagick\_s

Fortran benchmarks:

603.bwaves\_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC\_SUPPRESS\_OPENMP  
-DSPEC\_OPENMP -O2 -xSSE4.2 -qopt-prefetch -ipo -O3  
-no-prec-div -ffinite-math-only -qopt-mem-layout-trans=3  
-qopenmp -nostandard-realloc-lhs -align array32byte

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## Peak Optimization Flags (Continued)

649.fotonik3d\_s: basepeak = yes

```
654.roms_s: -DSPEC_OPENMP -xSSE4.2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3
-qopenmp -nostandard-realloc-lhs -align array32byte
```

Benchmarks using both Fortran and C:

```
621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xSSE4.2
-qopt-prefetch -ipo -O3 -no-prec-div -ffinite-math-only
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte
```

```
627.cam4_s: -xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte
```

628.pop2\_s: Same as 621.wrf\_s

Benchmarks using Fortran, C, and C++:

```
-xSSE4.2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic19.0-official-linux64.2019-01-15.html>  
<http://www.spec.org/cpu2017/flags/Default-Platform-Flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic19.0-official-linux64.2019-01-15.xml>  
<http://www.spec.org/cpu2017/flags/Default-Platform-Flags.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

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