SPEC CPU®2017 Integer Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen10
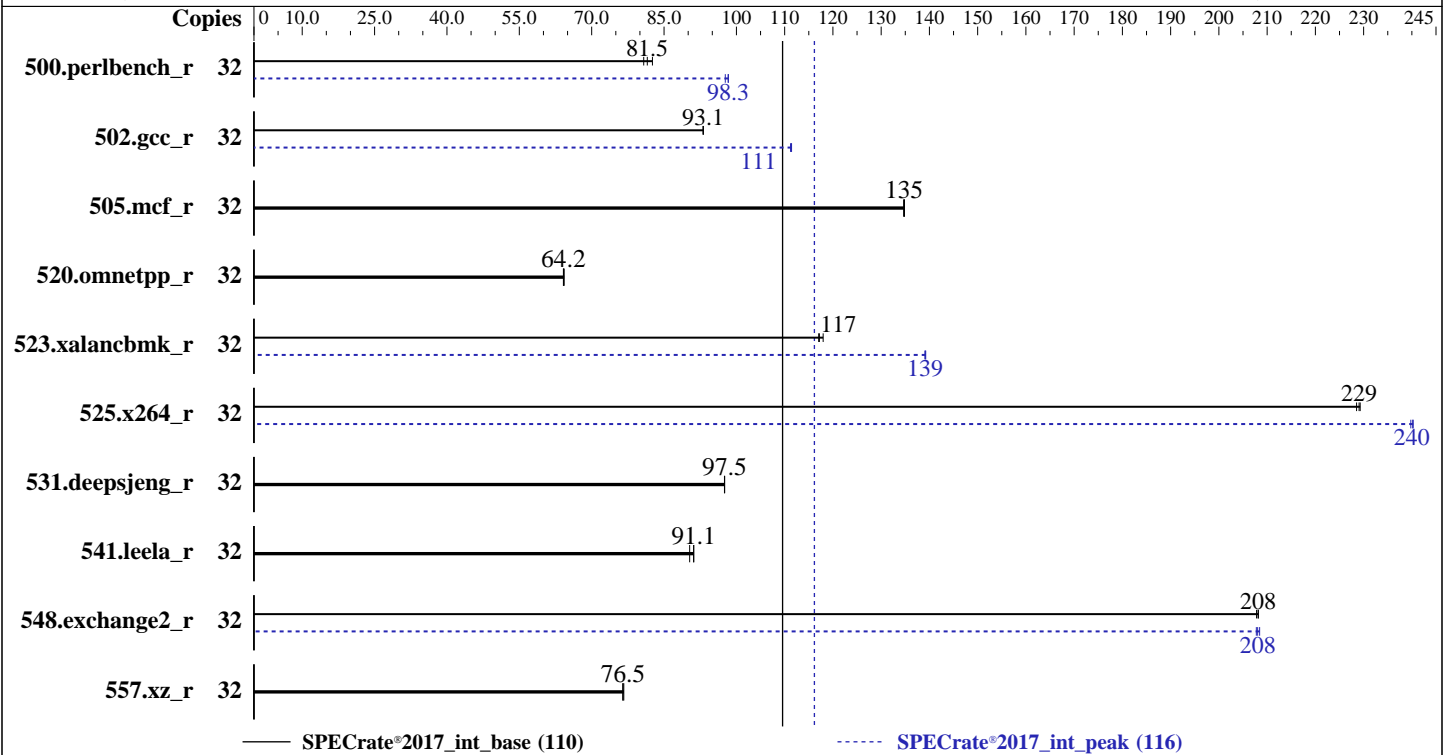(3.20 GHz, Intel Xeon Gold 6134)

$SPECrate^{®}2017\_int\_base = 110$

$SPECrate^{®}2017\_int\_peak = 116$

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Dec-2018
Hardware Availability: Oct-2018
Software Availability: Nov-2018

Copies			
500.perlbench_r	32	base	81.5
		peak	98.3
502.gcc_r	32	base	93.1
		peak	111
505.mcf_r	32	base	135
520.omnetpp_r	32	base	64.2
523.xalancbmk_r	32	base	117
		peak	139
525.x264_r	32	base	229
		peak	240
531.deepsjeng_r	32	base	97.5
541.leela_r	32	base	91.1
548.exchange2_r	32	base	208
		peak	208
557.xz_r	32	base	76.5

SPECrate®2017_int_base (110)   SPECrate®2017_int_peak (116)

Hardware

CPU Name: Intel Xeon Gold 6134
 Max MHz: 3700
 Nominal: 3200
Enabled: 16 cores, 2 chips, 2 threads/core
Orderable: 1, 2 chip(s)
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 24.75 MB I+D on chip per chip
 Other: None
Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)
Storage: 2 x 300 GB 10 K SAS, RAID 1
Other: None

Software

OS: SUSE Linux Enterprise Server 12 SP3
 Kernel 4.4.162-94.72-default
Compiler: C/C++: Version 19.0.0.117 of Intel
 C/C++ Compiler for Linux;
 Fortran: Version 19.0.0.117 of
 Intel Fortran Compiler for Linux
Parallel: No
Firmware: HPE BIOS Version U30 10/02/2018 released Oct-2018
File System: btrfs
System State: Run level 5 (multi-user, w/GUI)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: --

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	32	**625**	**81.5**	617	82.6	631	80.8	32	**518**	**98.3**	518	98.3	521	97.7
502.gcc_r	32	**487**	**93.1**	487	93.1	487	93.1	32	407	111	**407**	**111**	407	111
505.mcf_r	32	**384**	**135**	384	135	384	135	32	**384**	**135**	384	135	384	135
520.omnetpp_r	32	**654**	**64.2**	653	64.3	655	64.1	32	**654**	**64.2**	653	64.3	655	64.1
523.xalancbmk_r	32	289	117	286	118	**288**	**117**	32	243	139	**243**	**139**	243	139
525.x264_r	32	244	229	245	229	**245**	**229**	32	**233**	**240**	234	240	233	240
531.deepsjeng_r	32	376	97.6	**376**	**97.5**	376	97.5	32	376	97.6	**376**	**97.5**	376	97.5
541.leela_r	32	**581**	**91.1**	581	91.2	587	90.3	32	**581**	**91.1**	581	91.2	587	90.3
548.exchange2_r	32	403	208	**403**	**208**	403	208	32	402	208	**403**	**208**	404	208
557.xz_r	32	451	76.7	**452**	**76.5**	452	76.4	32	451	76.7	**452**	**76.5**	452	76.4
SPECrate®2017_int_base =		**110**											
SPECrate®2017_int_peak =		**116**											

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3>   /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
  numactl --interleave=all runcpu <etc>

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/cpu2017/lib/ia32:/cpu2017/lib/intel64:/cpu2017/je5.0.1-32:/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

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General Notes (Continued)

```
 is mitigated in the system as tested and documented.
 jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS Configuration:
  Memory Patrol Scrubbing set to Disabled
  LLC Dead Line Allocation set to Disabled
  Thermal Configuration set to Maximum Cooling
  LLC Prefetch set to Enabled
  Workload Profile set to General Throughput Compute
    Minimum Processor Idle Power Core C-State set to C1E

 Sysinfo program /cpu2017/bin/sysinfo
 Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
 running on linux-daj0 Thu Dec 20 16:06:33 2018

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 6134 CPU @ 3.20GHz
       2  "physical id"s (chips)
       32 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 8
       siblings  : 16
       physical 0: cores 0 2 3 9 16 19 26 27
       physical 1: cores 0 2 3 9 16 19 26 27

 From lscpu:
      Architecture:         x86_64
      CPU op-mode(s):       32-bit, 64-bit
      Byte Order:           Little Endian
      CPU(s):               32
      On-line CPU(s) list:  0-31
      Thread(s) per core:   2
      Core(s) per socket:   8
      Socket(s):            2
      NUMA node(s):         4
      Vendor ID:            GenuineIntel
      CPU family:           6
```

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Platform Notes (Continued)

```
      Model:                85
      Model name:           Intel(R) Xeon(R) Gold 6134 CPU @ 3.20GHz
      Stepping:             4
      CPU MHz:              3192.526
      BogoMIPS:             6385.05
      Virtualization:       VT-x
      L1d cache:            32K
      L1i cache:            32K
      L2 cache:             1024K
      L3 cache:             25344K
      NUMA node0 CPU(s):    0-3,16-19
      NUMA node1 CPU(s):    4-7,20-23
      NUMA node2 CPU(s):    8-11,24-27
      NUMA node3 CPU(s):    12-15,28-31
      Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
    pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
    lm ibrs flush_l1d constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
    nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2
    ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
    tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb
    invpcid_single pln pts dtherm intel_pt ssbd ibpb stibp kaiser tpr_shadow vnmi
    flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
    cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl
    xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc pku ospke

 /proc/cpuinfo cache data
   cache size : 25344 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
   available: 4 nodes (0-3)
   node 0 cpus: 0 1 2 3 16 17 18 19
   node 0 size: 96263 MB
   node 0 free: 95279 MB
   node 1 cpus: 4 5 6 7 20 21 22 23
   node 1 size: 96766 MB
   node 1 free: 93177 MB
   node 2 cpus: 8 9 10 11 24 25 26 27
   node 2 size: 96766 MB
   node 2 free: 94061 MB
   node 3 cpus: 12 13 14 15 28 29 30 31
   node 3 size: 96624 MB
   node 3 free: 96209 MB
   node distances:
   node   0   1   2   3
     0:  10  21  31  31
     1:  21  10  31  31
```

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Platform Notes (Continued)

```
     2:  31  31  10  21
     3:  31  31  21  10

From /proc/meminfo
   MemTotal:       395694608 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP3

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 3
      # This file is deprecated and will be removed in a future service pack or release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP3"
      VERSION_ID="12.3"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
   Linux linux-daj0 4.4.162-94.72-default #1 SMP Mon Nov 12 18:57:45 UTC 2018 (9de753f)
   x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown):           Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB, IBPB, IBRS_FW, STIBP, RSB
filling

run-level 5 Dec 20 13:41

SPEC is set to: /cpu2017
   Filesystem      Type   Size  Used Avail Use% Mounted on
   /dev/sda3      btrfs  278G   13G  265G   5% /

Additional information from dmidecode follows.  WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
```

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Platform Notes (Continued)

```
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   BIOS HPE U30 10/02/2018
   Memory:
    12x HPE 840758-091 32 GB 2 rank 2666
    12x UNKNOWN NOT AVAILABLE

 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C       | 502.gcc_r(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version
  19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
        | 525.x264_r(base, peak) 557.xz_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C       | 502.gcc_r(peak)
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Copyright (C) 1985-2018 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
        | 525.x264_r(base, peak) 557.xz_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C++     | 523.xalancbmk_r(peak)
```

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Compiler Version Notes (Continued)

```
--------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version
  19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------


==========================================================================
C++      | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
         | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
--------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------


==========================================================================
C++      | 523.xalancbmk_r(peak)
--------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version
  19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------


==========================================================================
C++      | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
         | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
--------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------


==========================================================================
Fortran | 548.exchange2_r(base, peak)
--------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.0.117 Build 20180804
Copyright (C) 1985-2018 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:

icc -m64 -std=c11

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Base Compiler Invocation (Continued)

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Base Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-DSPEC_LP64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LP64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
`-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

C++ benchmarks:
`-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

Fortran benchmarks:
`-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`
`-L/usr/local/je5.0.1-64/lib -ljemalloc`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64 -std=c11`

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Peak Compiler Invocation (Continued)

502.gcc_r: `icc -m32 -std=c11 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.0.117/linux/compiler/lib/ia32_lin`

C++ benchmarks (except as noted below):
`icpc -m64`

523.xalancbmk_r: `icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.0.117/linux/compiler/lib/ia32_lin`

Fortran benchmarks:
`ifort -m64`

Peak Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-D_FILE_OFFSET_BITS=64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-D_FILE_OFFSET_BITS=64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Peak Optimization Flags

C benchmarks:

500.perlbench_r: `-Wl,-z,muldefs -prof-gen`(pass 1) `-prof-use`(pass 2) `-ipo`
`-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3`
`-fno-strict-overflow -L/usr/local/je5.0.1-64/lib`
`-ljemalloc`

502.gcc_r: `-Wl,-z,muldefs -prof-gen`(pass 1) `-prof-use`(pass 2) `-ipo`
`-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3`
`-L/usr/local/je5.0.1-32/lib -ljemalloc`

505.mcf_r: `basepeak = yes`

525.x264_r: `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
`-qopt-mem-layout-trans=3 -fno-alias`
`-L/usr/local/je5.0.1-64/lib -ljemalloc`

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Peak Optimization Flags (Continued)

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic19.0-official-linux64.2019-01-15.html
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html

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You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.0-official-linux64.2019-01-15.xml
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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