



# SPEC® CPU2017 Integer Speed Result

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## Supermicro

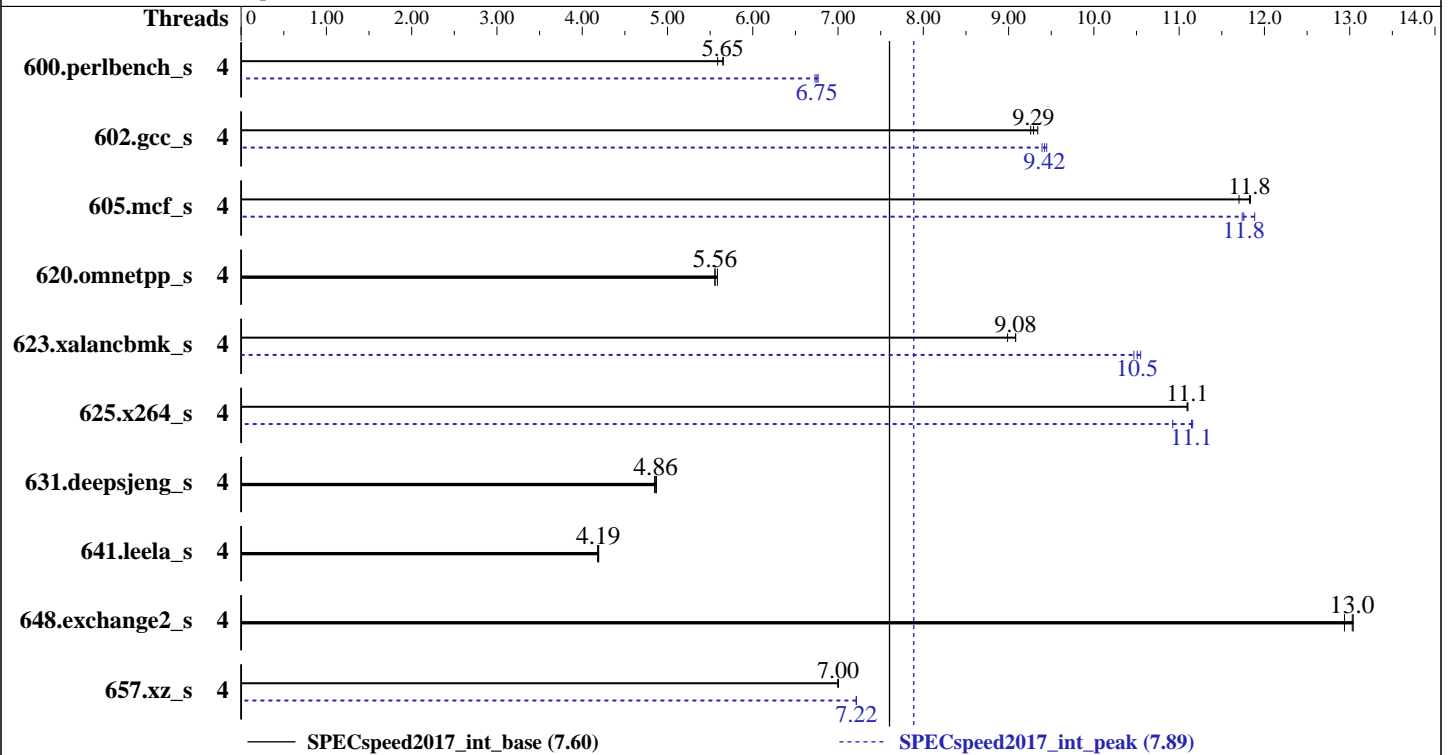
SuperServer E300-9C (X11SCV-Q , Intel Core i3-8100)

SPECspeed2017\_int\_base = 7.60

SPECspeed2017\_int\_peak = 7.89

CPU2017 License: 001176  
Test Sponsor: Supermicro  
Tested by: Supermicro

Test Date: Nov-2018  
Hardware Availability: Apr-2018  
Software Availability: Mar-2018



### Hardware

CPU Name: Intel Core i3-8100  
Max MHz.: 3600  
Nominal: 3600  
Enabled: 4 cores, 1 chip  
Orderable: 1 chip  
Cache L1: 32 KB I + 32 KB D on chip per core  
L2: 256 KB I+D on chip per core  
L3: 6 MB I+D on chip per chip  
Other: None  
Memory: 32 GB (2 x 16 GB 2Rx8 PC4-2666V-S, running at 2400)  
Storage: 1 x 1 TB SATA III, 7200RPM  
Other: None

### Software

OS: SUSE Linux Enterprise Server 12 SP3 (x86\_64)  
Kernel 4.4.114-94.11-default  
Compiler: C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux;  
Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux  
Parallel: Yes  
Firmware: Version 1.0 released Aug-2018  
File System: xfs  
System State: Run level 5 (multi-user with display manager)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other: jemalloc memory allocator library V5.0.1



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	4	318	5.59	<b>314</b>	<b>5.65</b>	314	5.66	4	264	6.73	262	6.77	<b>263</b>	<b>6.75</b>
602.gcc_s	4	<b>429</b>	<b>9.29</b>	426	9.34	430	9.26	4	424	9.39	421	9.45	<b>423</b>	<b>9.42</b>
605.mcf_s	4	<b>399</b>	<b>11.8</b>	403	11.7	399	11.8	4	402	11.7	<b>402</b>	<b>11.8</b>	397	11.9
620.omnetpp_s	4	294	5.56	292	5.59	<b>293</b>	<b>5.56</b>	4	294	5.56	292	5.59	<b>293</b>	<b>5.56</b>
623.xalancbmk_s	4	158	8.99	156	9.08	<b>156</b>	<b>9.08</b>	4	<b>135</b>	<b>10.5</b>	135	10.5	134	10.5
625.x264_s	4	<b>159</b>	<b>11.1</b>	159	11.1	159	11.1	4	161	10.9	<b>158</b>	<b>11.1</b>	158	11.2
631.deepsjeng_s	4	296	4.85	<b>295</b>	<b>4.86</b>	294	4.87	4	296	4.85	<b>295</b>	<b>4.86</b>	294	4.87
641.leela_s	4	407	4.19	<b>407</b>	<b>4.19</b>	408	4.18	4	407	4.19	<b>407</b>	<b>4.19</b>	408	4.18
648.exchange2_s	4	227	12.9	<b>226</b>	<b>13.0</b>	225	13.0	4	227	12.9	<b>226</b>	<b>13.0</b>	225	13.0
657.xz_s	4	<b>883</b>	<b>7.00</b>	882	7.01	884	7.00	4	857	7.21	<b>857</b>	<b>7.22</b>	857	7.22

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

OMP\_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



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### Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9  
running on linux-cgrt Sun Nov 18 07:58:50 2018

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Core(TM) i3-8100 CPU @ 3.60GHz  
1 "physical id"s (chips)  
4 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 4  
siblings : 4  
physical 0: cores 0 1 2 3

From lscpu:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 4  
On-line CPU(s) list: 0-3  
Thread(s) per core: 1  
Core(s) per socket: 4  
Socket(s): 1  
NUMA node(s): 1  
Vendor ID: GenuineIntel  
CPU family: 6  
Model: 158  
Model name: Intel(R) Core(TM) i3-8100 CPU @ 3.60GHz  
Stepping: 10  
CPU MHz: 3600.000  
CPU max MHz: 3600.0000  
CPU min MHz: 800.0000  
BogoMIPS: 7199.96  
Virtualization: VT-x  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 256K  
L3 cache: 6144K  
NUMA node0 CPU(s): 0-3  
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant\_tsc art arch\_perfmon pebs bts rep\_good nopl xtopology nonstop\_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds\_cpl vmx est tm2 ssse3 sdbg fma

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### Platform Notes (Continued)

```
cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch arat epb invpcid_single pln pts dtherm hwp
hwp_notify hwp_act_window hwp_epp intel_pt rsb_ctxsw spec_ctrl retpoline kaiser
tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms
invpcid mpx rdseed adx smap clflushopt xsaveopt xsavec xgetbv1
```

```
/proc/cpuinfo cache data
cache size : 6144 KB
```

```
From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
```

```
available: 1 nodes (0)
node 0 cpus: 0 1 2 3
node 0 size: 31808 MB
node 0 free: 16878 MB
node distances:
node    0
0:     10
```

```
From /proc/meminfo
```

```
MemTotal:      32572016 kB
HugePages_Total:       0
Hugepagesize:       2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP3
```

```
From /etc/*release* /etc/*version*
```

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
```

```
os-release:
NAME="SLES"
VERSION="12-SP3"
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

```
uname -a:
```

```
Linux linux-cgrt 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
x86_64 x86_64 x86_64 GNU/Linux
```

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### Platform Notes (Continued)

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Mitigation: PTI  
CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers  
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 5 Nov 17 19:08

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda3	xf	890G	59G	831G	7%	/home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0 08/03/2018

Memory:

2x Micron 16ATF2G64HZ-2G6E1 16 GB 2 rank 2667, configured at 2400

(End of data from sysinfo program)

### Compiler Version Notes

=====  
CC 600.perlbench\_s(base) 602.gcc\_s(base) 605.mcf\_s(base) 625.x264\_s(base, peak) 657.xz\_s(base)  
-----

icc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
CC 600.perlbench\_s(peak) 602.gcc\_s(peak) 605.mcf\_s(peak) 657.xz\_s(peak)  
-----

icc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
CXXC 620.omnetpp\_s(base) 623.xalancbmk\_s(base) 631.deepsjeng\_s(base) 641.leela\_s(base)  
-----

icpc (ICC) 18.0.2 20180210

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
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## Compiler Version Notes (Continued)

=====  
CXXC 620.omnetpp\_s(peak) 623.xalancbmk\_s(peak) 631.deepsjeng\_s(peak)  
641.leela\_s(peak)  
-----

icpc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
FC 648.exchange2\_s(base, peak)  
-----

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:  
icc -m64 -std=c11

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LP64 -DSPEC\_LINUX  
625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64



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## Base Optimization Flags

C benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

C++ benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64 -std=c11
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
623.xalancbmk_s: icpc -m32 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin
```

Fortran benchmarks:

```
ifort -m64
```

## Peak Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64  
602.gcc_s: -DSPEC_LP64  
605.mcf_s: -DSPEC_LP64  
620.omnetpp_s: -DSPEC_LP64  
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX  
625.x264_s: -DSPEC_LP64  
631.deepsjeng_s: -DSPEC_LP64  
641.leela_s: -DSPEC_LP64  
648.exchange2_s: -DSPEC_LP64  
657.xz_s: -DSPEC_LP64
```



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## Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2  
-xCORE-AVX2 -qopt-prefetch -ipo -O3  
-qopt-mem-layout-trans=3 -no-prec-div  
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP  
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib  
-ljemalloc
```

```
602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2  
-xCORE-AVX2 -qopt-prefetch -ipo -O3  
-qopt-mem-layout-trans=3 -no-prec-div  
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp  
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
625.x264_s: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-prefetch -qopt-mem-layout-trans=3 -qopenmp  
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc
```

657.xz\_s: Same as 602.gcc\_s

C++ benchmarks:

620.omnetpp\_s: basepeak = yes

```
623.xalancbmk_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp  
-DSPEC_OPENMP -L/usr/local/je5.0.1-32/lib -ljemalloc
```

631.deepsjeng\_s: basepeak = yes

641.leela\_s: basepeak = yes

Fortran benchmarks:

648.exchange2\_s: basepeak = yes





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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.html>  
<http://www.spec.org/cpu2017/flags/Default-Platform-Flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml>  
<http://www.spec.org/cpu2017/flags/Default-Platform-Flags.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

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