



# SPEC® CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Dell Inc.

PowerEdge MX840c (Intel Xeon Platinum 8156 CPU, 3.60GHz)

SPECrate2017\_int\_base = 110

SPECrate2017\_int\_peak = 116

CPU2017 License: 55

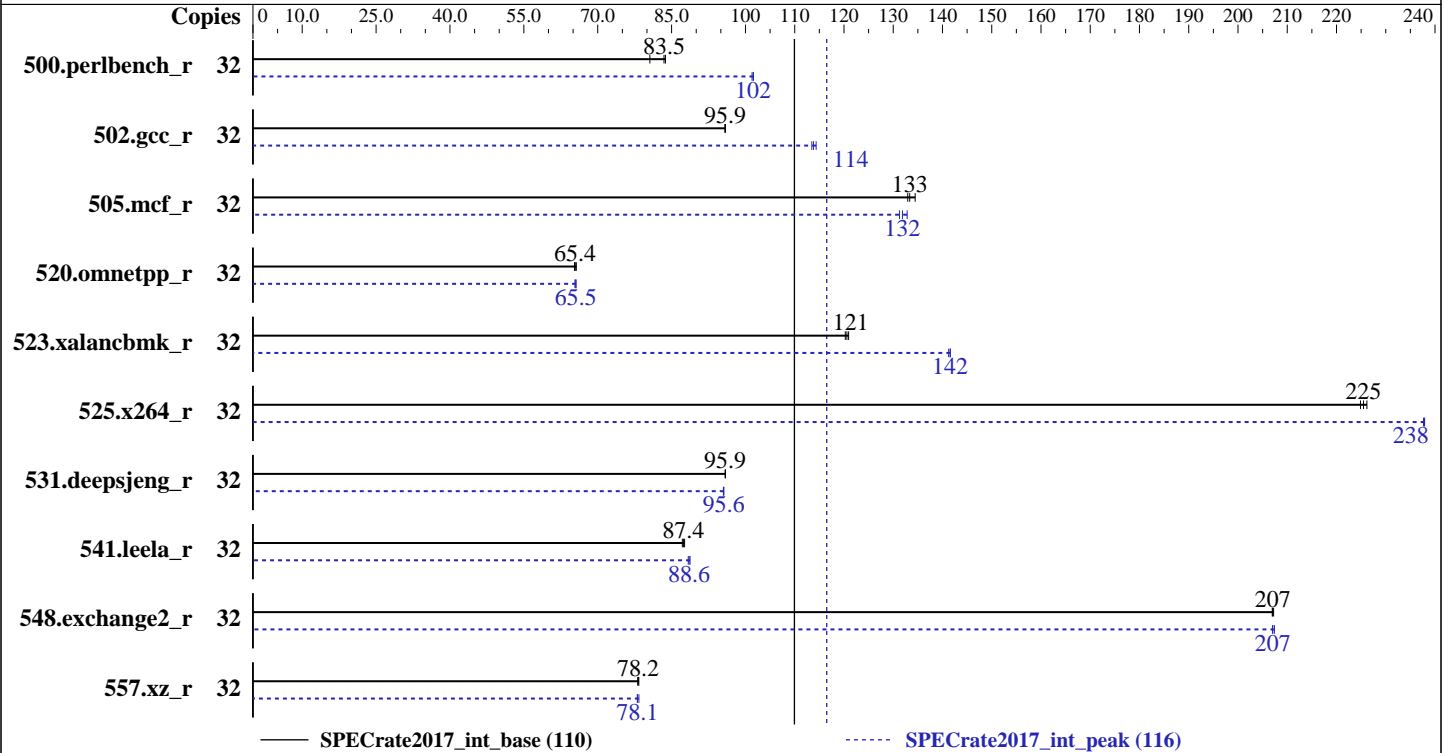
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2018

Hardware Availability: Sep-2017

Software Availability: Sep-2017



### Hardware

CPU Name: Intel Xeon Platinum 8156  
 Max MHz.: 3700  
 Nominal: 3600  
 Enabled: 16 cores, 4 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 16.5 MB I+D on chip per chip  
 Other: None  
 Memory: 768 GB (24 x 32 GB 2Rx8 PC4-2666V-R)  
 Storage: 960 GB SAS SSD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 12 SP3  
 4.4.114-94.11-default  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++  
 Compiler for Linux;  
 Fortran: Version 18.0.0.128 of Intel Fortran  
 Compiler for Linux  
 Parallel: No  
 Firmware: Version 0.3.12 released Feb-2018  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc memory allocator library, version 5.0.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	32	608	83.8	632	80.6	<b>610</b>	<b>83.5</b>	32	501	102	<b>502</b>	<b>102</b>	503	101
502.gcc_r	32	472	95.9	<b>472</b>	<b>95.9</b>	473	95.8	32	399	113	396	114	<b>398</b>	<b>114</b>
505.mcf_r	32	389	133	385	134	<b>388</b>	<b>133</b>	32	<b>392</b>	<b>132</b>	389	133	394	131
520.omnetpp_r	32	<b>642</b>	<b>65.4</b>	643	65.3	639	65.7	32	642	65.4	<b>641</b>	<b>65.5</b>	640	65.6
523.xalancbmk_r	32	281	120	<b>280</b>	<b>121</b>	279	121	32	<b>239</b>	<b>142</b>	239	142	239	141
525.x264_r	32	248	226	<b>248</b>	<b>225</b>	249	225	32	236	238	236	238	<b>236</b>	<b>238</b>
531.deepsjeng_r	32	382	95.9	382	95.9	<b>382</b>	<b>95.9</b>	32	384	95.6	<b>384</b>	<b>95.6</b>	384	95.5
541.leela_r	32	608	87.2	605	87.6	<b>606</b>	<b>87.4</b>	32	597	88.7	600	88.4	<b>598</b>	<b>88.6</b>
548.exchange2_r	32	405	207	405	207	<b>405</b>	<b>207</b>	32	<b>405</b>	<b>207</b>	404	207	405	207
557.xz_r	32	<b>442</b>	<b>78.2</b>	442	78.1	441	78.3	32	443	78.0	441	78.4	<b>443</b>	<b>78.1</b>

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH = "/root/cpu2017/lib/ia32:/root/cpu2017/lib/intel64:/root/cpu2017/je5.0.1-32:/root/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86\_64) targets; jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;

jemalloc: sources available via jemalloc.net

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

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## General Notes (Continued)

```
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
```

## Platform Notes

BIOS settings:

Sub NUMA Cluster Enabled

Virtualization Technology Disabled

System Profile set to Custom

CPU Performance set to Maximum Performance

C States set to Autonomous

C1E Disabled

Uncore Frequency set to Dynamic

Energy Efficiency Policy set to Performance

Memory Patrol Scrub Disabled

Logical Processor Enabled

CPU Interconnect Bus Link Power Management Disabled

PCI ASPM L1 Link Power Management Disabled

Sysinfo program /root/cpu2017/bin/sysinfo

Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f

running on linux-5y3r Mon Mar 12 08:30:03 2018

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Platinum 8156 CPU @ 3.60GHz

4 "physical id"s (chips)

32 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 4

siblings : 8

physical 0: cores 1 5 9 13

physical 1: cores 1 2 5 11

physical 2: cores 2 3 4 10

physical 3: cores 0 5 9 13

From lscpu:

Architecture: x86\_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

CPU(s): 32

On-line CPU(s) list: 0-31

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## Platform Notes (Continued)

```

Thread(s) per core:      2
Core(s) per socket:     4
Socket(s):               4
NUMA node(s):           8
Vendor ID:               GenuineIntel
CPU family:              6
Model:                   85
Model name:              Intel(R) Xeon(R) Platinum 8156 CPU @ 3.60GHz
Stepping:                4
CPU MHz:                 3591.728
BogoMIPS:                7183.45
Virtualization:         VT-x
L1d cache:               32K
L1i cache:               32K
L2 cache:                1024K
L3 cache:                16896K
NUMA node0 CPU(s):      0,8,16,24
NUMA node1 CPU(s):      1,9,17,25
NUMA node2 CPU(s):      2,10,18,26
NUMA node3 CPU(s):      3,11,19,27
NUMA node4 CPU(s):      4,12,20,28
NUMA node5 CPU(s):      5,13,21,29
NUMA node6 CPU(s):      6,14,22,30
NUMA node7 CPU(s):      7,15,23,31
Flags:                   fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
dtherm intel_pt rsb_ctxsw spec_ctrl retpoline kaiser tpr_shadow vnmi flexpriority
ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx
avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt
xsavec xgetbv1 cqm_llc cqm_occup_llc pku ospke

```

```

/proc/cpuinfo cache data
cache size : 16896 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0 8 16 24
node 0 size: 95366 MB
node 0 free: 95191 MB
node 1 cpus: 1 9 17 25
node 1 size: 96763 MB
node 1 free: 96633 MB

```

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## Platform Notes (Continued)

```

node 2 cpus: 2 10 18 26
node 2 size: 96763 MB
node 2 free: 96632 MB
node 3 cpus: 3 11 19 27
node 3 size: 96763 MB
node 3 free: 96628 MB
node 4 cpus: 4 12 20 28
node 4 size: 96763 MB
node 4 free: 96640 MB
node 5 cpus: 5 13 21 29
node 5 size: 96763 MB
node 5 free: 96644 MB
node 6 cpus: 6 14 22 30
node 6 size: 96763 MB
node 6 free: 96655 MB
node 7 cpus: 7 15 23 31
node 7 size: 96761 MB
node 7 free: 96632 MB
node distances:
node  0  1  2  3  4  5  6  7
 0: 10 21 21 21 11 21 21 21
 1: 21 10 21 21 21 11 21 21
 2: 21 21 10 21 21 21 11 21
 3: 21 21 21 10 21 21 21 11
 4: 11 21 21 21 10 21 21 21
 5: 21 11 21 21 21 10 21 21
 6: 21 21 11 21 21 21 10 21
 7: 21 21 21 11 21 21 21 10

```

From /proc/meminfo

MemTotal: 791254448 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d

SUSE Linux Enterprise Server 12 SP3

From /etc/\*release\* /etc/\*version\*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86\_64)

VERSION = 12

PATCHLEVEL = 3

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP3"

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## Platform Notes (Continued)

```

VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="/o:suse:sles:12:sp3"

```

```

uname -a:
Linux linux-5y3r 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Mar 12 08:28

```

SPEC is set to: /root/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       xfs   882G   16G  866G   2% /

```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```

BIOS Dell Inc. 0.3.12 02/06/2018
Memory:
12x 00AD00B300AD HMA84GR7AFR4N-VK 32 GB 2 rank 2666
12x 00AD063200AD HMA84GR7AFR4N-VK 32 GB 2 rank 2666
24x Not Specified Not Specified

```

(End of data from sysinfo program)

## Compiler Version Notes

```

=====
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
525.x264_r(base, peak) 557.xz_r(base, peak)
-----

```

```

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----

```

```

=====
CC 500.perlbench_r(peak) 502.gcc_r(peak)
-----

```

```

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----
=====

```

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## Compiler Version Notes (Continued)

CXXC 520.omnetpp\_r(base) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base)  
541.leela\_r(base)

-----  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
CXXC 520.omnetpp\_r(peak) 523.xalancbmk\_r(peak) 531.deepsjeng\_r(peak)  
541.leela\_r(peak)

-----  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
FC 548.exchange2\_r(base, peak)

-----  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64

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## Base Portability Flags (Continued)

548.exchange2\_r: -DSPEC\_LP64

557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

C++ benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

## Base Other Flags

C benchmarks:

```
-m64 -std=c11
```

C++ benchmarks:

```
-m64
```

Fortran benchmarks:

```
-m64
```

## Peak Compiler Invocation

C benchmarks:

```
icc
```

C++ benchmarks:

```
icpc
```

Fortran benchmarks:

```
ifort
```





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## Peak Portability Flags

```

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

```

## Peak Optimization Flags

C benchmarks:

```

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

```

```

502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

```

```

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib
-ljemalloc

```

```

525.x264_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -fno-alias
-L/usr/local/je5.0.1-64/lib -ljemalloc

```

557.xz\_r: Same as 505.mcf\_r

C++ benchmarks:

```

520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc

```

```

523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3

```

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## Peak Optimization Flags (Continued)

523.xalancbmk\_r (continued):

`-L/usr/local/je5.0.1-32/lib -ljemalloc`

531.deepsjeng\_r: Same as 520.omnetpp\_r

541.leela\_r: Same as 520.omnetpp\_r

Fortran benchmarks:

`-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div`

`-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`

`-L/usr/local/je5.0.1-64/lib -ljemalloc`

## Peak Other Flags

C benchmarks (except as noted below):

`-m64 -std=c11`

502.gcc\_r: `-m32 -std=c11`

C++ benchmarks (except as noted below):

`-m64`

523.xalancbmk\_r: `-m32`

Fortran benchmarks:

`-m64`

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revC.xml>

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