



# SPEC® CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

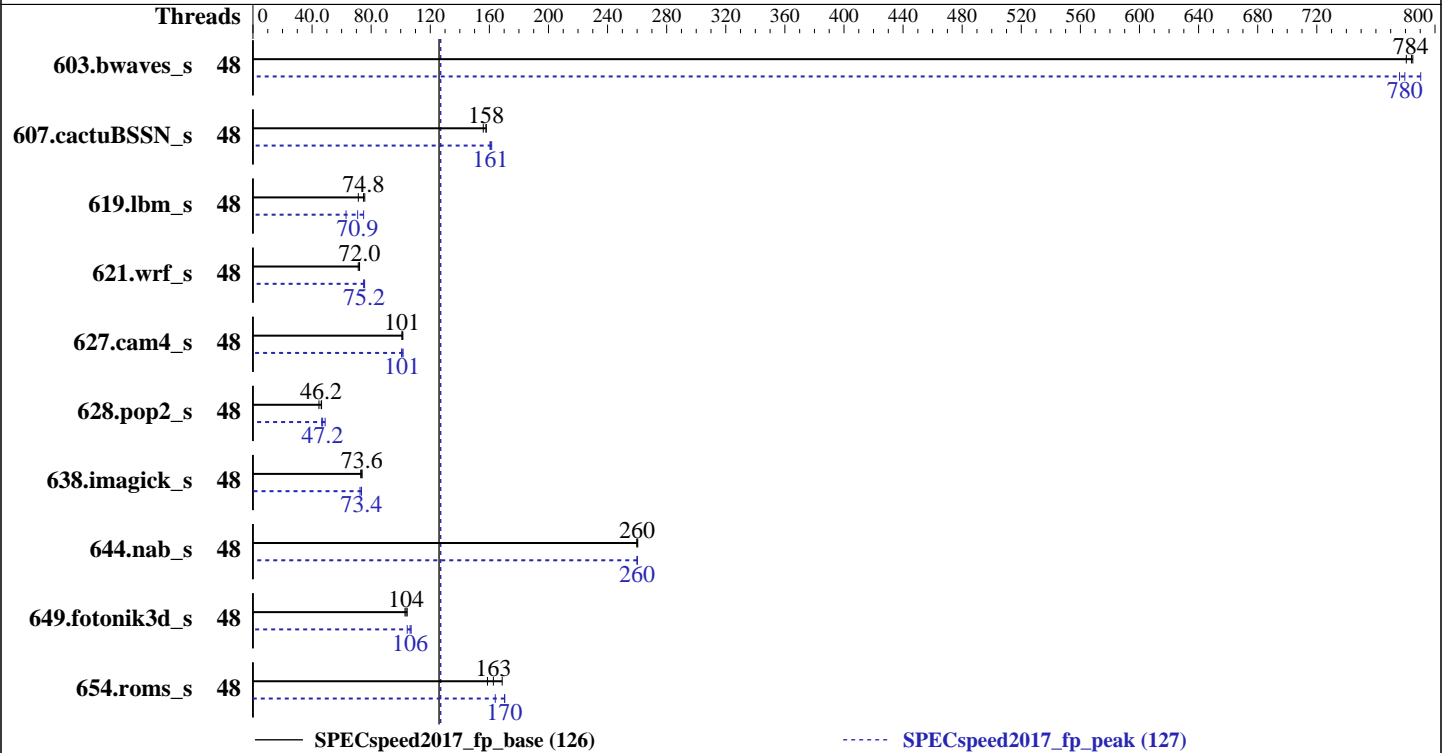
ThinkSystem SR850  
(2.60 GHz, Intel Xeon Gold 6126T)

SPECspeed2017\_fp\_base = 126

SPECspeed2017\_fp\_peak = 127

CPU2017 License: 9017  
Test Sponsor: Lenovo Global Technology  
Tested by: Lenovo Global Technology

Test Date: Aug-2018  
Hardware Availability: Aug-2017  
Software Availability: May-2018



### Hardware

CPU Name: Intel Xeon Gold 6126T  
Max MHz.: 3700  
Nominal: 2600  
Enabled: 48 cores, 4 chips  
Orderable: 2,4 chips  
Cache L1: 32 KB I + 32 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 19.25 MB I+D on chip per chip  
Other: None  
Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R)  
Storage: 1 x 800 GB SAS SSD  
Other: None

### Software

OS: SUSE Linux Enterprise Server 12 SP2 (x86\_64)  
Kernel 4.4.121-92.80-default  
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++  
Compiler for Linux;  
Fortran: Version 18.0.0.128 of Intel Fortran  
Compiler for Linux  
Parallel: Yes  
Firmware: Lenovo BIOS Version TEE123N 1.40 released Jun-2018  
File System: btrfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: None



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR850  
(2.60 GHz, Intel Xeon Gold 6126T)

SPECSpeed2017\_fp\_base = 126

SPECSpeed2017\_fp\_peak = 127

CPU2017 License: 9017  
Test Sponsor: Lenovo Global Technology  
Tested by: Lenovo Global Technology

Test Date: Aug-2018  
Hardware Availability: Aug-2017  
Software Availability: May-2018

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	48	75.2	785	<b><u>75.2</u></b>	<b><u>784</u></b>	75.6	781	48	76.0	776	<b><u>75.7</u></b>	<b><u>780</u></b>	74.7	790
607.cactuBSSN_s	48	107	156	106	158	<b><u>106</u></b>	<b><u>158</u></b>	48	103	162	<b><u>104</u></b>	<b><u>161</u></b>	104	160
619.lbm_s	48	73.5	71.3	69.2	75.7	<b><u>70.0</u></b>	<b><u>74.8</u></b>	48	83.1	63.0	69.9	74.9	<b><u>73.9</u></b>	<b><u>70.9</u></b>
621.wrf_s	48	186	71.3	<b><u>184</u></b>	<b><u>72.0</u></b>	183	72.2	48	177	74.9	175	75.6	<b><u>176</u></b>	<b><u>75.2</u></b>
627.cam4_s	48	87.4	101	87.9	101	<b><u>87.7</u></b>	<b><u>101</u></b>	48	88.1	101	<b><u>87.8</u></b>	<b><u>101</u></b>	87.2	102
628.pop2_s	48	<b><u>257</u></b>	<b><u>46.2</u></b>	266	44.7	256	46.4	48	255	46.5	243	48.8	<b><u>252</u></b>	<b><u>47.2</u></b>
638.imagick_s	48	198	72.9	<b><u>196</u></b>	<b><u>73.6</u></b>	195	73.9	48	<b><u>197</u></b>	<b><u>73.4</u></b>	199	72.5	196	73.5
644.nab_s	48	<b><u>67.2</u></b>	<b><u>260</u></b>	67.3	260	67.1	260	48	67.2	260	<b><u>67.2</u></b>	<b><u>260</u></b>	67.2	260
649.fotonik3d_s	48	<b><u>88.0</u></b>	<b><u>104</u></b>	88.6	103	87.3	104	48	87.3	104	85.1	107	<b><u>85.7</u></b>	<b><u>106</u></b>
654.roms_s	48	99.2	159	93.3	169	<b><u>96.8</u></b>	<b><u>163</u></b>	48	<b><u>92.5</u></b>	<b><u>170</u></b>	92.4	170	96.0	164

SPECSpeed2017\_fp\_base = 126

SPECSpeed2017\_fp\_peak = 127

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
OMP_STACKSIZE = "192M"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:

Choose Operating Mode set to Custom Mode

CPU P-state Control set to None

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR850  
(2.60 GHz, Intel Xeon Gold 6126T)

SPECspeed2017\_fp\_base = 126

SPECspeed2017\_fp\_peak = 127

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2018  
**Hardware Availability:** Aug-2017  
**Software Availability:** May-2018

### Platform Notes (Continued)

Page Policy set to Adaptive  
C-States set to Legacy  
Energy Efficient Turbo set to Disable  
Platform Controlled Type set to Maximum Performance  
Hyper-Threading set to Disable  
DCU Streamer Prefetcher set to Disable  
Trusted Execution Technology set to Enable  
DCA set to Enable  
LLC dead line alloc set to Enable  
Stale A to S set to Enable  
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f  
running on linux-nx6k Wed Aug 1 15:23:00 2018

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 6126T CPU @ 2.60GHz  
4 "physical id"s (chips)  
48 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 12  
siblings : 12  
physical 0: cores 1 2 3 4 5 6 8 9 10 11 12 13  
physical 1: cores 0 2 3 4 5 8 9 10 11 12 13 14  
physical 2: cores 0 2 3 4 5 6 8 9 10 11 12 14  
physical 3: cores 0 1 3 4 5 6 8 9 10 11 12 13

From lscpu:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 48  
On-line CPU(s) list: 0-47  
Thread(s) per core: 1  
Core(s) per socket: 12  
Socket(s): 4  
NUMA node(s): 4  
Vendor ID: GenuineIntel  
CPU family: 6  
Model: 85  
Model name: Intel(R) Xeon(R) Gold 6126T CPU @ 2.60GHz  
Stepping: 4  
CPU MHz: 2593.911

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR850  
(2.60 GHz, Intel Xeon Gold 6126T)

SPECspeed2017\_fp\_base = 126

SPECspeed2017\_fp\_peak = 127

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2018

Hardware Availability: Aug-2017

Software Availability: May-2018

### Platform Notes (Continued)

```

BogoMIPS:          5187.82
Virtualization:    VT-x
L1d cache:         32K
L1i cache:         32K
L2 cache:          1024K
L3 cache:          19712K
NUMA node0 CPU(s): 0-11
NUMA node1 CPU(s): 12-23
NUMA node2 CPU(s): 24-35
NUMA node3 CPU(s): 36-47

```

```

Flags:              fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
dtherm intel_pt rsb_ctxsw spec_ctrl stibp ssbd retpoline kaiser tpr_shadow vmi
flexpriority ept vpid fsgsbase tsc_adjust bmil hle avx2 smep bmi2 erms invpcid rtm
cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl
xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

```

```

/proc/cpuinfo cache data
cache size : 19712 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11
node 0 size: 386657 MB
node 0 free: 386107 MB
node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23
node 1 size: 387055 MB
node 1 free: 386285 MB
node 2 cpus: 24 25 26 27 28 29 30 31 32 33 34 35
node 2 size: 387055 MB
node 2 free: 386735 MB
node 3 cpus: 36 37 38 39 40 41 42 43 44 45 46 47
node 3 size: 387052 MB
node 3 free: 384515 MB
node distances:
node  0  1  2  3
 0:  10  21  21  31
 1:  21  10  31  21
 2:  21  31  10  21
 3:  31  21  21  10

```

From /proc/meminfo

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR850  
(2.60 GHz, Intel Xeon Gold 6126T)

SPECspeed2017\_fp\_base = 126

SPECspeed2017\_fp\_peak = 127

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2018  
**Hardware Availability:** Aug-2017  
**Software Availability:** May-2018

### Platform Notes (Continued)

MemTotal: 1584968872 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86\_64)

VERSION = 12

PATCHLEVEL = 2

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP2"

VERSION\_ID="12.2"

PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP2"

ID="sles"

ANSI\_COLOR="0;32"

CPE\_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:

Linux linux-nx6k 4.4.121-92.80-default #1 SMP Mon May 21 14:40:10 UTC 2018 (2afdd00)  
x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Aug 1 10:10

SPEC is set to: /home/cpu2017.1.1.0.2.ic18.0

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	btrfs	744G	370G	372G	50%	/home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[TEE123N-1.40]- 06/12/2018

Memory:

48x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)

### Compiler Version Notes

=====  
CC 619.lbm\_s(base) 638.imagick\_s(base, peak) 644.nab\_s(base, peak)  
=====

icc (ICC) 18.0.0 20170811

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR850  
(2.60 GHz, Intel Xeon Gold 6126T)

SPECspeed2017\_fp\_base = 126

SPECspeed2017\_fp\_peak = 127

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2018  
**Hardware Availability:** Aug-2017  
**Software Availability:** May-2018

### Compiler Version Notes (Continued)

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====  
CC 619.lbm\_s(peak)  
-----

icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
FC 607.cactuBSSN\_s(base)  
-----

icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
FC 607.cactuBSSN\_s(peak)  
-----

icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
FC 603.bwaves\_s(base) 649.fotonik3d\_s(base) 654.roms\_s(base)  
-----

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
FC 603.bwaves\_s(peak) 649.fotonik3d\_s(peak) 654.roms\_s(peak)  
-----

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

SPECspeed2017\_fp\_base = 126

ThinkSystem SR850  
(2.60 GHz, Intel Xeon Gold 6126T)

SPECspeed2017\_fp\_peak = 127

CPU2017 License: 9017

Test Date: Aug-2018

Test Sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: May-2018

## Compiler Version Notes (Continued)

CC 621.wrf\_s(base) 627.cam4\_s(base, peak) 628.pop2\_s(base)

-----  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
CC 621.wrf\_s(peak) 628.pop2\_s(peak)

-----  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:

icc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactuBSSN\_s: -DSPEC\_LP64  
619.lbm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
627.cam4\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
628.pop2\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
-assume byterecl  
638.imagick\_s: -DSPEC\_LP64  
644.nab\_s: -DSPEC\_LP64  
649.fotonik3d\_s: -DSPEC\_LP64

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed2017\_fp\_base = 126

ThinkSystem SR850  
(2.60 GHz, Intel Xeon Gold 6126T)

SPECspeed2017\_fp\_peak = 127

CPU2017 License: 9017

Test Date: Aug-2018

Test Sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: May-2018

## Base Portability Flags (Continued)

654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC\_OPENMP

Fortran benchmarks:

-DSPEC\_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp  
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC\_OPENMP  
-nostandard-realloc-lhs -align array32byte

Benchmarks using Fortran, C, and C++:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC\_OPENMP  
-nostandard-realloc-lhs -align array32byte

## Base Other Flags

C benchmarks:

-m64 -std=c11

Fortran benchmarks:

-m64

Benchmarks using both Fortran and C:

-m64 -std=c11

Benchmarks using Fortran, C, and C++:

-m64 -std=c11





# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

ThinkSystem SR850  
(2.60 GHz, Intel Xeon Gold 6126T)

SPECspeed2017\_fp\_base = 126

SPECspeed2017\_fp\_peak = 127

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2018

Hardware Availability: Aug-2017

Software Availability: May-2018

## Peak Compiler Invocation

C benchmarks:

icc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP
```

```
638.imagick_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP
```

644.nab\_s: Same as 638.imagick\_s

Fortran benchmarks:

```
-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte
```

Benchmarks using both Fortran and C:

```
621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
```

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed2017\_fp\_base = 126

ThinkSystem SR850  
(2.60 GHz, Intel Xeon Gold 6126T)

SPECspeed2017\_fp\_peak = 127

CPU2017 License: 9017

Test Date: Aug-2018

Test Sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: May-2018

## Peak Optimization Flags (Continued)

621.wrf\_s (continued):

```
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte
```

627.cam4\_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

```
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp  
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte
```

628.pop2\_s: Same as 621.wrf\_s

Benchmarks using Fortran, C, and C++:

```
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch  
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3  
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs  
-align array32byte
```

## Peak Other Flags

C benchmarks:

```
-m64 -std=c11
```

Fortran benchmarks:

```
-m64
```

Benchmarks using both Fortran and C:

```
-m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c11
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-H.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-H.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.2 on 2018-08-01 03:22:59-0400.

Report generated on 2018-10-31 18:41:18 by CPU2017 PDF formatter v6067.

Originally published on 2018-08-21.