SPEC® CPU2017 Floating Point Rate Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD530
(2.60 GHz, Intel Xeon Gold 6126T)

SPECrate2017_fp_base = 143
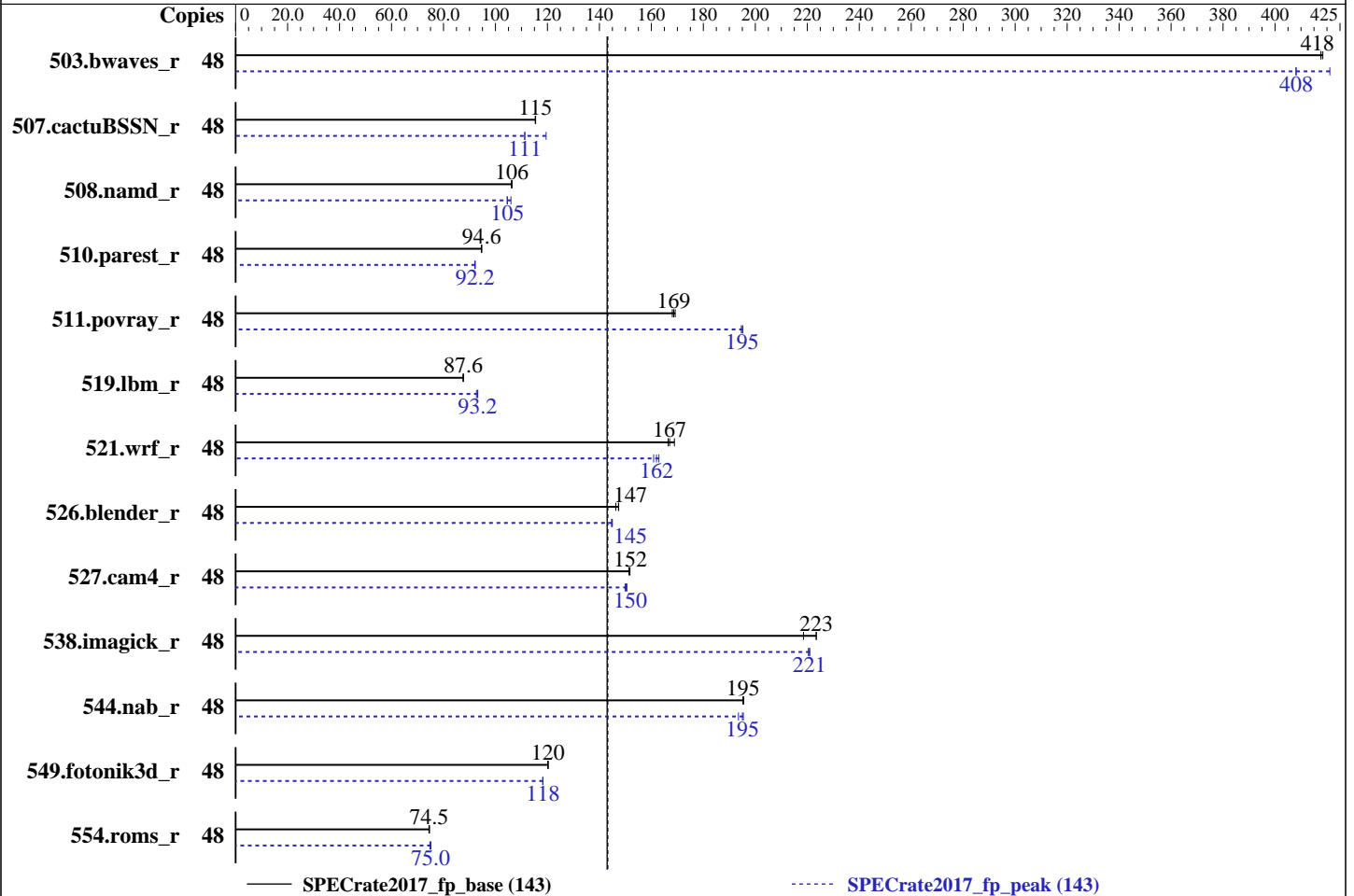
SPECrate2017_fp_peak = 143

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jul-2018
Hardware Availability: Aug-2017
Software Availability: May-2018

Benchmark results (Copies = 48):

Benchmark	base	peak
503.bwaves_r	418	408
507.cactuBSSN_r	115	111
508.namd_r	106	105
510.parest_r	94.6	92.2
511.povray_r	169	195
519.lbm_r	87.6	93.2
521.wrf_r	167	162
526.blender_r	147	145
527.cam4_r	152	150
538.imagick_r	223	221
544.nab_r	195	195
549.fotonik3d_r	120	118
554.roms_r	74.5	75.0

——— SPECrate2017_fp_base (143)   ------ SPECrate2017_fp_peak (143)

Hardware

CPU Name: Intel Xeon Gold 6126T
 Max MHz.: 3700
 Nominal: 2600
Enabled: 24 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 19.25 MB I+D on chip per chip
 Other: None
Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)
Storage: 1 x 800 GB SAS SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 12 SP2 (x86_64)
 Kernel 4.4.121-92.80-default
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++
 Compiler for Linux;
 Fortran: Version 18.0.0.128 of Intel Fortran
 Compiler for Linux
Parallel: No
Firmware: Lenovo BIOS Version TEE123N 1.40 released Jun-2018
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None

SPEC CPU2017 Floating Point Rate Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD530
(2.60 GHz, Intel Xeon Gold 6126T)

SPECrate2017_fp_base =   143

SPECrate2017_fp_peak =   143

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jul-2018
Hardware Availability: Aug-2017
Software Availability: May-2018

Results Table

Benchmark	Base						Peak					
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	48	1150	418	**1152**	**418**	1153	418	48	1143	421	**1179**	**408**	1180	408
507.cactuBSSN_r	48	527	115	526	115	**526**	**115**	48	509	119	**546**	**111**	546	111
508.namd_r	48	429	106	**429**	**106**	429	106	48	430	106	436	104	**436**	**105**
510.parest_r	48	**1327**	**94.6**	1325	94.7	1328	94.6	48	1362	92.2	1365	92.0	**1362**	**92.2**
511.povray_r	48	667	168	**665**	**169**	663	169	48	574	195	**575**	**195**	576	195
519.lbm_r	48	**578**	**87.6**	577	87.7	578	87.6	48	546	92.7	543	93.2	**543**	**93.2**
521.wrf_r	48	**644**	**167**	637	169	646	166	48	668	161	660	163	**664**	**162**
526.blender_r	48	500	146	**496**	**147**	496	147	48	505	145	**505**	**145**	504	145
527.cam4_r	48	555	151	**554**	**152**	553	152	48	558	151	**559**	**150**	560	150
538.imagick_r	48	546	219	534	223	**534**	**223**	48	540	221	542	220	**541**	**221**
544.nab_r	48	413	196	**414**	**195**	414	195	48	413	195	**414**	**195**	418	193
549.fotonik3d_r	48	**1556**	**120**	1557	120	1555	120	48	1581	118	1583	118	**1582**	**118**
554.roms_r	48	1025	74.4	1022	74.6	**1024**	**74.5**	48	**1017**	**75.0**	1015	75.2	1020	74.8
SPECrate2017_fp_base =		**143**											
SPECrate2017_fp_peak =		**143**											

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.4
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3>   /proc/sys/vm/drop_caches
 runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.

(Continued on next page)

SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD530
(2.60 GHz, Intel Xeon Gold 6126T)

SPECrate2017_fp_base = 143

SPECrate2017_fp_peak = 143

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jul-2018
Hardware Availability: Aug-2017
Software Availability: May-2018

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Platform Notes

```
BIOS configuration:
Choose Operating Mode set to Maximum Performance
SNC set to Enable
Hardware Prefetcher set to Disable
MONITOR/MWAIT set to Enable
Execute Disable Bit set to Disable
Intel Virtualization Technology set to Disable
DCA set to Enable
 Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
 Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
 running on linux-6zlr Fri Jul 20 09:31:44 2018

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 6126T CPU @ 2.60GHz
       2  "physical id"s (chips)
       48 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 12
       siblings  : 24
       physical 0: cores 0 2 3 4 5 8 9 10 11 12 13 14
       physical 1: cores 0 1 2 4 5 6 8 9 10 11 13 14

 From lscpu:
      Architecture:        x86_64
      CPU op-mode(s):      32-bit, 64-bit
      Byte Order:          Little Endian
      CPU(s):              48
      On-line CPU(s) list: 0-47
      Thread(s) per core:  2
      Core(s) per socket:  12
      Socket(s):           2
      NUMA node(s):        4
      Vendor ID:           GenuineIntel
      CPU family:          6
```

(Continued on next page)

Standard Performance Evaluation Corporation (info@spec.org)   https://www.spec.org/

Lenovo Global Technology

SPECrate2017_fp_base =   143

ThinkSystem SD530
(2.60 GHz, Intel Xeon Gold 6126T)

SPECrate2017_fp_peak =   143

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jul-2018

Hardware Availability: Aug-2017

Software Availability: May-2018

Platform Notes (Continued)

```
         Model:                 85
         Model name:            Intel(R) Xeon(R) Gold 6126T CPU @ 2.60GHz
         Stepping:              4
         CPU MHz:               2593.912
         BogoMIPS:              5187.82
         Virtualization:        VT-x
         L1d cache:             32K
         L1i cache:             32K
         L2 cache:              1024K
         L3 cache:              19712K
         NUMA node0 CPU(s):     0-2,5-7,24-26,29-31
         NUMA node1 CPU(s):     3,4,8-11,27,28,32-35
         NUMA node2 CPU(s):     12-14,18-20,36-38,42-44
         NUMA node3 CPU(s):     15-17,21-23,39-41,45-47
         Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
   pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
   lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
   aperfmperf eagerfpu pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
   xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
   avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts dtherm
   intel_pt rsb_ctxsw spec_ctrl stibp ssbd retpoline kaiser tpr_shadow vnmi
   flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
   cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl
   xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

 /proc/cpuinfo cache data
   cache size : 19712 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
   available: 4 nodes (0-3)
   node 0 cpus: 0 1 2 5 6 7 24 25 26 29 30 31
   node 0 size: 96355 MB
   node 0 free: 96042 MB
   node 1 cpus: 3 4 8 9 10 11 27 28 32 33 34 35
   node 1 size: 96751 MB
   node 1 free: 96519 MB
   node 2 cpus: 12 13 14 18 19 20 36 37 38 42 43 44
   node 2 size: 96751 MB
   node 2 free: 96532 MB
   node 3 cpus: 15 16 17 21 22 23 39 40 41 45 46 47
   node 3 size: 96749 MB
   node 3 free: 96559 MB
   node distances:
   node   0   1   2   3
     0:  10  11  21  21
     1:  11  10  21  21
```

(Continued on next page)

Lenovo Global Technology

ThinkSystem SD530
(2.60 GHz, Intel Xeon Gold 6126T)

SPECrate2017_fp_base =   143

SPECrate2017_fp_peak =   143

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jul-2018

Hardware Availability: Aug-2017

Software Availability: May-2018

Platform Notes (Continued)

```
     2:   21   21   10   11
     3:   21   21   11   10

From /proc/meminfo
   MemTotal:       395885772 kB
   HugePages_Total:        0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 2
      # This file is deprecated and will be removed in a future service pack or release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP2"
      VERSION_ID="12.2"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
   Linux linux-6zlr 4.4.121-92.80-default #1 SMP Mon May 21 14:40:10 UTC 2018 (2afdd00)
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 20 09:30

SPEC is set to: /home/cpu2017.1.0.2.ic18.0
   Filesystem      Type   Size   Used  Avail Use% Mounted on
   /dev/sda3       xfs    744G    33G   712G   5% /

Additional information from dmidecode follows.  WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   BIOS Lenovo -[TEE123N-1.40]- 06/12/2018
   Memory:
    4x NO DIMM NO DIMM
    12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)
```

Lenovo Global Technology

ThinkSystem SD530
(2.60 GHz, Intel Xeon Gold 6126T)

SPECrate2017_fp_base =   143

SPECrate2017_fp_peak =   143

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jul-2018
Hardware Availability: Aug-2017
Software Availability: May-2018

Compiler Version Notes

```
==============================================================================
  CC   519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
CC    519.lbm_r(peak) 544.nab_r(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
  CXXC 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
CXXC 508.namd_r(peak) 510.parest_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
  CC   511.povray_r(base) 526.blender_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
CC    511.povray_r(peak) 526.blender_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
```

(Continued on next page)

Lenovo Global Technology

SPECrate2017_fp_base = 143

ThinkSystem SD530
(2.60 GHz, Intel Xeon Gold 6126T)

SPECrate2017_fp_peak = 143

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jul-2018
Hardware Availability: Aug-2017
Software Availability: May-2018

Compiler Version Notes (Continued)

```
  FC  507.cactuBSSN_r(base)
-------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
-------------------------------------------------------------------------------


===============================================================================
FC   507.cactuBSSN_r(peak)
-------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
-------------------------------------------------------------------------------


===============================================================================
 FC  503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)
-------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
-------------------------------------------------------------------------------


===============================================================================
FC   554.roms_r(peak)
-------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
-------------------------------------------------------------------------------


===============================================================================
 CC  521.wrf_r(base) 527.cam4_r(base)
-------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
-------------------------------------------------------------------------------


===============================================================================
CC   521.wrf_r(peak) 527.cam4_r(peak)
-------------------------------------------------------------------------------
```

(Continued on next page)

SPEC CPU2017 Floating Point Rate Result

Lenovo Global Technology

ThinkSystem SD530
(2.60 GHz, Intel Xeon Gold 6126T)

SPECrate2017_fp_base =   143

SPECrate2017_fp_peak =   143

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jul-2018
Hardware Availability: Aug-2017
Software Availability: May-2018

Compiler Version Notes (Continued)

```
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
```
icc -m64 -std=c11
```

C++ benchmarks:
```
icpc -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using both C and C++:
```
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:
```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

Base Portability Flags

503.bwaves_r: `-DSPEC_LP64`
507.cactuBSSN_r: `-DSPEC_LP64`
508.namd_r: `-DSPEC_LP64`
510.parest_r: `-DSPEC_LP64`
511.povray_r: `-DSPEC_LP64`
519.lbm_r: `-DSPEC_LP64`
521.wrf_r: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
526.blender_r: `-DSPEC_LP64 -DSPEC_LINUX -funsigned-char`
527.cam4_r: `-DSPEC_LP64 -DSPEC_CASE_FLAG`
538.imagick_r: `-DSPEC_LP64`
544.nab_r: `-DSPEC_LP64`
549.fotonik3d_r: `-DSPEC_LP64`
554.roms_r: `-DSPEC_LP64`

Lenovo Global Technology

ThinkSystem SD530
(2.60 GHz, Intel Xeon Gold 6126T)

SPECrate2017_fp_base =   143

SPECrate2017_fp_peak =   143

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jul-2018
Hardware Availability: Aug-2017
Software Availability: May-2018

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
```

Benchmarks using both Fortran and C:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
```

Benchmarks using both C and C++:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3
```

Benchmarks using Fortran, C, and C++:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
```

Peak Compiler Invocation

C benchmarks:
```
icc -m64 -std=c11
```

C++ benchmarks:
```
icpc -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using both C and C++:
```
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:
```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD530
(2.60 GHz, Intel Xeon Gold 6126T)

SPECrate2017_fp_base =   143

SPECrate2017_fp_peak =   143

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jul-2018
Hardware Availability: Aug-2017
Software Availability: May-2018

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

544.nab_r: Same as 519.lbm_r

C++ benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3
-nostandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

(Continued on next page)

Lenovo Global Technology

ThinkSystem SD530
(2.60 GHz, Intel Xeon Gold 6126T)

SPECrate2017_fp_base =   143

SPECrate2017_fp_peak =   143

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jul-2018
Hardware Availability: Aug-2017
Software Availability: May-2018

Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

```
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.html
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-G.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-G.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-07-19 21:31:43-0400.
Report generated on 2018-10-31 18:42:58 by CPU2017 PDF formatter v6067.
Originally published on 2018-08-21.