



# SPEC® CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate2017\_fp\_base = 50.9

PowerEdge C6420 (Intel Xeon Silver 4112, 2.60 GHz)

SPECrate2017\_fp\_peak = Not Run

CPU2017 License: 55

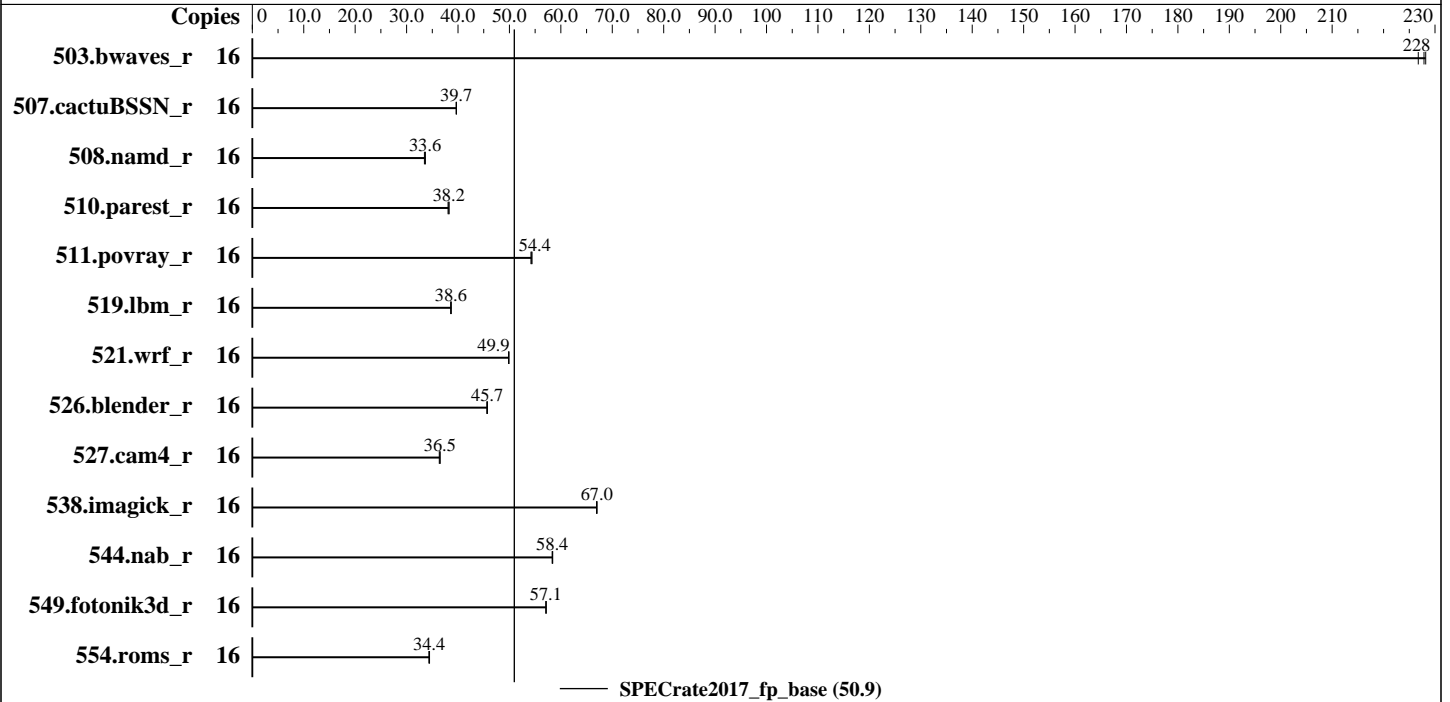
Test Date: Dec-2017

Test Sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Sep-2017



## Hardware

CPU Name: Intel Xeon Silver 4112  
 Max MHz.: 3000  
 Nominal: 2600  
 Enabled: 8 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 8.25 MB I+D on chip per chip  
 Other: None  
 Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
 Storage: 480GB SATA SSD  
 Other: None

## Software

OS: CentOS Linux release 7.4.1708 (Core)  
 3.10.0-693.5.2.el7.x86\_64  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++  
 Compiler for Linux;  
 Fortran: Version 18.0.0.128 of Intel Fortran  
 Compiler for Linux  
 Parallel: No  
 Firmware: Version 1.0.8 released Jul-2017  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate2017\_fp\_base = 50.9

PowerEdge C6420 (Intel Xeon Silver 4112, 2.60 GHz)

SPECrate2017\_fp\_peak = Not Run

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Dec-2017  
Hardware Availability: Sep-2017  
Software Availability: Sep-2017

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	16	<b>704</b>	<b>228</b>	708	227	703	228							
507.cactuBSSN_r	16	510	39.7	<b>511</b>	<b>39.7</b>	511	39.7							
508.namd_r	16	454	33.5	452	33.6	<b>452</b>	<b>33.6</b>							
510.parest_r	16	<b>1095</b>	<b>38.2</b>	1100	38.1	1094	38.2							
511.povray_r	16	<b>687</b>	<b>54.4</b>	687	54.4	690	54.2							
519.lbm_r	16	<b>437</b>	<b>38.6</b>	436	38.7	437	38.6							
521.wrf_r	16	719	49.9	719	49.9	<b>719</b>	<b>49.9</b>							
526.blender_r	16	<b>533</b>	<b>45.7</b>	533	45.7	534	45.6							
527.cam4_r	16	<b>767</b>	<b>36.5</b>	767	36.5	770	36.4							
538.imagick_r	16	594	67.0	<b>594</b>	<b>67.0</b>	594	66.9							
544.nab_r	16	461	58.4	462	58.3	<b>461</b>	<b>58.4</b>							
549.fotonik3d_r	16	1092	57.1	1091	57.1	<b>1092</b>	<b>57.1</b>							
554.roms_r	16	<b>739</b>	<b>34.4</b>	738	34.5	740	34.4							

SPECrate2017\_fp\_base = 50.9

SPECrate2017\_fp\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH = "/root/cpu2017/lib/ia32:/root/cpu2017/lib/intel64:/root/cpu2017/je5.0.1-32:/root/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate2017\_fp\_base = 50.9

PowerEdge C6420 (Intel Xeon Silver 4112, 2.60 GHz)

SPECrate2017\_fp\_peak = Not Run

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Dec-2017

Hardware Availability: Sep-2017

Software Availability: Sep-2017

## General Notes (Continued)

No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, <http://www.spec.org/osg/policy.html>

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

## Platform Notes

BIOS settings:

Virtualization Technology disabled

System Profile set to Custom

CPU Power Management set to Maximum Performance

Memory Frequency set to Maximum Performance

Turbo Boost enabled

C States disabled

Memory Patrol Scrub disabled

PCI ASPM L1 Link Power Management disabled

    Sysinfo program /root/cpu2017/bin/sysinfo

    Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f

    running on localhost.localdomain Wed Dec 6 00:04:06 2017

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

    model name : Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz

    2 "physical id"s (chips)

    16 "processors"

    cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

        cpu cores : 4

        siblings : 8

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate2017\_fp\_base = 50.9

PowerEdge C6420 (Intel Xeon Silver 4112, 2.60 GHz)

SPECrate2017\_fp\_peak = Not Run

CPU2017 License: 55

Test Date: Dec-2017

Test Sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Sep-2017

## Platform Notes (Continued)

physical 0: cores 1 2 4 5  
physical 1: cores 1 2 4 5

From lscpu:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:             Little Endian
CPU(s):                 16
On-line CPU(s) list:   0-15
Thread(s) per core:    2
Core(s) per socket:    4
Socket(s):              2
NUMA node(s):          2
Vendor ID:              GenuineIntel
CPU family:             6
Model:                  85
Model name:             Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
Stepping:               4
CPU MHz:                2600.000
BogoMIPS:               5200.00
Virtualization:         VT-x
L1d cache:              32K
L1i cache:              32K
L2 cache:               1024K
L3 cache:               8448K
NUMA node0 CPU(s):     0,2,4,6,8,10,12,14
NUMA node1 CPU(s):     1,3,5,7,9,11,13,15
```

```
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma
cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 intel_pt
tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb
avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc
cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts
```

```
/proc/cpuinfo cache data
cache size : 8448 KB
```

```
From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14
node 0 size: 96965 MB
node 0 free: 94288 MB
```

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate2017\_fp\_base = 50.9

PowerEdge C6420 (Intel Xeon Silver 4112, 2.60 GHz)

SPECrate2017\_fp\_peak = Not Run

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Dec-2017

Hardware Availability: Sep-2017

Software Availability: Sep-2017

## Platform Notes (Continued)

```
node 1 cpus: 1 3 5 7 9 11 13 15
node 1 size: 98304 MB
node 1 free: 95658 MB
node distances:
node 0 1
  0: 10 21
  1: 21 10
```

From /proc/meminfo

```
MemTotal: 196690028 kB
HugePages_Total: 128
Hugepagesize: 2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
centos-release: CentOS Linux release 7.4.1708 (Core)
centos-release-upstream: Derived from Red Hat Enterprise Linux 7.4 (Source)
os-release:
  NAME="CentOS Linux"
  VERSION="7 (Core)"
  ID="centos"
  ID_LIKE="rhel fedora"
  VERSION_ID="7"
  PRETTY_NAME="CentOS Linux 7 (Core)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:centos:centos:7"
redhat-release: CentOS Linux release 7.4.1708 (Core)
system-release: CentOS Linux release 7.4.1708 (Core)
system-release-cpe: cpe:/o:centos:centos:7
```

uname -a:

```
Linux localhost.localdomain 3.10.0-693.5.2.el7.x86_64 #1 SMP Fri Oct 20 20:32:50 UTC
2017 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Dec 4 23:59

SPEC is set to: /root/cpu2017

```
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 433G 18G 415G 5% /
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.0.8 07/12/2017

Memory:

```
12x 002C00B3002C 18ASF2G72PDZ-2G6D1 16 GB 2 rank 2666, configured at 2400
4x Not Specified Not Specified
```

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate2017\_fp\_base = 50.9

PowerEdge C6420 (Intel Xeon Silver 4112, 2.60 GHz)

SPECrate2017\_fp\_peak = Not Run

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Dec-2017

Hardware Availability: Sep-2017

Software Availability: Sep-2017

## Platform Notes (Continued)

(End of data from sysinfo program)

## Compiler Version Notes

=====  
CC 519.lbm\_r(base) 538.imagick\_r(base) 544.nab\_r(base)  
-----

icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
CXXC 508.namd\_r(base) 510.parest\_r(base)  
-----

icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
CC 511.povray\_r(base) 526.blender\_r(base)  
-----

icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
FC 507.cactuBSSN\_r(base)  
-----

icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
FC 503.bwaves\_r(base) 549.fotonik3d\_r(base) 554.roms\_r(base)  
-----

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate2017\_fp\_base = 50.9

PowerEdge C6420 (Intel Xeon Silver 4112, 2.60 GHz)

SPECrate2017\_fp\_peak = Not Run

CPU2017 License: 55

Test Date: Dec-2017

Test Sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Sep-2017

## Compiler Version Notes (Continued)

=====  
CC 521.wrf\_r(base) 527.cam4\_r(base)  
=====

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
=====

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using both C and C++:

icpc icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate2017\_fp\_base = 50.9

PowerEdge C6420 (Intel Xeon Silver 4112, 2.60 GHz)

SPECrate2017\_fp\_peak = Not Run

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Dec-2017

Hardware Availability: Sep-2017

Software Availability: Sep-2017

## Base Portability Flags (Continued)

554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

## Base Other Flags

C benchmarks:

-m64 -std=c11

C++ benchmarks:

-m64

Fortran benchmarks:

-m64

Benchmarks using both Fortran and C:

-m64 -std=c11

(Continued on next page)





# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate2017\_fp\_base = 50.9

PowerEdge C6420 (Intel Xeon Silver 4112, 2.60 GHz)

SPECrate2017\_fp\_peak = Not Run

CPU2017 License: 55

Test Date: Dec-2017

Test Sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Sep-2017

## Base Other Flags (Continued)

Benchmarks using both C and C++:

-m64 -std=c11

Benchmarks using Fortran, C, and C++:

-m64 -std=c11

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revD.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.2 on 2017-12-06 01:04:05-0500.

Report generated on 2018-10-31 16:18:45 by CPU2017 PDF formatter v6067.

Originally published on 2018-02-27.