



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

M Computers s.r.o.

HPC S2600WFT
(1.80 GHz, Intel Xeon Silver 4108)

SPECrate®2017_fp_base = 70.6

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 4204

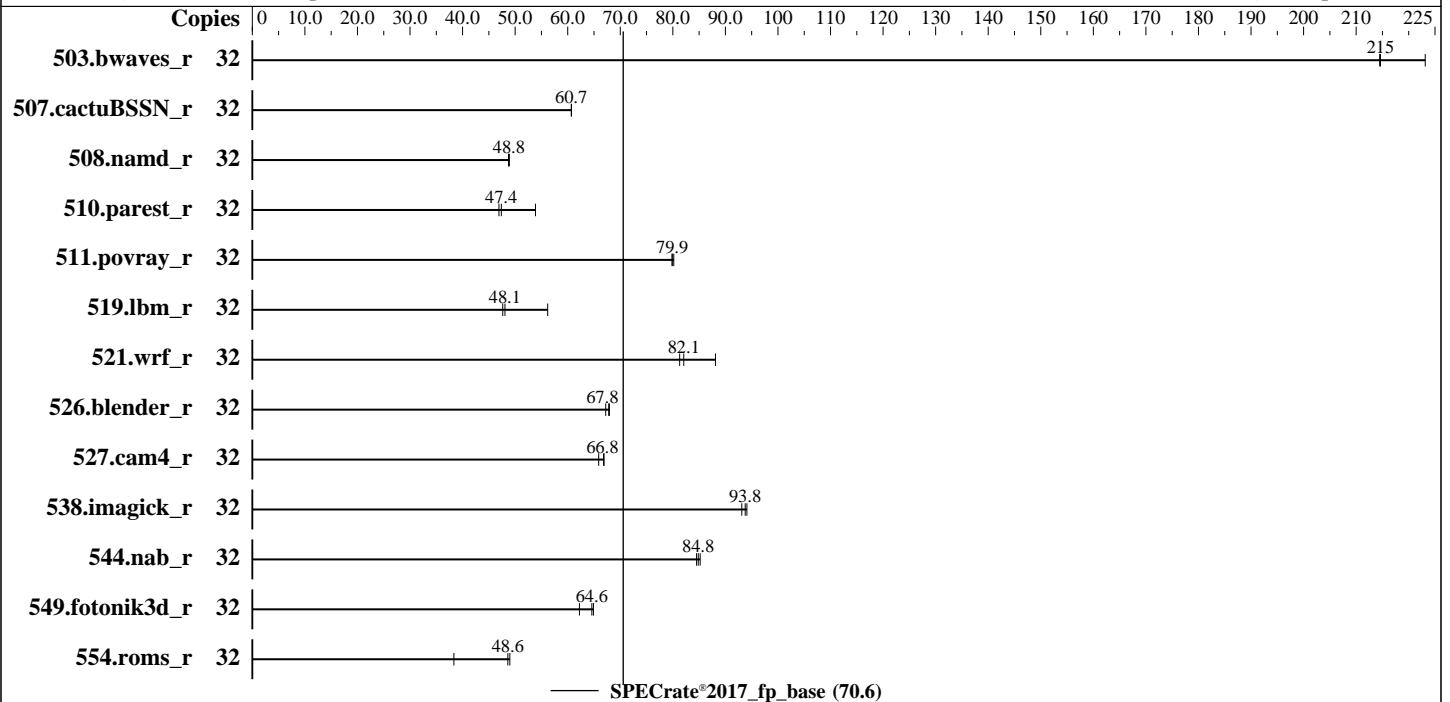
Test Sponsor: M Computers s.r.o.

Tested by: M Computers s.r.o.

Test Date: Dec-2017

Hardware Availability: Oct-2017

Software Availability: Sep-2017



Hardware

CPU Name: Intel Xeon Silver 4108
 Max MHz: 3000
 Nominal: 1800
 Enabled: 16 cores, 2 chips, 2 threads/core
 Orderable: 1, 2 chip(s)
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 11 MB I+D on chip per chip
 Other: None
 Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)
 Storage: 1 x 480 GB SATA SSD
 Other: None

Software

OS: SUSE Linux Enterprise Server 12 SP2 4.4.21-69-default
 Compiler: C/C++: Version 18.0.1 of Intel C/C++ Compiler for Linux;
 Fortran: Version 18.0.1 of Intel Fortran Compiler for Linux
 Parallel: No
 Firmware: Intel Version SE5C620.86B.00.01.0009.101920170742 released Oct-2017
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: None
 Power Management: --



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

M Computers s.r.o.

HPC S2600WFT
(1.80 GHz, Intel Xeon Silver 4108)

SPECrate®2017_fp_base = 70.6

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 4204
Test Sponsor: M Computers s.r.o.
Tested by: M Computers s.r.o.

Test Date: Dec-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	32	<u>1495</u>	<u>215</u>	1496	214	1438	223							
507.cactuBSSN_r	32	667	60.7	667	60.7	<u>667</u>	<u>60.7</u>							
508.namd_r	32	<u>623</u>	<u>48.8</u>	622	48.9	624	48.7							
510.parest_r	32	<u>1767</u>	<u>47.4</u>	1784	46.9	1554	53.9							
511.povray_r	32	932	80.2	<u>935</u>	<u>79.9</u>	936	79.9							
519.lbm_r	32	708	47.6	600	56.2	<u>702</u>	<u>48.1</u>							
521.wrf_r	32	882	81.3	<u>873</u>	<u>82.1</u>	814	88.1							
526.blender_r	32	725	67.2	<u>719</u>	<u>67.8</u>	717	67.9							
527.cam4_r	32	849	65.9	836	66.9	<u>838</u>	<u>66.8</u>							
538.imagick_r	32	846	94.1	<u>849</u>	<u>93.8</u>	855	93.1							
544.nab_r	32	632	85.2	637	84.5	<u>635</u>	<u>84.8</u>							
549.fotonik3d_r	32	2004	62.2	1922	64.9	<u>1931</u>	<u>64.6</u>							
554.roms_r	32	1326	38.3	1038	49.0	<u>1046</u>	<u>48.6</u>							

SPECrate®2017_fp_base = 70.6

SPECrate®2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "\$/opt/intel/compilers_and_libraries/linux/lib/ia32_lin
:\$/opt/intel/compilers_and_libraries/linux/lib/intel64_lin"
Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

M Computers s.r.o.

SPECrate®2017_fp_base = 70.6

HPC S2600WFT
(1.80 GHz, Intel Xeon Silver 4108)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 4204
Test Sponsor: M Computers s.r.o.
Tested by: M Computers s.r.o.

Test Date: Dec-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017

General Notes (Continued)

is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, <http://www.spec.org/osg/policy.html>

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Platform Notes

BIOS Configuration:
Patrol Scrub=Disabled
CPU and Power Performance Policy=Performance
Set Fan Profile=Performance
Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on taborlin2 Sat Dec 30 15:52:05 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4108 CPU @ 1.80GHz
2 "physical id"s (chips)
32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

M Computers s.r.o.

HPC S2600WFT
(1.80 GHz, Intel Xeon Silver 4108)

SPECrate®2017_fp_base = 70.6

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 4204
Test Sponsor: M Computers s.r.o.
Tested by: M Computers s.r.o.

Test Date: Dec-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017

Platform Notes (Continued)

```

Byte Order:                Little Endian
CPU(s):                    32
On-line CPU(s) list:      0-31
Thread(s) per core:       2
Core(s) per socket:       8
Socket(s):                 2
NUMA node(s):             2
Vendor ID:                 GenuineIntel
CPU family:                6
Model:                     85
Model name:               Intel(R) Xeon(R) Silver 4108 CPU @ 1.80GHz
Stepping:                  4
CPU MHz:                   1000.000
CPU max MHz:              1801.0000
CPU min MHz:              800.0000
BogoMIPS:                 3591.57
Virtualization:           VT-x
L1d cache:                 32K
L1i cache:                 32K
L2 cache:                  1024K
L3 cache:                  11264K
NUMA node0 CPU(s):        0-7,16-23
NUMA node1 CPU(s):        8-15,24-31
Flags:                    fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx fl6c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

```

```

/proc/cpuinfo cache data
cache size : 11264 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
node 0 size: 192872 MB
node 0 free: 192108 MB
node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
node 1 size: 193368 MB
node 1 free: 192661 MB
node distances:
node  0  1

```

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

M Computers s.r.o.

HPC S2600WFT
(1.80 GHz, Intel Xeon Silver 4108)

SPECrate®2017_fp_base = 70.6

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 4204
Test Sponsor: M Computers s.r.o.
Tested by: M Computers s.r.o.

Test Date: Dec-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017

Platform Notes (Continued)

```
0: 10 21
1: 21 10
```

```
From /proc/meminfo
MemTotal:      395510112 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
```

```
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:
Linux taborlin2 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67) x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Dec 30 15:50
```

```
SPEC is set to: /spec2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda1        xfs   400G   67G  334G  17% /
```

Additional information from dmidecode follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Intel Corporation SE5C620.86B.00.01.0009.101920170742 10/19/2017

Memory:

24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

M Computers s.r.o.

HPC S2600WFT
(1.80 GHz, Intel Xeon Silver 4108)

SPECrate®2017_fp_base = 70.6

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 4204
Test Sponsor: M Computers s.r.o.
Tested by: M Computers s.r.o.

Test Date: Dec-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017

Compiler Version Notes

=====
C | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)

icc (ICC) 18.0.1 20171018
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====
C++ | 508.namd_r(base) 510.parest_r(base)

icpc (ICC) 18.0.1 20171018
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====
C++, C | 511.povray_r(base) 526.blender_r(base)

icpc (ICC) 18.0.1 20171018
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.1 20171018
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====
C++, C, Fortran | 507.cactuBSSN_r(base)

icpc (ICC) 18.0.1 20171018
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.1 20171018
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.1 20171018
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====
Fortran | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)

ifort (IFORT) 18.0.1 20171018
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====
Fortran, C | 521.wrf_r(base) 527.cam4_r(base)

ifort (IFORT) 18.0.1 20171018
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.1 20171018

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

M Computers s.r.o.

SPECrate®2017_fp_base = 70.6

HPC S2600WFT
(1.80 GHz, Intel Xeon Silver 4108)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 4204

Test Date: Dec-2017

Test Sponsor: M Computers s.r.o.

Hardware Availability: Oct-2017

Tested by: M Computers s.r.o.

Software Availability: Sep-2017

Compiler Version Notes (Continued)

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using both C and C++:

icpc icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

M Computers s.r.o.

SPECrate®2017_fp_base = 70.6

HPC S2600WFT
(1.80 GHz, Intel Xeon Silver 4108)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 4204

Test Date: Dec-2017

Test Sponsor: M Computers s.r.o.

Hardware Availability: Oct-2017

Tested by: M Computers s.r.o.

Software Availability: Sep-2017

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Base Other Flags

C benchmarks:

-m64 -std=c11

C++ benchmarks:

-m64

Fortran benchmarks:

-m64

Benchmarks using both Fortran and C:

-m64 -std=c11

Benchmarks using both C and C++:

-m64 -std=c11

Benchmarks using Fortran, C, and C++:

-m64 -std=c11



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

M Computers s.r.o.

HPC S2600WFT
(1.80 GHz, Intel Xeon Silver 4108)

SPECrate®2017_fp_base = 70.6

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 4204

Test Sponsor: M Computers s.r.o.

Tested by: M Computers s.r.o.

Test Date: Dec-2017

Hardware Availability: Oct-2017

Software Availability: Sep-2017

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/MComputers-Platform-Settings-SKL-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/MComputers-Platform-Settings-SKL-revA.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.0.2 on 2017-12-30 09:52:04-0500.

Report generated on 2020-02-04 11:56:06 by CPU2017 PDF formatter v6255.

Originally published on 2018-02-28.