



# SPEC® CPU2017 Integer Rate Result

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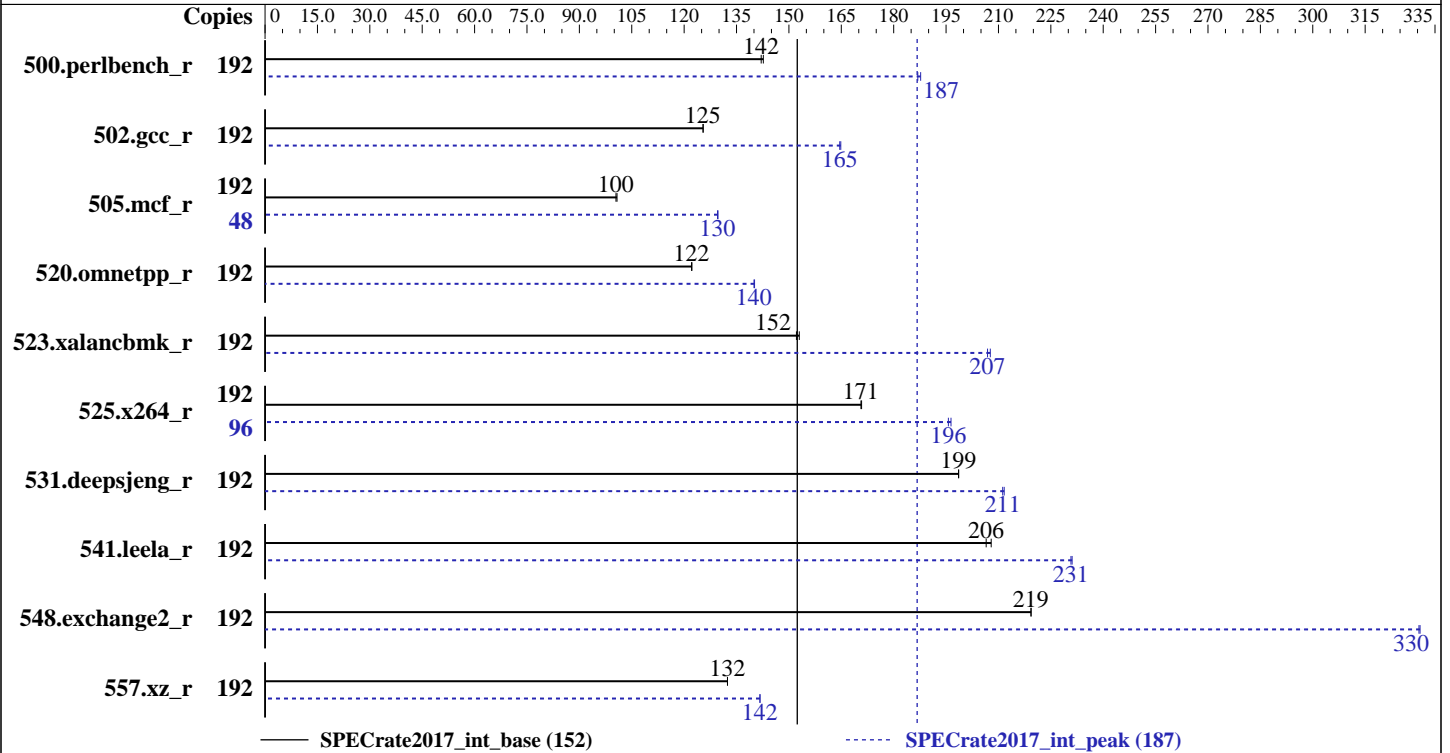
## Fujitsu Fujitsu SPARC M12-2S

SPECrate2017\_int\_base = 152

SPECrate2017\_int\_peak = 187

CPU2017 License: 19  
Test Sponsor: Fujitsu  
Tested by: Fujitsu

Test Date: Dec-2017  
Hardware Availability: Apr-2017  
Software Availability: Jul-2017



### Hardware

CPU Name: SPARC64 XII  
 Max MHz.: 4350  
 Nominal: 4250  
 Enabled: 24 cores, 2 chips, 8 threads/core  
 Orderable: 1 to 16 BBs; each BB contains 1 or 2 CPU chips;  
 2, 3, 4, ... 384 cores  
 Cache L1: 64 KB I + 64 KB D on chip per core  
 L2: 512 KB I+D on chip per core  
 L3: 32 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (32 x 32 GB 2Rx4 PC4-2400T-R)  
 Storage: 1 x 600 GB 10K RPM SAS (for system disk)  
 Other: None

### Software

OS: Oracle Solaris 11.3 SRU 24.4  
 Compiler: C/C++/Fortran: Version 12.6 of Oracle Developer Studio  
 Parallel: No  
 Firmware: Fujitsu HCP Version 3040 released Oct-2017  
 File System: tmpfs  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other: None



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	192	<b><u>2152</u></b>	<b><u>142</u></b>	2142	143			192	<b><u>1635</u></b>	<b><u>187</u></b>	1628	188		
502.gcc_r	192	2167	125	<b><u>2168</u></b>	<b><u>125</u></b>			192	1649	165	<b><u>1652</u></b>	<b><u>165</u></b>		
505.mcf_r	192	3079	101	<b><u>3088</u></b>	<b><u>100</u></b>			48	598	130	<b><u>599</u></b>	<b><u>130</u></b>		
520.omnetpp_r	192	<b><u>2063</u></b>	<b><u>122</u></b>	2061	122			192	<b><u>1799</u></b>	<b><u>140</u></b>	1797	140		
523.xalancbmk_r	192	<b><u>1332</u></b>	<b><u>152</u></b>	1325	153			192	<b><u>980</u></b>	<b><u>207</u></b>	976	208		
525.x264_r	192	<b><u>1970</u></b>	<b><u>171</u></b>	1968	171			96	856	196	<b><u>859</u></b>	<b><u>196</u></b>		
531.deepsjeng_r	192	1108	199	<b><u>1108</u></b>	<b><u>199</u></b>			192	<b><u>1042</u></b>	<b><u>211</u></b>	1040	212		
541.leela_r	192	1529	208	<b><u>1540</u></b>	<b><u>206</u></b>			192	<b><u>1378</u></b>	<b><u>231</u></b>	1376	231		
548.exchange2_r	192	2293	219	<b><u>2294</u></b>	<b><u>219</u></b>			192	<b><u>1523</u></b>	<b><u>330</u></b>	1521	331		
557.xz_r	192	1566	132	<b><u>1566</u></b>	<b><u>132</u></b>			192	1463	142	<b><u>1463</u></b>	<b><u>142</u></b>		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

Processes were assigned to specific processors using 'pbind' commands. The config file option 'submit' was used, along with a list of processors in the 'BIND' variable, to generate the pbind commands. (For details, please see the config file.)

## Operating System Notes

### Shell Environments:

ulimit -s 131072 was used to limit the space consumed by the stack (and therefore make more space available to the heap).

The "Logical Domains Manager" service was turned off using the command "svcadm disable ldmd".

### System Tunables:

(/etc/system parameters)

autoup = 86400

Causes pages older than the listed number of seconds to be written by fsflush.  
doiflush = 0

Controls whether file system metadata syncs will be executed during fsflush invocations.  
dopageflush = 0

Controls whether memory is examined for modified pages during fsflush invocations.  
zfs:zfs\_arc\_max=1073741824

Determines the maximum size of the ZFS Adaptive Replacement Cache (ARC).



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## General Notes

The Building Block (BB) is just a Fujitsu SPARC M12-2S that is the basic unit to be expanded as if stacking up children's blocks.

File System:

tmpfs: output\_root was used to put run directories in /tmp/cpu2017  
zfs: operating system

Binaries were compiled on a system with 2x SPARC64 XII CPU + 1TB Memory using Oracle Solaris 11.3 SRU 24.4

## Platform Notes

Firmware Settings:

(XSCF operations)

Set High Speed Mode via XSCF command "sethsmode -s on".

Sysinfo program /export/cpu2017/bin/sysinfo

Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f

running on H2S-232-D0 Sat Dec 2 11:27:17 2017

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /usr/sbin/psrinfo

SPARC64-XII (chipid 0, clock 4250 MHz)

SPARC64-XII (chipid 1, clock 4250 MHz)

2 chips

192 threads

4250 MHz

From kstat: 24 cores

From prtconf: 1046016 Megabytes

/etc/release:

Oracle Solaris 11.3 SPARC

uname -a:

SunOS H2S-232-D0 5.11 11.3 sun4v sparc sun4v

disk: df -h /export/cpu2017

Filesystem	Size	Used	Available	Capacity	Mounted on
rpool/export	547G	8.7G	107G	8%	/export

(End of data from sysinfo program)



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## Compiler Version Notes

=====  
CXXC 520.omnetpp\_r(base) 523.xalancbmk\_r(base, peak) 531.deepsjeng\_r(base)  
541.leela\_r(base)  
-----

CC: Studio 12.6 Sun C++ 5.15 SunOS\_sparc 2017/05/30  
-----

=====  
CXXC 520.omnetpp\_r(peak) 531.deepsjeng\_r(peak) 541.leela\_r(peak)  
-----

CC: Studio 12.6 Sun C++ 5.15 SunOS\_sparc 2017/05/30  
-----

=====  
CC 500.perlbench\_r(base) 502.gcc\_r(base) 505.mcf\_r(base) 525.x264\_r(base)  
557.xz\_r(base)  
-----

cc: Studio 12.6 Sun C 5.15 SunOS\_sparc 2017/05/30  
-----

=====  
CC 500.perlbench\_r(peak) 502.gcc\_r(peak) 505.mcf\_r(peak) 525.x264\_r(peak)  
557.xz\_r(peak)  
-----

cc: Studio 12.6 Sun C 5.15 SunOS\_sparc 2017/05/30  
-----

=====  
FC 548.exchange2\_r(base)  
-----

f90: Studio 12.6 Fortran 95 8.8 SunOS\_sparc 2017/05/30  
-----

=====  
FC 548.exchange2\_r(peak)  
-----

f90: Studio 12.6 Fortran 95 8.8 SunOS\_sparc 2017/05/30  
-----

## Base Compiler Invocation

C benchmarks:  
cc

C++ benchmarks:  
CC

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## Base Compiler Invocation (Continued)

Fortran benchmarks:  
f90

## Base Portability Flags

```
500.perlbench_r: -DSPEC_SOLARIS_SPARC
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -D_FILE_OFFSET_BITS=64
520.omnetpp_r: -DSPEC_GCC_MANGLE -D_FILE_OFFSET_BITS=64
523.xalancbmk_r: -DSPEC_SOLARIS -D_FILE_OFFSET_BITS=64
525.x264_r: -D_FILE_OFFSET_BITS=64
531.deepsjeng_r: -D_FILE_OFFSET_BITS=64
541.leela_r: -D_FILE_OFFSET_BITS=64
548.exchange2_r: -D_FILE_OFFSET_BITS=64
557.xz_r: -D_FILE_OFFSET_BITS=64
```

## Base Optimization Flags

C benchmarks:  
-m32 -fast -xtarget=sparc64xii -xipo=2 -xpagesize=4M  
-xsegment\_align=4M -xthroughput -xalias\_level=weak

C++ benchmarks:  
-m32 -fast -xtarget=sparc64xii -xipo=2 -xpagesize=4M  
-xsegment\_align=4M -xthroughput -std=c++03 -lfast

Fortran benchmarks:  
-m32 -fast -xtarget=sparc64xii -xipo=2 -xpagesize=4M  
-xsegment\_align=4M -xthroughput

## Base Other Flags

C benchmarks:  
-xjobs=8

C++ benchmarks:  
-xjobs=8

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## Base Other Flags (Continued)

Fortran benchmarks:  
-xjobs=8

## Peak Compiler Invocation

C benchmarks:  
cc

C++ benchmarks:  
CC

Fortran benchmarks:  
f90

## Peak Portability Flags

```
500.perlbenc_r: -DSPEC_SOLARIS_SPARC
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -D_FILE_OFFSET_BITS=64
520.omnetpp_r: -D_FILE_OFFSET_BITS=64
523.xalancbmk_r: -DSPEC_SOLARIS -D_FILE_OFFSET_BITS=64
525.x264_r: -D_FILE_OFFSET_BITS=64
531.deepsjeng_r: -D_FILE_OFFSET_BITS=64
541.leela_r: -D_FILE_OFFSET_BITS=64
548.exchange2_r: -D_FILE_OFFSET_BITS=64
557.xz_r: -D_FILE_OFFSET_BITS=64
```

## Peak Optimization Flags

C benchmarks:

```
500.perlbenc_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M
-xsegment_align=256M -xthroughput -xO4
-xalias_level=layout -xinline_param=level:3 -lfast

502.gcc_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M
-xsegment_align=256M -xthroughput -xtarget=sparc64xplus
-xipo=1 -xinline_param=level:3 -xprefetch=no%auto
```

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## Peak Optimization Flags (Continued)

502.gcc\_r (continued):

```
-xthroughput=no -lfast
```

505.mcf\_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32

```
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xalias_level=strong  
-xprefetch=no%auto -xthroughput=no
```

525.x264\_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32

```
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xunroll=3  
-xprefetch=no%auto -W2,-Afully_unroll:always=on
```

557.xz\_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32

```
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xalias_level=std  
-xprefetch=latx:0.4
```

C++ benchmarks:

520.omnetpp\_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32

```
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xalias_level=compatible  
-xprefetch=latx:0.4 -library=stdcxx4 -template=extdef  
-lfast
```

523.xalancbmk\_r: -m32 -fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M

```
-xsegment_align=256M -xthroughput -xprefetch=no%auto  
-library=stlport4 -lfast
```

531.deepsjeng\_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32

```
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput -xalias_level=compatible  
-xinline_param=level:1 -xunroll=2 -xprefetch=no%auto  
-std=c++03
```

541.leela\_r: -xprofile=collect:./feedback -xprofile=use:./feedback -m32

```
-fast -xtarget=sparc64xii -xipo=2 -xpagesize=256M  
-xsegment_align=256M -xthroughput  
-xinline_param=max_growth:500 -xprefetch=no%auto  
-xthroughput=no -Wc,-Qiselect-funcalign=4  
-Qoption iropt -Afully_unroll:always=on -std=c++03
```

Fortran benchmarks:

```
-xprofile=collect:./feedback -xprofile=use:./feedback -m32 -fast  
-xtarget=sparc64xii -xipo=2 -xpagesize=256M -xsegment_align=256M
```

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## Peak Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-xthroughput -xtarget=sparc64xplus -xprefetch=no%auto  
-Qoption iropt -Afully_unroll:always=on
```

## Peak Other Flags

C benchmarks:

```
-xjobs=8
```

C++ benchmarks:

```
-xjobs=8
```

Fortran benchmarks:

```
-xjobs=8
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Oracle-Developer-Studio12.6.html>

<http://www.spec.org/cpu2017/flags/Fujitsu-M12-2S.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Oracle-Developer-Studio12.6.xml>

<http://www.spec.org/cpu2017/flags/Fujitsu-M12-2S.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

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