



SPEC® CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R940
(Intel Xeon Gold 6152, 2.10 GHz)

SPECrate2017_int_base = 416

SPECrate2017_int_peak = 441

CPU2017 License: 55

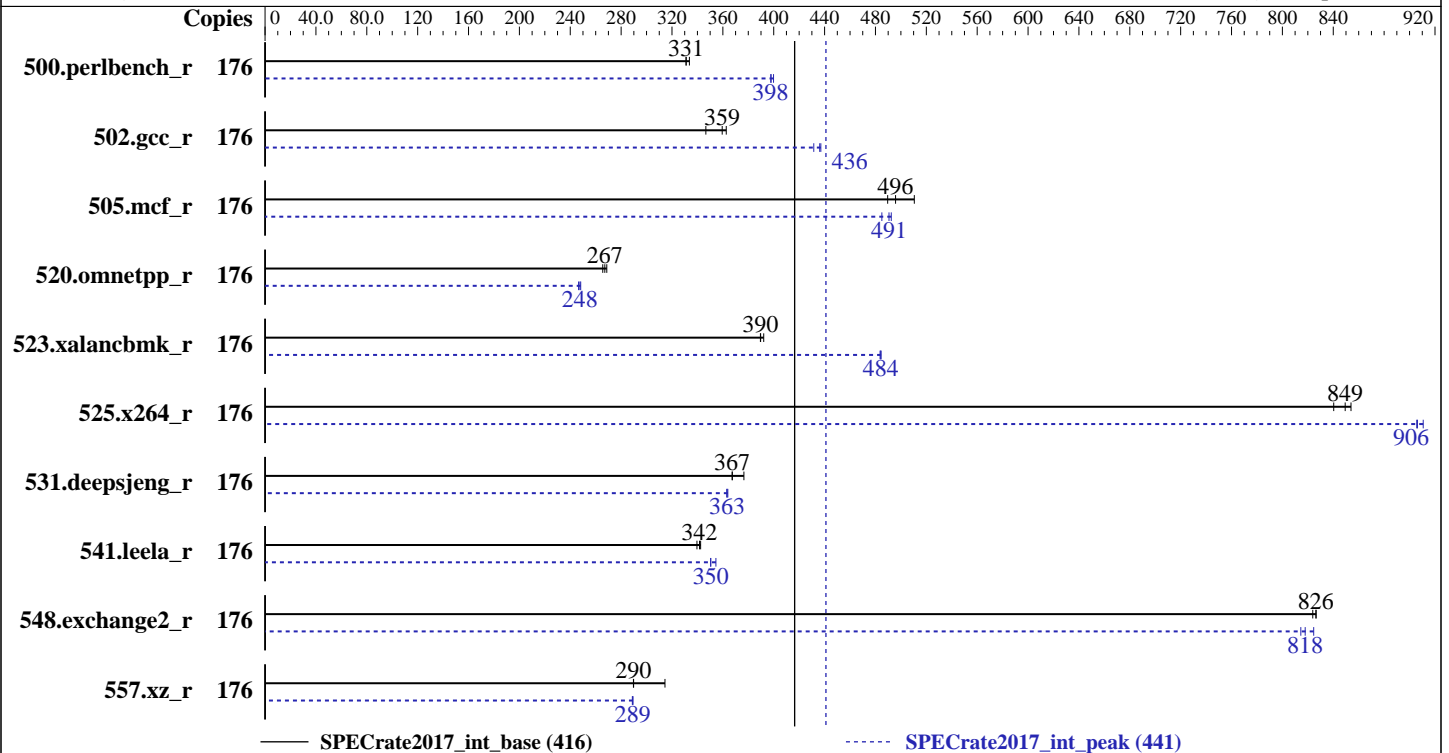
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Oct-2017

Hardware Availability: Sep-2017

Software Availability: Sep-2017



Hardware

CPU Name: Intel Xeon Gold 6152
 Max MHz.: 3700
 Nominal: 2100
 Enabled: 88 cores, 4 chips, 2 threads/core
 Orderable: 2,4 chip
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 30.25 MB I+D on chip per chip
 Other: None
 Memory: 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)
 Storage: 1 x 900 GB 15K RPM SAS12
 Other: None

Software

OS: SUSE Linux Enterprise Server 12 SP2
 4.4.21-69-default
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++
 Compiler for Linux;
 Fortran: Version 18.0.0.128 of Intel Fortran
 Compiler for Linux
 Parallel: No
 Firmware: Version 1.1.7 released Sep-2017
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other: jemalloc: jemalloc memory allocator library
 V5.0.1;
 jemalloc: configured and built at default for
 32bit (i686) and 64bit (x86_64) targets;
 jemalloc: built with the RedHat Enterprise 7.4,
 and the system compiler gcc 4.8.5;
 jemalloc: sources available from jemalloc.net or
 releases



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	176	840	334	846	331	846	331	176	701	400	704	398	705	398
502.gcc_r	176	719	347	687	363	693	359	176	578	431	571	436	570	437
505.mcf_r	176	557	511	574	496	581	490	176	586	485	580	491	578	492
520.omnetpp_r	176	864	267	859	269	870	266	176	937	247	930	248	933	248
523.xalancbmk_r	176	477	390	474	392	477	390	176	384	484	384	484	384	484
525.x264_r	176	367	840	363	849	361	854	176	338	911	340	906	340	906
531.deepsjeng_r	176	536	377	549	367	549	367	176	555	363	554	364	556	363
541.leela_r	176	858	340	851	343	853	342	176	832	350	822	355	832	350
548.exchange2_r	176	558	826	558	827	560	824	176	559	825	564	818	566	814
557.xz_r	176	604	314	656	290	656	290	176	658	289	657	289	657	289

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop_caches

runcpu command invoked through numactl i.e.:

numactl --interleave=all runcpu <etc>



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Platform Notes

BIOS settings:

Logical Processor Enabled
 Virtualization Technology Disabled
 Sub NUMA Cluster Enabled
 System Profile set to Custom
 CPU Performance set to Maximum Performance
 C1E Disabled
 C States set to Autonomous
 Uncore Frequency set to Dynamic
 Memory Patrol Scrub Disabled
 Energy Efficiency Policy set to Performance
 CPU Interconnect Bus Link Power Management Disabled
 PCI ASPM L1 Link Power Management Disabled
 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
 running on linux-apb2 Thu Oct 19 22:33:34 2017

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz
 4 "physical id"s (chips)
 176 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 22
siblings : 44
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 176
On-line CPU(s) list: 0-175
Thread(s) per core: 2
Core(s) per socket: 22
Socket(s): 4
NUMA node(s): 8
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
```

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Platform Notes (Continued)

```

Model name: Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz
Stepping: 4
CPU MHz: 2095.081
BogoMIPS: 4190.16
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 30976K
NUMA node0 CPU(s):
0,8,16,24,32,40,48,56,64,72,80,88,96,104,112,120,128,136,144,152,160,168
NUMA node1 CPU(s):
1,9,17,25,33,41,49,57,65,73,81,89,97,105,113,121,129,137,145,153,161,169
NUMA node2 CPU(s):
2,10,18,26,34,42,50,58,66,74,82,90,98,106,114,122,130,138,146,154,162,170
NUMA node3 CPU(s):
3,11,19,27,35,43,51,59,67,75,83,91,99,107,115,123,131,139,147,155,163,171
NUMA node4 CPU(s):
4,12,20,28,36,44,52,60,68,76,84,92,100,108,116,124,132,140,148,156,164,172
NUMA node5 CPU(s):
5,13,21,29,37,45,53,61,69,77,85,93,101,109,117,125,133,141,149,157,165,173
NUMA node6 CPU(s):
6,14,22,30,38,46,54,62,70,78,86,94,102,110,118,126,134,142,150,158,166,174
NUMA node7 CPU(s):
7,15,23,31,39,47,55,63,71,79,87,95,103,111,119,127,135,143,151,159,167,175
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 sse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bml hle avx2 smep bmi2
erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

```

```

/proc/cpuinfo cache data
cache size : 30976 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0 8 16 24 32 40 48 56 64 72 80 88 96 104 112 120 128 136 144 152 160 168
node 0 size: 95359 MB
node 0 free: 94979 MB
node 1 cpus: 1 9 17 25 33 41 49 57 65 73 81 89 97 105 113 121 129 137 145 153 161 169
node 1 size: 96760 MB
node 1 free: 96408 MB

```

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Platform Notes (Continued)

```

node 2 cpus: 2 10 18 26 34 42 50 58 66 74 82 90 98 106 114 122 130 138 146 154 162 170
node 2 size: 96760 MB
node 2 free: 96403 MB
node 3 cpus: 3 11 19 27 35 43 51 59 67 75 83 91 99 107 115 123 131 139 147 155 163 171
node 3 size: 96760 MB
node 3 free: 96414 MB
node 4 cpus: 4 12 20 28 36 44 52 60 68 76 84 92 100 108 116 124 132 140 148 156 164 172
node 4 size: 96760 MB
node 4 free: 96417 MB
node 5 cpus: 5 13 21 29 37 45 53 61 69 77 85 93 101 109 117 125 133 141 149 157 165 173
node 5 size: 96760 MB
node 5 free: 96416 MB
node 6 cpus: 6 14 22 30 38 46 54 62 70 78 86 94 102 110 118 126 134 142 150 158 166 174
node 6 size: 96760 MB
node 6 free: 96415 MB
node 7 cpus: 7 15 23 31 39 47 55 63 71 79 87 95 103 111 119 127 135 143 151 159 167 175
node 7 size: 96757 MB
node 7 free: 96412 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10  21  21  21  11  21  21  21
1:  21  10  21  21  21  11  21  21
2:  21  21  10  21  21  21  11  21
3:  21  21  21  10  21  21  21  11
4:  11  21  21  21  10  21  21  21
5:  21  11  21  21  21  10  21  21
6:  21  21  11  21  21  21  10  21
7:  21  21  21  11  21  21  21  10

```

From /proc/meminfo

MemTotal: 791224268 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb_release -d

SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86_64)

VERSION = 12

PATCHLEVEL = 2

This file is deprecated and will be removed in a future service pack or release.

Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP2"

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Platform Notes (Continued)

```
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="/o:suse:sles:12:sp2"
```

uname -a:

```
Linux linux-apb2 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Oct 19 22:33

SPEC is set to: /home/cpu2017

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   796G   17G  779G   3% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.1.7 08/10/2017

Memory:

```
12x 002C00B3002C 18ASF2G72PDZ-2G6D1 16 GB 2 rank 2666
12x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666
24x 00CE063200CE M393A2K43BB1-CTD 16 GB 2 rank 2666
```

(End of data from sysinfo program)

Compiler Version Notes

```
=====  
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)  
525.x264_r(base, peak) 557.xz_r(base, peak)  
-----
```

icc (ICC) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

```
=====  
CC 500.perlbench_r(peak) 502.gcc_r(peak)  
-----
```

icc (ICC) 18.0.0 20170811

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Compiler Version Notes (Continued)

CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
541.leela_r(base)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====

CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
541.leela_r(peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====

FC 548.exchange2_r(base, peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64

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Base Portability Flags (Continued)

548.exchange2_r: -DSPEC_LP64

557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Base Other Flags

C benchmarks:

-m64 -std=c11

C++ benchmarks:

-m64

Fortran benchmarks:

-m64

Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort



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Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc
```

```
502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc
```

```
505.mcf_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib
-ljemalloc
```

```
525.x264_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -fno-alias
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
557.xz_r: Same as 505.mcf_r
```

C++ benchmarks:

```
520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
```

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Peak Optimization Flags (Continued)

523.xalancbmk_r (continued):

`-L/usr/local/je5.0.1-32/lib -ljemalloc`

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:

`-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div`

`-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`

`-L/usr/local/je5.0.1-64/lib -ljemalloc`

Peak Other Flags

C benchmarks (except as noted below):

`-m64 -std=c11`

502.gcc_r: `-m32 -std=c11`

C++ benchmarks (except as noted below):

`-m64`

523.xalancbmk_r: `-m32`

Fortran benchmarks:

`-m64`

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revC.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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