



# SPEC® CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### ProLiant DL560 Gen10

(2.00 GHz, Intel Xeon Platinum 8153)

SPECspeed2017\_fp\_base = 137

SPECspeed2017\_fp\_peak = Not Run

CPU2017 License: 3

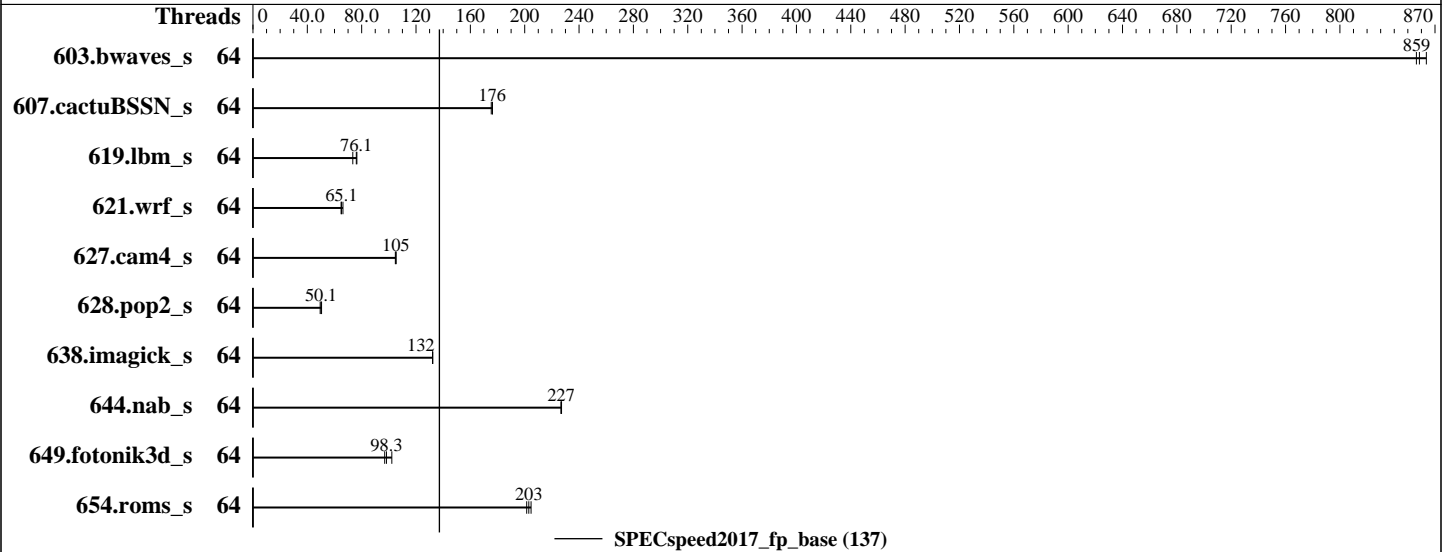
Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2017

Hardware Availability: Oct-2017

Software Availability: Sep-2017



### Hardware

CPU Name: Intel Xeon Platinum 8153  
 Max MHz.: 2800  
 Nominal: 2000  
 Enabled: 64 cores, 4 chips  
 Orderable: 1, 2, 4 chip(s)  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 22 MB I+D on chip per chip  
 Other: None  
 Memory: 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)  
 Storage: 1 x 480 GB SSD SATA, RAID 0  
 Other: None

### Software

OS: Red Hat Enterprise Linux Server release 7.3  
 Kernel 4.4.73-5-default  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
 Parallel: Yes  
 Firmware: HPE BIOS Version U34 released Oct-2017 (tested with U34 9/29/2017)  
 File System: xfs  
 System State: Run level 3  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: None



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL560 Gen10**

(2.00 GHz, Intel Xeon Platinum 8153)

SPECspeed2017\_fp\_base = 137

SPECspeed2017\_fp\_peak = Not Run

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Oct-2017  
**Hardware Availability:** Oct-2017  
**Software Availability:** Sep-2017

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	64	68.3	864	<b><u>68.7</u></b>	<b><u>859</u></b>	68.9	856							
607.cactuBSSN_s	64	95.1	175	94.6	176	<b><u>94.6</u></b>	<b><u>176</u></b>							
619.lbm_s	64	68.5	76.5	<b><u>68.9</u></b>	<b><u>76.1</u></b>	71.3	73.5							
621.wrf_s	64	200	66.2	<b><u>203</u></b>	<b><u>65.1</u></b>	204	64.9							
627.cam4_s	64	84.6	105	<b><u>84.2</u></b>	<b><u>105</u></b>	84.2	105							
628.pop2_s	64	235	50.5	240	49.5	<b><u>237</u></b>	<b><u>50.1</u></b>							
638.imagick_s	64	109	132	109	132	<b><u>109</u></b>	<b><u>132</u></b>							
644.nab_s	64	<b><u>77.0</u></b>	<b><u>227</u></b>	76.9	227	77.1	227							
649.fotonik3d_s	64	89.2	102	94.0	97.0	<b><u>92.8</u></b>	<b><u>98.3</u></b>							
654.roms_s	64	<b><u>77.6</u></b>	<b><u>203</u></b>	76.9	205	78.1	201							

SPECspeed2017\_fp\_base = 137

SPECspeed2017\_fp\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Filesystem page cache cleared with:  
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run  
irqbalance disabled with "systemctl stop irqbalance"  
tuned profile set with "tuned-adm profile throughput-performance"

## General Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=core,compact"  
LD\_LIBRARY\_PATH = "/cpu2017/lib/ia32:/cpu2017/lib/intel64:/cpu2017/je5.0.1-32"  
OMP\_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.4

## Platform Notes

BIOS Configuration:  
Intel Hyperthreading set to Disabled  
Thermal Configuration set to Maximum Cooling  
Memory Patrol Scrubbing set to Disabled  
LLC Prefetcher set to Enabled  
LLC Dead Line Allocation set to Disabled  
Stale A to S set to Enabled  
Workload Profile set to General Peak Frequency Compute

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL560 Gen10**

(2.00 GHz, Intel Xeon Platinum 8153)

SPECspeed2017\_fp\_base = 137

SPECspeed2017\_fp\_peak = Not Run

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Oct-2017

**Hardware Availability:** Oct-2017

**Software Availability:** Sep-2017

## Platform Notes (Continued)

Energy/Performance Bias set to Maximum Performance  
Uncore Frequency Scaling set to Auto  
Workload Profile set to General Peak Frequency Compute  
NUMA Group Size Optimization set to Flat

Sysinfo program /cpu2017/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f  
running on DL560-Gen10 Mon Oct 16 20:35:39 2017

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Platinum 8153 CPU @ 2.00GHz  
4 "physical id"s (chips)  
64 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 16  
siblings : 16  
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15  
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15  
physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15  
physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 64  
On-line CPU(s) list: 0-63  
Thread(s) per core: 1  
Core(s) per socket: 16  
Socket(s): 4  
NUMA node(s): 4  
Vendor ID: GenuineIntel  
CPU family: 6  
Model: 85  
Model name: Intel(R) Xeon(R) Platinum 8153 CPU @ 2.00GHz  
Stepping: 4  
CPU MHz: 2000.000  
BogoMIPS: 4005.10  
Virtualization: VT-x  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 1024K

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL560 Gen10**

(2.00 GHz, Intel Xeon Platinum 8153)

SPECspeed2017\_fp\_base = 137

SPECspeed2017\_fp\_peak = Not Run

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Oct-2017  
**Hardware Availability:** Oct-2017  
**Software Availability:** Sep-2017

## Platform Notes (Continued)

L3 cache: 22528K  
NUMA node0 CPU(s): 0-15  
NUMA node1 CPU(s): 16-31  
NUMA node2 CPU(s): 32-47  
NUMA node3 CPU(s): 48-63

```
/proc/cpuinfo cache data
cache size : 22528 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
node 0 size: 196267 MB
node 0 free: 179973 MB
node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
node 1 size: 196608 MB
node 1 free: 191254 MB
node 2 cpus: 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47
node 2 size: 196608 MB
node 2 free: 185658 MB
node 3 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63
node 3 size: 196607 MB
node 3 free: 190966 MB
node distances:
node  0  1  2  3
  0:  10  21  21  21
  1:  21  10  21  21
  2:  21  21  10  21
  3:  21  21  21  10
```

```
From /proc/meminfo
MemTotal: 792073812 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
```

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL560 Gen10**

(2.00 GHz, Intel Xeon Platinum 8153)

SPECspeed2017\_fp\_base = 137

SPECspeed2017\_fp\_peak = Not Run

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Oct-2017

**Hardware Availability:** Oct-2017

**Software Availability:** Sep-2017

## Platform Notes (Continued)

```
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server
```

uname -a:

```
Linux DL560-Gen10 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016 x86_64
x86_64 x86_64 GNU/Linux
```

run-level 3 Oct 13 16:19

SPEC is set to: /cpu2017

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdb1        xfs   447G   28G  419G   7% /
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U34 09/29/2017

Memory:

48x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666

(End of data from sysinfo program)

## Compiler Version Notes

=====  
CC 619.lbm\_s(base) 638.imagick\_s(base) 644.nab\_s(base)  
=====

icc (ICC) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
=====

=====  
FC 607.cactuBSSN\_s(base)  
=====

icpc (ICC) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

icc (ICC) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

ifort (IFORT) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
=====

=====  
FC 603.bwaves\_s(base) 649.fotonik3d\_s(base) 654.roms\_s(base)  
=====

(Continued on next page)



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL560 Gen10**

(2.00 GHz, Intel Xeon Platinum 8153)

SPECspeed2017\_fp\_base = 137

SPECspeed2017\_fp\_peak = Not Run

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Oct-2017

**Hardware Availability:** Oct-2017

**Software Availability:** Sep-2017

## Compiler Version Notes (Continued)

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====  
CC 621.wrf\_s(base) 627.cam4\_s(base) 628.pop2\_s(base)

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactuBSSN\_s: -DSPEC\_LP64  
619.lbm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
627.cam4\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
628.pop2\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
-assume byterecl  
638.imagick\_s: -DSPEC\_LP64  
644.nab\_s: -DSPEC\_LP64  
649.fotonik3d\_s: -DSPEC\_LP64  
654.roms\_s: -DSPEC\_LP64



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL560 Gen10**

(2.00 GHz, Intel Xeon Platinum 8153)

SPECspeed2017\_fp\_base = 137

SPECspeed2017\_fp\_peak = Not Run

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Oct-2017

**Hardware Availability:** Oct-2017

**Software Availability:** Sep-2017

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
```

Fortran benchmarks:

```
-DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp  
-nostandard-realloc-lhs -align array32byte
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP  
-nostandard-realloc-lhs -align array32byte
```

Benchmarks using Fortran, C, and C++:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP  
-nostandard-realloc-lhs -align array32byte
```

## Base Other Flags

C benchmarks:

```
-m64 -std=c11
```

Fortran benchmarks:

```
-m64
```

Benchmarks using both Fortran and C:

```
-m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c11
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.xml>



# SPEC CPU2017 Floating Point Speed Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

**ProLiant DL560 Gen10**

(2.00 GHz, Intel Xeon Platinum 8153)

SPECspeed2017\_fp\_base = 137

SPECspeed2017\_fp\_peak = Not Run

**CPU2017 License:** 3

**Test Sponsor:** HPE

**Tested by:** HPE

**Test Date:** Oct-2017

**Hardware Availability:** Oct-2017

**Software Availability:** Sep-2017

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.2 on 2017-10-16 21:35:39-0400.

Report generated on 2018-10-31 12:56:03 by CPU2017 PDF formatter v6067.

Originally published on 2017-11-14.