



CINT2000 Result

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Hewlett-Packard Company

ProLiant DL380 G5 (2.66 GHz, Intel Xeon processor X5355)

SPECint2000 = 2732

SPECint_base2000 = 2727

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Feb-2007 Hardware Avail: Jan-2007 Software Avail: Nov-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	84.8	1652	84.9	1649	
175.vpr	1400	70.3	1990	69.0	2028	
176.gcc	1100	39.7	2774	39.7	2774	
181.mcf	1800	42.8	4206	42.6	4226	
186.crafty	1000	41.1	2434	41.1	2432	
197.parser	1800	82.2	2191	81.9	2198	
252.eon	1300	37.5	3465	37.6	3458	
253.perlbmk	1800	56.0	3212	55.9	3220	
254.gap	1100	42.4	2593	42.4	2592	
255.vortex	1900	42.0	4524	42.0	4522	
256.bzip2	1500	74.0	2028	74.0	2028	
300.twolf	3000	96.0	3124	96.0	3124	

Hardware

CPU: Intel Xeon processor X5355 (2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus)
CPU MHz: 2666
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip (4 MB shared per 2 cores)
L3 Cache: N/A
Other Cache: N/A
Memory: 16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem: 4x36 GB 10 K SAS
Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise X64 Edition SP1
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20061103Z)
Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
MicroQuill Smartheap Library 8.0
File System: NTFS
System State: Default

Notes/Tuning Information

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib

Base tuning for C++ programs: -fast -Qcxx_features +FDO ONESTEP=yes

Portability flags:

176.gcc: -Dalloca=_alloca /F10000000

186.crafty: -DNT_i386

253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT

254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Peak tuning:

164.gzip: -fast +FDO ONESTEP=yes

175.vpr: -fast +FDO ONESTEP=yes

176.gcc: basepeak=1

181.mcf: -fast +FDO

186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -fast +FDO ONESTEP=yes

252.eon: -fast +FDO

253.perlbmk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -fast +FDO ONESTEP=yes

255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib



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Notes/Tuning Information (Continued)

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256.bzip2:    basepeak=1
300.twolf:    -fast          +FDO          shlw32M.lib
BIOS Configuration Notes
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch disabled
Other Configuration Notes
The start /b /wait /affinity command is used to bind CPU(s) to processes.
```