



CINT2000 Result

Copyright ©1999-2007, Standard Performance Evaluation Corporation

IBM Corporation

IBM BladeCenter HS21 XM (1.86 GHz Xeon E5320, 8MB L2 Cache)

SPECint_rate2000 = --

SPECint_rate_base2000 = 144

SPEC license #: 11 | Tested by: IBM Corporation | Test date: Jan-2007 | Hardware Avail: Feb-2007 | Software Avail: Mar-2006

| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|-------------|-------------|--------------|------------|--------|---------|-------|
| 164.gzip | 8 | 127 | 103 | | | |
| 175.vpr | 8 | 125 | 104 | | | |
| 176.gcc | 8 | 75.4 | 135 | | | |
| 181.mcf | 8 | 158 | 106 | | | |
| 186.crafty | 8 | 57.8 | 160 | | | |
| 197.parser | 8 | 140 | 119 | | | |
| 252.eon | 8 | 53.9 | 224 | | | |
| 253.perlbnk | 8 | 83.3 | 201 | | | |
| 254.gap | 8 | 93.6 | 109 | | | |
| 255.vortex | 8 | 71.1 | 248 | | | |
| 256.bzip2 | 8 | 128 | 109 | | | |
| 300.twolf | 8 | 140 | 198 | | | |

Hardware

CPU: Intel Xeon processor E5320 (1.86 GHz, 1066 MHz bus)
CPU MHz: 1866
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1, 2
Parallel: No
Primary Cache: 32KB(I) + 32KB(D) on chip (per core)
Secondary Cache: 8MB(I+D) on chip, per chip (4MB shared per 2 cores)
L3 Cache: N/A
Other Cache: N/A
Memory: 8 x 2048 MB ECC PC2-5300F
Disk Subsystem: 73GB SAS 10K RPM
Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler: Intel C++ Compiler 9.1 for 32-bit applications
Build 20060323Z
Microsoft Visual Studio 2005(for libraries)
SmartHeap Library Version 8.0 from <http://www.microquill.com/>
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlw32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
252.eon: -DHAS_ERRLIST
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
```