



# CINT2000 Result

Copyright ©1999-2007, Standard Performance Evaluation Corporation

**Hewlett-Packard Company**  
ProLiant BL460c (2.66 GHz, Intel Xeon processor X5355)

SPECint\_rate2000 = **200**  
SPECint\_rate\_base2000 = **199**

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jan-2007 Hardware Avail: Jan-2007 Software Avail: Nov-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	8	89.4	145	8	89.7	145
175.vpr	8	91.2	143	8	89.9	144
176.gcc	8	57.4	178	8	57.4	178
181.mcf	8	122	137	8	119	140
186.crafty	8	41.3	225	8	41.3	225
197.parser	8	94.9	176	8	94.7	176
252.eon	8	37.7	320	8	37.6	321
253.perlbnk	8	59.8	279	8	59.1	283
254.gap	8	70.0	146	8	69.9	146
255.vortex	8	51.8	340	8	51.5	342
256.bzip2	8	92.7	150	8	92.7	150
300.twolf	8	97.3	286	8	97.3	286

### Hardware

CPU: Intel Xeon processor X5355 (2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus)  
CPU MHz: 2666  
FPU: Integrated  
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
CPU(s) orderable: 1,2 chips  
Parallel: No  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 8 MB I+D on chip per chip (4 MB shared per 2 cores)  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 16 GB (8x2 GB PC2-5300F CL5)  
Disk Subsystem: 2x72 GB 10 K SAS  
Other Hardware:

### Software

Operating System: Windows Server 2003 Enterprise X64 Edition (x86\_64) SP1  
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20061103Z)  
Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)  
MicroQuill Smartheap Library 8.0  
File System: NTFS  
System State: Default

## Notes/Tuning Information

+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use

Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib

Base tuning for C++ programs: -fast -Qcxx\_features +FDO ONESTEP=yes

Portability flags:

176.gcc: -Dalloca=\_alloca /F10000000

186.crafty: -DNT\_i386

253.perlbnk: -DSPEC\_CPU2000\_NTOS -DPERL DLL /MT

254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

Peak tuning:

164.gzip: -fast +FDO ONESTEP=yes

175.vpr: -fast +FDO ONESTEP=yes

176.gcc: basepeak=1

181.mcf: -fast +FDO

186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -fast +FDO ONESTEP=yes

252.eon: -fast +FDO

253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -fast +FDO ONESTEP=yes

255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib



# CINT2000 Result

Copyright ©1999-2007, Standard Performance Evaluation Corporation

Hewlett-Packard Company  
ProLiant BL460c (2.66 GHz, Intel Xeon processor X5355)

SPECint\_rate2000 = 200  
SPECint\_rate\_base2000 = 199

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jan-2007 Hardware Avail: Jan-2007 Software Avail: Nov-2006

## Notes/Tuning Information (Continued)

```
256.bzip2:    basepeak=1
300.twolf:    -fast                +FDO                shlw32M.lib
BIOS Configuration Notes
  Power Regulator set to Static High
  Adjacent Sector Prefetch disabled
Other Configuration Notes
  The start /b /wait /affinity command is used to bind CPU(s) to processes.
```