



CFP2000 Result

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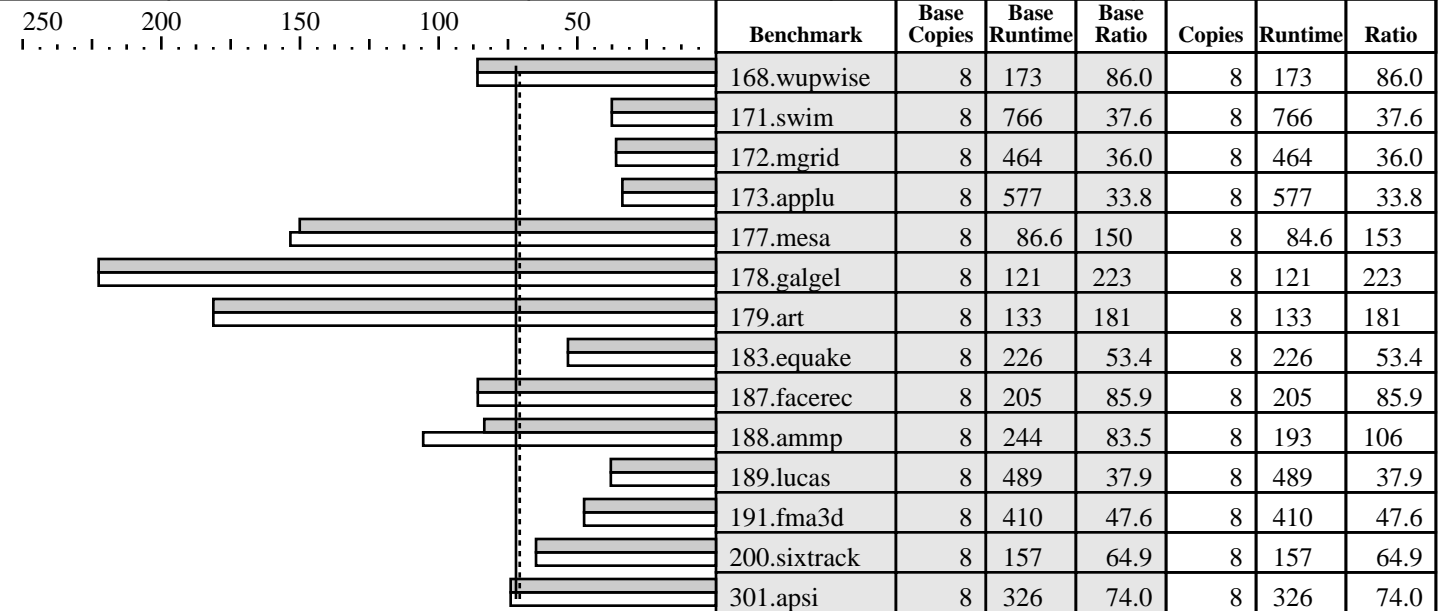
Bull

NovaScale B260 (Intel Xeon processor E5320, 1.86 GHz)

SPECfp_rate2000 = 72.1

SPECfp_rate_base2000 = 70.8

SPEC license #: 20 Tested by: Bull Test date: Dec-2006 Hardware Avail: Jan-2007 Software Avail: Dec-2006



Hardware

CPU: Intel Xeon Processor E5320 (1.86 GHz, 8MB L2, 1066MHz bus)
 CPU MHz: 1860
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1 to 2 chips
 Parallel: No
 Primary Cache: 32KB(I) + 32KB(D) on chip, per core
 Secondary Cache: 8MB(I+D) on chip, per chip (4MB shared by 2 cores)
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 8GB (2GB DIMMx4, FB-DIMM PC2-5300F ECC CL5)
 Disk Subsystem: 1x73GB SAS 10000 rpm
 Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise Edition (32 bits), Service Pack 1
 Compiler: Intel(R) C++ Compiler for 32-bit app., Version 9.1, - Build 20061103Z Package ID: W_CC_C_9.1.033
 Intel(R) Fortran Compiler for 32-bit app., Version 9.1, - Build 20061103Z Package ID: W_FC_C_9.1.033
 Microsoft Visual Studio .NET 2003 (for libraries)
 MicroQuill SmartHeap Library 8.0 (shlW32M.lib)
 File System: NTFS
 System State: Default

Notes/Tuning Information

Portability flags:

178.galgel: -FI /F32000000

Feedback optimization:

+FDO: PASS1= -Qprof_gen PASS2= -Qprof_use

Baseline Tuning Flags:

for C programs:
 -fast +FDO ONESTEP=yes shlW32M.lib
 for Fortran programs:
 -fast -Qansi_alias +FDO

Peak Tuning Flags:

168.wupwise: basepeak=yes
 171.swim: basepeak=yes
 172.mgrid: basepeak=yes
 173.applu: basepeak=yes



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Notes/Tuning Information (Continued)

```
177.mesa: -fast +FDO ONESTEP=yes
178.galgel: basepeak=yes
179.art: basepeak=yes
183.quake: basepeak=yes
187.facerec: basepeak=yes
188.ammp: -fast +FDO ONESTEP=yes
189.lucas: basepeak=yes
191.fma3d: basepeak=yes
200.sixtrack: basepeak=yes
301.apsi: basepeak=yes
```

Other Configuration Notes

For information about Bull please see:
<http://www.bull.com>