



# CINT2000 Result

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**Acer Incorporated**  
Acer Altos R910 (2.66 GHz Intel Xeon Processor 7020)

SPECint\_rate2000 = 46.2  
SPECint\_rate\_base2000 = 46.0

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Jun-2006 | Hardware Avail: Jun-2006 | Software Avail: Nov-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	162	40.1	4	162	40.1
175.vpr	4	242	26.8	4	237	27.4
176.gcc	4	89.6	57.0	4	89.6	57.0
181.mcf	4	412	20.2	4	412	20.2
186.crafty	4	104	44.7	4	104	44.7
197.parser	4	193	43.4	4	192	43.4
252.eon	4	77.6	77.7	4	77.1	78.3
253.perlbnk	4	122	68.2	4	122	68.4
254.gap	4	95.8	53.3	4	95.8	53.3
255.vortex	4	113	78.1	4	113	78.1
256.bzip2	4	230	30.2	4	231	30.1
300.twolf	4	247	56.4	4	245	56.7

### Hardware

CPU: Intel Xeon Processor 7020 (2.66 GHz, 2x1MB L2, 667MHz system bus)  
CPU MHz: 2660  
FPU: Integrated  
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip (Hyper-Threading Technology disabled)  
CPU(s) orderable: 1,2,3,4  
Parallel: No  
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip, per core  
Secondary Cache: 1024KB(I+D) on chip, per core  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 16 x 1024MB ECC DDR2-400 SDRAM  
Disk Subsystem: 1 x 73GB 10000RPM U320 SCSI HDD  
Other Hardware:

### Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790), Service Pack 1  
Compiler: Intel C++ Compiler 9.0 Build 20051130Z for IA32  
Microsoft Visual Studio .Net 2003 (7.1.3091, for libraries)  
shlw32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/  
File System: NTFS  
System State: Default

## Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlw32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: basepeak=yes
175.vpr: -fast -Qansi_alias +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: basepeak=yes
197.parser: -fast -Qansi_alias +FDO
252.eon: -fast +FDO
253.perlbnk: -fast -Qansi_alias +FDO shlw32M.lib
254.gap: basepeak=yes
255.vortex: basepeak=yes
256.bzip2: -fast -Oa -Qunroll1 +FDO
```



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## Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlw32M.lib  
Enable hardware prefetching and adjacent cache line prefetch BIOS setting