



CINT2000 Result

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Acer Incorporated
Acer Altos G320 (3.2 GHz Intel Pentium D 940)

SPECint_rate2000 = 35.9
SPECint_rate_base2000 = 35.9

SPEC license #: 97 Tested by: Acer Incorporated Test date: May-2006 Hardware Avail: May-2006 Software Avail: Nov-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	129	25.1	2	129	25.1
175.vpr	2	132	24.5	2	131	24.9
176.gcc	2	58.4	43.7	2	58.4	43.7
181.mcf	2	104	40.0	2	104	40.0
186.crafty	2	84.3	27.5	2	84.3	27.5
197.parser	2	135	31.0	2	135	31.0
252.eon	2	64.7	46.6	2	64.2	47.0
253.perlbnk	2	94.6	44.1	2	94.5	44.2
254.gap	2	67.1	38.0	2	67.1	38.0
255.vortex	2	71.7	61.5	2	71.7	61.5
256.bzip2	2	126	27.6	2	126	27.6
300.twolf	2	187	37.2	2	187	37.3

Hardware

CPU: Intel Pentium D 940 (3.2 GHz, 2x2MB L2, 800MHz system bus)
CPU MHz: 3200
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1
Parallel: No
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip, per core
Secondary Cache: 2048KB(I+D) on chip, per core
L3 Cache: N/A
Other Cache: N/A
Memory: 4 x 1024MB ECC DDR2-667 SDRAM
Disk Subsystem: 1 x 73GB 10000RPM SATA HDD
Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790), Service Pack 1
Compiler: Intel C++ Compiler 9.0 Build 20051130Z for IA32
Microsoft Visual Studio .Net 2003 (7.1.3091, for libraries)
shlw32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlw32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: basepeak=yes
175.vpr: -fast -Qansi_alias +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: basepeak=yes
197.parser: -fast -Qansi_alias +FDO
252.eon: -fast +FDO
253.perlbnk: -fast -Qansi_alias +FDO shlw32M.lib
254.gap: basepeak=yes
255.vortex: basepeak=yes
256.bzip2: -fast -Oa -Qunroll1 +FDO
```



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Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlw32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at <http://www.acer.com>