



# CFP2000 Result

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Acer Incorporated  
Altos R510 (3.0 GHz Intel Xeon)

SPECfp2000 = 1546  
SPECfp\_base2000 = 1527

SPEC license #: 97 Tested by: Acer Incorporated Test date: May-2005 Hardware Avail: May-2005 Software Avail: Oct-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
168.wupwise	1600	74.1	2160	74.0	2161	
171.swim	3100	153	2026	153	2029	
172.mgrid	1800	139	1297	139	1296	
173.applu	2100	163	1291	163	1291	
177.mesa	1400	107	1311	107	1311	
178.galgel	2900	104	2794	104	2795	
179.art	2600	72.4	3590	72.4	3590	
183.quake	1300	66.2	1964	66.2	1963	
187.facerec	1900	120	1586	120	1586	
188.amp	2200	238	924	204	1079	
189.lucas	2000	110	1821	110	1825	
191.fma3d	2100	167	1256	167	1256	
200.sixtrack	1100	203	541	201	546	
301.apsi	2600	238	1095	238	1094	

### Hardware

CPU: Intel Xeon (3.0 GHz, 2MB L2, 800MHz system bus)  
CPU MHz: 3000  
FPU: Integrated  
CPU(s) enabled: 1 core, 1 chip, 1 core/chip(Hyper-Threading Technology disabled)  
CPU(s) orderable: 1,2  
Parallel: No  
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip  
Secondary Cache: 2048KB(I+D) on chip  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 6 x 512MB ECC DDR-333 SDRAM  
Disk Subsystem: 1 x 80GB 7200RPM SATA HDD  
Other Hardware:

### Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)  
Compiler: Intel C++ Build 20041019Z and Fortran Compiler 8.1 Build 20040802Z  
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)  
SmartHeap Library Version 7.4 from http://www.microquill.com/  
File System: NTFS  
System State: Default

## Notes/Tuning Information

```
+FDO: PASS1= -Qprof_gen PASS2=-Qprof_use
Base tuning for Fortran Programs : -fast -Qansi_alias +FDO
177.mesa:Base for C Program: -fast +FDO shlw32M.lib
179.art:Base for C Program: -fast +FDO shlw32M.lib
183.quake:Base for C Program: -fast +FDO shlw32M.lib
188.amp:Base for C Program: -fast +FDO shlw32M.lib
Portability:
178.galgel: -FI /F32000000
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast -Qansi_alias +FDO
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=yes
177.mesa: basepeak=yes
178.galgel: -fast -Qansi_alias +FDO
179.art: basepeak=yes
183.quake: -fast -Oa -Qrcd -Qprof_gen shlw32M.lib
```



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## Notes/Tuning Information (Continued)

187.facerec: basepeak=yes  
188.amp: -Oa -QxP  
189.lucas: -fast -Qprefetch- +FDO  
191.fma3d: basepeak=yes  
200.sixtrack: -Qipo -QxP +FDO  
301.apsi: -fast +FDO  
Enable hardware prefetching and adjacent cache line prefetch BIOS setting  
Product description located at <http://www.acer.com>