



CINT2000 Result

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Acer Incorporated
Altos R510 (3.6 GHz Intel Xeon)

SPECint2000 = 1679
SPECint_base2000 = 1675

SPEC license #: 97 Tested by: Acer Incorporated Test date: May-2005 Hardware Avail: May-2005 Software Avail: Oct-2004

| Benchmark | Reference Time | Base Runtime | Base Ratio | Runtime | Ratio | 1000 2000 3000 4000 | | | |
|-------------|----------------|--------------|------------|---------|-------|---------------------|--|--|--|
| 164.gzip | 1400 | 117 | 1194 | 117 | 1194 | | | | |
| 175.vpr | 1400 | 122 | 1144 | 120 | 1162 | | | | |
| 176.gcc | 1100 | 55.2 | 1994 | 55.2 | 1994 | | | | |
| 181.mcf | 1800 | 104 | 1733 | 104 | 1733 | | | | |
| 186.crafty | 1000 | 74.7 | 1339 | 74.7 | 1339 | | | | |
| 197.parser | 1800 | 124 | 1453 | 124 | 1453 | | | | |
| 252.eon | 1300 | 57.7 | 2252 | 57.6 | 2257 | | | | |
| 253.perlbmk | 1800 | 87.0 | 2069 | 86.9 | 2072 | | | | |
| 254.gap | 1100 | 58.0 | 1895 | 58.0 | 1895 | | | | |
| 255.vortex | 1900 | 69.4 | 2739 | 69.4 | 2739 | | | | |
| 256.bzip2 | 1500 | 122 | 1226 | 122 | 1233 | | | | |
| 300.twolf | 3000 | 167 | 1797 | 167 | 1797 | | | | |

Hardware

CPU: Intel Xeon (3.6 GHz, 2MB L2, 800MHz system bus)
 CPU MHz: 3600
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip (Hyper-Threading Technology disabled)
 CPU(s) orderable: 1,2
 Parallel: No
 Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip
 Secondary Cache: 2048KB(I+D) on chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 6 x 512MB ECC DDR-333 SDRAM
 Disk Subsystem: 1 x 120GB 7200RPM SATA HDD
 Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
 Compiler: Intel C++ Compiler 8.1 Build 20041019Z
 Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)
 SmartHeap Library Version 7.4 from <http://www.microquill.com/>
 File System: NTFS
 System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon: Approved stdcpp src.alt used
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: basepeak=yes
175.vpr: -fast -Qansi_alias +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: basepeak=yes
197.parser: -fast -Qansi_alias +FDO
252.eon: -fast +FDO
253.perlbmk: -fast -Qansi_alias +FDO shlw32M.lib
254.gap: basepeak=yes
255.vortex: basepeak=yes
```



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Notes/Tuning Information (Continued)

256.bzip2: -fast -Oa -Qunroll1 +FDO
300.twolf: -fast -O3 +FDO shlw32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at <http://www.acer.com>