



CINT2000 Result

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Fujitsu Siemens Computers
PRIMERGY RX300 S2, 64-bit Intel Xeon processor 3.60 GHz

SPECint2000 = 1705
SPECint_base2000 = 1705

SPEC license #: 22 Tested by: Fujitsu Siemens Computers Test date: May-2005 Hardware Avail: Mar-2005 Software Avail: Jan-2005

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	1000 2000 3000 4000			
164.gzip	1400	116	1203	116	1203	[Bar chart showing ratio 1203]			
175.vpr	1400	120	1168	120	1168	[Bar chart showing ratio 1168]			
176.gcc	1100	54.3	2026	54.3	2026	[Bar chart showing ratio 2026]			
181.mcf	1800	93.0	1935	93.0	1935	[Bar chart showing ratio 1935]			
186.crafy	1000	74.6	1341	74.6	1341	[Bar chart showing ratio 1341]			
197.parser	1800	123	1462	123	1462	[Bar chart showing ratio 1462]			
252.eon	1300	57.6	2258	57.6	2258	[Bar chart showing ratio 2258]			
253.perlbmk	1800	87.3	2063	87.3	2063	[Bar chart showing ratio 2063]			
254.gap	1100	58.4	1884	58.4	1884	[Bar chart showing ratio 1884]			
255.vortex	1900	67.9	2799	67.9	2799	[Bar chart showing ratio 2799]			
256.bzip2	1500	119	1263	119	1263	[Bar chart showing ratio 1263]			
300.twolf	3000	166	1806	166	1806	[Bar chart showing ratio 1806]			

Hardware

CPU: 64-bit Intel Xeon processor (3.60 GHz, 800 MHz bus)
CPU MHz: 3600
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip (Hyper-Threading Technology disabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12k micro-ops I + 16KBD on chip
Secondary Cache: 2048KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 8x512 MB DDRII-RAM PC2-3200R
Disk Subsystem: Fujitsu MAS3367NC (SCSI U320 15 krpm)
Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1
Compiler: Intel C++ Compiler 8.1 Build 20050405Z
Microsoft Visual Studio .Net 2003 7.1.3088 (for libraries)
SmartHeap Library Version 6.03a from http://www.microquill.com
File System: NTFS
System State: default

Notes/Tuning Information

GENERAL

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability flags

176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon: Approved srcalt stdcpp is used
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags

for C programs: -fast +FDO shlW32M.lib
for C++ programs: -fast -Qcxx_features +FDO

Peak tuning flags

same as baseline (basepeak=yes set globally)

The system bus runs at 800 MHz

This result was measured with 32-bit binaries using the 32-bit version of



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Notes/Tuning Information (Continued)

the operating system.

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<http://www.fujitsu-siemens.com/countries>