CINT2000 Result

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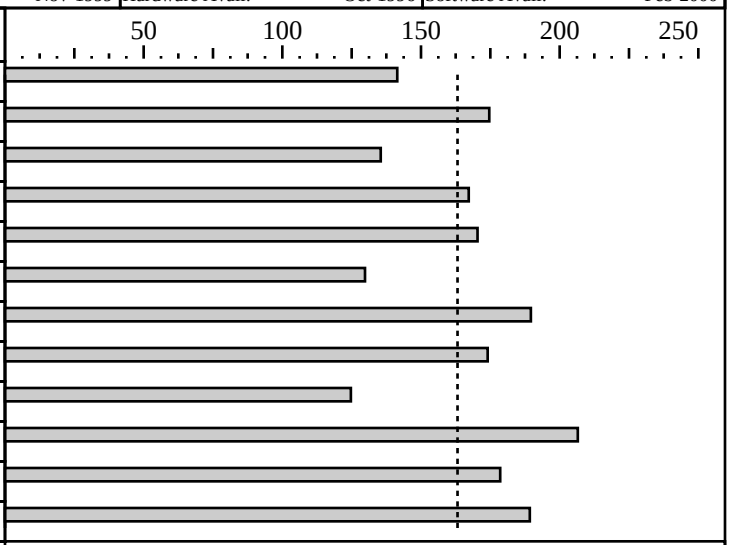
Compaq Computer Corporation
AlphaStation 500/500

SPECint2000 = --

SPECint_base2000 = 163

SPEC license #: 2	Tested by: Compaq NH	Test date: Nov-1999	Hardware Avail: Oct-1996	Software Avail: Feb-2000

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	990	141		
175.vpr	1400	802	175		
176.gcc	1100	812	135		
181.mcf	1800	1076	167		
186.crafty	1000	587	170		
197.parser	1800	1387	130		
252.eon	1300	686	190		
253.perlbmk	1800	1034	174		
254.gap	1100	882	125		
255.vortex	1900	920	207		
256.bzip2	1500	840	179		
300.twolf	3000	1585	189		



Hardware

CPU:	Alpha 21164
CPU MHz:	500
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1
Parallel:	No
Primary Cache:	8KB(I)+8KB(D) on chip
Secondary Cache:	96KB on chip
L3 Cache:	8MB off chip
Other Cache:	None
Memory:	512MB
Disk Subsystem:	3x4GB
Other Hardware:	Ethernet

Software

Operating System:	Tru64 UNIX V4.0F
Compiler:	Compaq C X6.2-259-449AT Compaq C++ V6.2-033-4298H
File System:	ufs
System State:	Multi-user

Notes/Tuning Information

```
Baseline C  : ONESTEP cc  -arch ev56 -fast GEMFB
         C++: ONESTEP cxx -arch ev56 -O2

GEMFB: fdo_pre0     = mkdir /tmp/pb; rm -f /tmp/pb/${baseexe}*
       PASS1_CFLAGS = -prof_gen_noopt    -prof_dir /tmp/pb
       PASS2_CFLAGS = -prof_use_feedback -prof_dir /tmp/pb


Portability: gcc: -Dalloca=__builtin_alloca; crafty: -DALPHA
perlbmk: -DSPEC_CPU2000_DUNIX; vortex: -DSPEC_CPU2000_LP64
gap: -DSYS_HAS_CALLOC_PROTO -DSYS_IS_BSD -DSYS_HAS_IOCTL_PROTO -DSPEC_CPU2000_LP64
The C compiler used here will be available to customers as V6.3
at http://www.unix.digital.com/dtk/ .  It will be available as an
ADK (Advanced Developers' Kit) in February, 2000 and as the final
version in July, 2000.
```