

28th IEEE INTERNATIONAL PARALLEL & DISTRIBUTED PROCESSING SYMPOSIUM



CONFERENCE INFORMATION

PAPERS BY SESSION

PAPERS BY AUTHOR

GETTING STARTED

TRADEMARKS

SEARCH

PROGRAM & PROCEEDINGS

2014 Industry Partners



ELSEVIER



2014 IEEE IPDPS Workshops (IPDPSW 2014)

19-23 May 2014
Phoenix (AZ) USA



Published by



Sponsored by



In cooperation with



ACM SIGARCH

IEEE Computer Society Technical Committee on Computer Architecture
IEEE Computer Society Technical Committee on Distributed Processing

Conference Information

2014 IEEE 28th International Parallel & Distributed Processing Symposium Workshops

- Message from the General Chair
- Message from the Workshops Chair
- Title Page (Book version)
- Copyright Page (Book version)
- Table of Contents (Book version)
- Author Index (Book version)
- Publisher's Information (Book version)

Sessions

- ❑ Workshop 1: HCW — Heterogeneity in Computing Workshop
- ❑ HCW Session 1: Heterogeneous Environments for Basic Linear Algebra
- ❑ HCW Session 2: Scheduling and Resource Allocation
- ❑ HCW Session 3: Resource-Related Performance Optimization
- ❑ Workshop 2: RAW — Reconfigurable Architectures Workshop
- ❑ RAW Session 1: Compilers and Binary Translation for Reconfigurable Architectures
- ❑ RAW Session 2: New Reconfigurable Architectures
- ❑ RAW Session 3: VIPES Papers
- ❑ RAW Session 4: Circuit-Level Applications
- ❑ RAW Session 5: Numerical Reconfigurable Computing Applications
- ❑ RAW Session 6: Applications of Reconfigurable Computing
- ❑ RAW Poster Session 1
- ❑ RAW Poster Session 2
- ❑ Workshop 3: HIPS — Workshop on High-Level Parallel Programming Models and Supportive Environments
- ❑ HIPS Session 1: System Support

Sessions (cont.)

- ❑ HIPS Session 2: Optimization
- ❑ HIPS Session 3: Effective Communication
- ❑ Workshop 4: NIDISC — Workshop on Nature Inspired Distributed Computing
- ❑ NIDISC Session 1: Applications of Bio-Inspired Algorithms
- ❑ NIDISC Session 2: Wireless Networks and Mobility Management
- ❑ NIDISC Session 3: Multi-objective Optimization
- ❑ Workshop 5: HiCOMB — Workshop on High Performance Computational Biology
- ❑ HiCOMB Session 1: Parallel Algorithms for Biological Sequence Analysis
- ❑ HiCOMB Session 2: Parallel/Distributed Architectures for Biological Applications
- ❑ HiCOMB Session 3: Metagenomics and Assembly
- ❑ Workshop 6: APDCM — Advances in Parallel and Distributed Computing Models
- ❑ APDCM Session 1
- ❑ APDCM Session 2
- ❑ APDCM Session 3
- ❑ APDCM Session 4
- ❑ Workshop 7: HPPAC — High-Performance, Power-Aware Computing

Sessions (cont.)

- ❑ HPPAC Session 1: Power and Energy Analysis and Profiling
- ❑ HPPAC Session 2: Power-Efficient Hardware
- ❑ HPPAC Session 3: Large Scale Power Management
- ❑ Workshop 8: HPGC — High-Performance Grid and Cloud Computing Workshop
- ❑ HPGC Session 1
- ❑ HPGC Session 2
- ❑ Workshop 9: AsHES — Accelerators and Hybrid Exascale Systems
- ❑ AsHES Session 1: Programming Model and Performance Optimizations
- ❑ AsHES Session 2: Accelerating Applications
- ❑ AsHES Session 3: Emerging Hybrid Systems
- ❑ Workshop 10: PLC — Programming Models, Languages, and Compilers Workshop for Manycore and Heterogeneous Architectures
- ❑ PLC Session 1: Programming and Compilation Techniques for GPUs
- ❑ PLC Session 2: Libraries and Optimization Frameworks
- ❑ PLC Session 3: Tools and Performance Evaluation
- ❑ Workshop 11: EduPar-NSF/TCPP Workshop on Parallel and Distributed Computing Education

Sessions (cont.)

- ❑ EduPar Session: Introductory Course and Across Curriculum
- ❑ EduPar Session: Software Engineering Courses
- ❑ EduPar Session: Miscellaneous
- ❑ Workshop 12: GABB — Graph Algorithms Building Blocks
- ❑ Workshop 13: PDSEC — Workshop on Parallel and Distributed Scientific and Engineering Computing
- ❑ PDSEC Session 1: Best Papers
- ❑ PDSEC Session 2: Algorithms (I)
- ❑ PDSEC Session 3: Systems and Performance Analysis
- ❑ PDSEC Session 4: Algorithms (II)
- ❑ Workshop 14: DPDNS — Dependable Parallel, Distributed, and Network-Centric Systems
- ❑ DPDNS Session: Applications
- ❑ DPDNS Session: Theoretical Aspects
- ❑ Workshop 15: MTAAP — Workshop on Multi-threaded Architectures and Applications
- ❑ MTAAP Session: Algorithms and Position Papers
- ❑ MTAAP Session: Graph Analytics

Sessions (cont.)

- ❑ MTAAP Session: Accelerators
- ❑ Workshop 16: LSPP — Workshop on Large-Scale Parallel Processing
- ❑ LSPP Session 1: Performance Analysis and Optimization
- ❑ LSPP Session 2: Modeling Performance for Scaling
- ❑ LSPP Session 3: Large-Scale Systems
- ❑ LSPP Session 4: Scheduling
- ❑ Workshop 17: PCO — Parallel Computing and Optimization
- ❑ PCO Session 1: Optimization Techniques for Parallel or Distributed Architectures
- ❑ PCO Session 2: Parallel Optimization Algorithms
- ❑ PCO Session 3: Task Scheduling and Miscellaneous
- ❑ Workshop 18: ParLearning — Workshop on Parallel and Distributed Computing for Large Scale Machine Learning and Big Data Analytics
- ❑ ParLearning Session 1
- ❑ ParLearning Session 2
- ❑ ParLearning Session 3
- ❑ Workshop 19: HPDIC - High Performance Data Intensive Computing

Sessions (cont.)

- ❑ HPDIC Session 1: Memory, I/O, and Performance Enhancement
- ❑ HPDIC Session 2: Clustering, Data Management, and Applications
- ❑ Workshop 20: JSSPP — Workshop on Job Scheduling Strategies for Parallel Processing
- ❑ Workshop 21: CHIUW — Chapel Implementers and Users Workshop

Papers by Session

Workshop 1: HCW — Heterogeneity in Computing Workshop

- ❑ HCW Introduction
Behrooz Shirazi and Uwe Schwiegelshohn
- ❑ Message from the HCW Steering Committee Chair
Behrooz Shirazi
- ❑ Message from the HCW General Chair
Uwe Schwiegelshohn
- ❑ Message from the HCW Program Chair
Shoukat Ali
- ❑ HCW 2014 Keynote Talk
David Abramson

Papers by Session

HCW Session 1: Heterogeneous Environments for Basic Linear Algebra

- ❑ Hybrid Multi-elimination ILU Preconditioners on GPUs
Dimitar Lukarski, Hartwig Anzt, Stanimire Tomov, and Jack Dongarra
- ❑ Searching for the Optimal Data Partitioning Shape for Parallel Matrix Matrix Multiplication on 3 Heterogeneous Processors
Ashley DeFlumere and Alexey Lastovetsky
- ❑ Taking Advantage of Hybrid Systems for Sparse Direct Solvers via Task-Based Runtimes
Xavier Lacoste, Mathieu Faverge, George Bosilca, Pierre Ramet, and Samuel Thibault
- ❑ Topology-Aware Optimization of Communications for Parallel Matrix Multiplication on Hierarchical Heterogeneous HPC Platform
Tania Malik, Vladimir Rychkov, Alexey Lastovetsky, and Jean-Noël Quintin

Papers by Session

HCW Session 2: Scheduling and Resource Allocation

- ❑ Scheduling Methods for Accelerating Applications on Architectures with Heterogeneous Cores
Linchuan Chen, Xin Huo, and Gagan Agrawal
- ❑ Utility Driven Dynamic Resource Management in an Oversubscribed Energy-Constrained Heterogeneous System
Bhavesh Khemka, Ryan Friese, Sudeep Pasricha, Anthony A. Maciejewski, Howard Jay Siegel, Gregory A. Koenig, Sarah Powers, Marcia Hilton, Rajendra Rambharos, and Steve Poole
- ❑ An Efficient Algorithm for Scheduling Jobs in Volunteer Computing Platforms
Adel Essafi, Denis Trystram, and Zied Zaidi

Papers by Session

HCW Session 3: Resource-Related Performance Optimization

- ❑ Resource Centered Computing Delivering High Parallel Performance
Jens Gustedt, Stephane Vialle, and Patrick Mercier
- ❑ Point-to-Point and Congestion Bandwidth Estimation: Experimental Evaluation on PlanetLab Data
Lionel Eyraud-Dubois and Przemysław Uznański
- ❑ Runtime Behavior Comparison of Modern Accelerators and Coprocessors
Ayman Tarakji and Niels Ole Salscheider

Papers by Session

Workshop 2: RAW — Reconfigurable Architectures Workshop

- ❑ RAW Introduction and Committees
Jürgen Becker, Ramachandran Vaidyanathan, Marco Santambrogio, Jim Tørresen, Ron Sass, and Philip Leong
- ❑ RAW 2014 Keynotes
Joshua Walstrom and Maya Gokhale

Papers by Session

RAW Session 1: Compilers and Binary Translation for Reconfigurable Architectures

- ❑ **Twill: A Hybrid Microcontroller-FPGA Framework for Parallelizing Single-Threaded C Programs**
Doug Gallatin, Aaron Keen, Chris Lupo, and John Oliver
- ❑ **A New Dataflow Compiler IR for Accelerating Control-Intensive Code in Spatial Hardware**
Ali Mustafa Zaidi and David Greaves
- ❑ **Efficient Software-Based Runtime Binary Translation for Coarse-Grained Reconfigurable Architectures**
Toan X. Mai and Jongeun Lee

Papers by Session

RAW Session 2: New Reconfigurable Architectures

- ❑ A Dependable Coarse-Grain Reconfigurable Multicore Array
Georgios Smaragdos, Danish Anis Khan, Ioannis Sourdis, Christos Strydis, Alirad Malek, and Stavros Tzilis
- ❑ Automated Hybrid Interconnect Design for FPGA Accelerators Using Data Communication Profiling
Cuong Pham-Quoc, Zaid Al-Ars, and Koen Bertels
- ❑ SmartBricks: A Visual Environment to Design and Explore Novel Custom Domain-Specific Architectures
AnilKumar Sistla, Xiaozhong Luo, Mukund Malladi, Marc Reisner, Rajasekhar Ganduri, and Gayatri Mehta

Papers by Session

RAW Session 3: ViPES Papers

- ❑ A Framework for Mapping Dynamic Virtual Kernels onto Heterogeneous Reconfigurable Platforms
Harry Sidiropoulos, Kostas Siozios, and Dimitrios Soudris
- ❑ A Hybrid ILP-CP Model for Mapping Directed Acyclic Task Graphs to Multicore Architectures
Andreas Emeretlis, George Theodoridis, Panayiotis Alefragis, and Nikolaos Voros
- ❑ A Framework for Customizing Virtual 3-D Reconfigurable Platforms at Run-Time
Kostas Siozios, Dimitrios Soudris, and Michael Hübner

Papers by Session

RAW Session 4: Circuit-Level Applications

- ❑ Over-clocking of Linear Projection Designs through Device Specific Optimisations
Rui Policarpo Duarte and Christos-Savvas Bouganis
- ❑ Influence of Magnetic Fields and X-Radiation on Ring Oscillators in FPGAs
Michael Raitza, Markus Vogt, Christian Hochberger, and Thilo Pionteck
- ❑ Radiation Tolerance of Color Configuration on an Optically Reconfigurable Gate Array
Takumi Fujimori and Minoru Watanabe

Papers by Session

RAW Session 5: Numerical Reconfigurable Computing Applications

- ❑ Adaptive Booth Algorithm for Three-Integers Multiplication for Reconfigurable Mesh
Esti Stein and Yosi Ben Asher
- ❑ An FPGA Implementation of the Hestenes-Jacobi Algorithm for Singular Value Decomposition
Xinying Wang and Joseph Zambreno

Papers by Session

RAW Session 6: Applications of Reconfigurable Computing

- ❑ CyGraph: A Reconfigurable Architecture for Parallel Breadth-First Search
Osama G. Attia, Tyler Johnson, Kevin Townsend, Philip Jones, and Joseph Zambreno
- ❑ Adaptive Raytracing Implementation Using Partial Dynamic Reconfiguration
Gianluca Durelli, Fabrizio Spada, Riccardo Cattaneo, Christian Pilato, Danilo Pau, and Marco D. Santambrogio
- ❑ PaRA-Sched: A Reconfiguration-Aware Scheduler for Reconfigurable Architectures
Riccardo Cattaneo, Riccardo Bellini, Gianluca Durelli, Christian Pilato, Marco D. Santambrogio, and Donatella Sciuto

Papers by Session

RAW Poster Session 1

- ❑ An ILP-Based Optimal Circuit Mapping Method for PLDs
Hiroki Nishiyama, Masato Inagi, Shin'ichi Wakabayashi, Shinobu Nagayama, Keisuke Inoue, and Mineo Kaneko
- ❑ High-Level Synthesis from C vs. a DSL-Based Approach
Cristiano Bacelar de Oliveira, João M. P. Cardoso, and Eduardo Marques
- ❑ An Evaluation of User Satisfaction Driven Scheduling in a Polymorphic Embedded System
Zhang Zhang, Swamy D. Ponpandi, and Akhilesh Tyagi
- ❑ A Low-Latency Algorithm and FPGA Design for the Min-Search of LDPC Decoders
Georgios Tzimpragos, Christoforos Kachris, Dimitrios Soudris, and Ioannis Tomkos
- ❑ FPGA Redundancy Configurations: An Automated Design Space Exploration
Jahanzeb Anwer, Marco Platzner, and Sebastian Meisner

Papers by Session

RAW Poster Session 2

- ❑ Hierarchical Pipeline Optimization of Coarse Grained Reconfigurable Processor for Multimedia Applications
Chen Mei, Peng Cao, Yang Zhang, Bo Liu, and Leibo Liu
- ❑ Module Placement Using Constraint Programming in Run-Time Reconfigurable Systems
Alexander Wold, Andreas Agne, and Jim Torresen
- ❑ An Efficient Heterogeneous Register File Implementation for FPGAs
Hasan Erdem Yantir and Arda Yurdakul
- ❑ Minimizing Scrubbing Effort through Automatic Netlist Partitioning and Floorplanning
Bernhard Schmidt, Daniel Ziener, and Jürgen Teich
- ❑ Virtualization Support for FPGA-Based Coprocessors Connected via PCI Express to an Intel Multicore Platform
Duy Viet Vu, Timo Sandmann, Steffen Baehr, Oliver Sander, and Juergen Becker

Papers by Session

Workshop 3: HIPS — Workshop on High-Level Parallel Programming Models and Supportive Environments

- ❑ HIPS Introduction and Committees
John Cavazos

Papers by Session

HIPS Session 1: System Support

- ❑ Bohrium: A Virtual Machine Approach to Portable Parallelism
Mads R.B. Kristensen, Simon A.F. Lund, Troels Blum, Kenneth Skovhede, and Brian Vinter
- ❑ HATI: Hardware Assisted Thread Isolation for Concurrent C/C++ Programs
Juan Carlos Martínez Santos and Yunsi Fei
- ❑ A General Model Checking Framework for Various Memory Consistency Models
Tatsuya Abe and Toshiyuki Maeda

Papers by Session

HIPS Session 2: Optimization

- ❑ Autotuning Tensor Transposition
Lai Wei and John Mellor-Crummey
- ❑ Automatic MPI-IO Tuning with the Periscope Tuning Framework
Weifeng Liu, Isaías A. Comprés Ureña, Michael Gerndt, and Bin Gong
- ❑ Optimizing Collective Communication in UPC
Jithin Jose, Khaled Hamidouche, Jie Zhang, Akshay Venkatesh, and Dhabaleswar K. (DK) Panda

Papers by Session

HIPS Session 3: Effective Communication

- ❑ SWIFT: A Transparent and Flexible Communication Layer for PCIe-Coupled Accelerators and (Co-)Processors
Simon Pickartz, Pablo Reble, Carsten Clauss, and Stefan Lankes
- ❑ Deterministic Synchronization of Multi-threaded Programs with Operational Transformation
Christopher Boelmann, Lorenz Schwittmann, and Torben Weis
- ❑ ABC2: Adaptively Balancing Computation and Communication in a DSM Cluster of Multicores for Irregular Applications
Sai Charan Koduru, Keval Vora, and Rajiv Gupta

Papers by Session

Workshop 4: NIDISC — Workshop on Nature Inspired Distributed Computing

- NIDISC Introduction and Committees
Pascal Bouvry, Franciszek Seredynski, and El-Ghazali Talbi

Papers by Session

NIDISC Session 1: Applications of Bio-Inspired Algorithms

- ❑ Using Physical Stigmergy in Decentralized Optimization under Multiple Non-separable Constraints: Formal Methods and an Intelligent Lighting Example
Theodore P. Pavlic
- ❑ Hybrid Metaheuristic for Annual Hydropower Generation Optimization
A. Nakib, El-Ghazali Talbi, and A. Fuser
- ❑ Machine-Learning-Based Identification of Defect Patterns in Semiconductor Wafer Maps: An Overview and Proposal
Fatima Adly, Paul D. Yoo, Sami Muhaidat, and Yousof Al-Hammadi
- ❑ Data Quality, Consistency, and Interpretation Management for Wind Farms by Using Neural Networks
Alain Fuser, Florent Fontaine, and Jack Copper

Papers by Session

NIDISC Session 2: Wireless Networks and Mobility Management

- ❑ Graph-Based Cellular Automata Approach to Maximum Lifetime Coverage Problem in Wireless Sensor Networks
Antonina Tretyakova, Franciszek Seredynski, and Pascal Bouvry
- ❑ GPU Accelerated Nature Inspired Methods for Modelling Large Scale Bi-directional Pedestrian Movement
Sankha Baran Dutta, Robert McLeod, and Marcia Friesen
- ❑ Improving Bus Ride Comfort Using GLOSA-Based Dynamic Speed Optimisation
Marcin Seredynski, Patricia Ruiz, Krzysztof Szczypiorski, and Djamel Khadraoui
- ❑ A Genetic Algorithm-Based Sparse Coverage over Urban VANETs
Huang Cheng, Xin Fei, Azzedine Boukerche, and Mohammed Almulla

Papers by Session

NIDISC Session 3: Multi-objective Optimization

- ❑ A Game-Theoretic Approach to Multiobjective Job Scheduling in Cloud Computing Systems
Jakub Gasió and Franciszek Seredynski
- ❑ Multi-level and Multi-objective Survey on Cloud Scheduling
Yacine Kessaci, Nouredine Melab, and El-Ghazali Talbi
- ❑ Comparison of Multi-objective Optimization Algorithms for the JShadObf JavaScript Obfuscator
Benoît Bertholon, Sébastien Varrette, and Pascal Bouvry

Papers by Session

Workshop 5: HiCOMB — Workshop on High Performance Computational Biology

- ❑ HiCOMB Introduction and Committees
Alba Cristina Magalhaes Alves de Melo, Srinivas Aluru, and David A. Bader
- ❑ HiCOMB Keynote and Invited Talks
Stephen Larson, Ümit V. Çatalyürek, and Ananth Kalyanaraman

Papers by Session

HiCOMB Session 1: Parallel Algorithms for Biological Sequence Analysis

- ❑ Constructing Similarity Graphs from Large-Scale Biological Sequence Collections
Jaroslav Zola
- ❑ Removing Sequential Bottlenecks in Analysis of Next-Generation Sequencing Data
Yi Wang, Gagan Agrawal, Gulcin Ozer, and Kun Huang

Papers by Session

HiCOMB Session 2: Parallel/Distributed Architectures for Biological Applications

- ❑ Efficient Computation of the Phylogenetic Likelihood Function on the Intel MIC Architecture
Alexey M. Kozlov, Christian Goll, and Alexandros Stamatakis
- ❑ Process Simulation of Complex Biochemical Pathways in Explicit 3D Space Enabled by Heterogeneous Computing Platform
Jie Li, Amin Salighehdar, and Narayan Ganesan
- ❑ Exploring Large Scale Receptor-Ligand Pairs in Molecular Docking Workflows in HPC Clouds
Kary Ocaña, Silvia Benza, Daniel de Oliveira, Jonas Dias, and Marta Mattoso
- ❑ A Comparison of a Campus Cluster and Open Science Grid Platforms for Protein-Guided Assembly Using Pegasus Workflow Management System
Natasha Pavlovikj, Kevin Begcy, Sairam Behera, Malachy Campbell, Harkamal Walia, and Jitender S. Deogun

Papers by Session

HiCOMB Session 3: Metagenomics and Assembly

- ❑ Design and Optimization of a Metagenomics Analysis Workflow for NVRAM
Sasha Ames, Jonathan E. Allen, David A. Hysom, G. Scott Lloyd, and Maya B. Gokhale
- ❑ Parallelization of the Trinity Pipeline for De Novo Transcriptome Assembly
V. Sachdeva, C.S. Kim, K.E. Jordan, and M.D. Winn
- ❑ HiPGA: A High Performance Genome Assembler for Short Read Sequence Data
Xiaohui Duan, Kun Zhao, and Weiguo Liu

Papers by Session

Workshop 6: APDCM — Advances in Parallel and Distributed Computing Models

- APDCM Introduction and Committees
Oscar H. Ibarra

Papers by Session

APDCM Session 1

- ❑ Bulk Execution of Oblivious Algorithms on the Unified Memory Machine, with GPU Implementation
Kazuya Tani, Daisuke Takafuji, Koji Nakano, and Yasuaki Ito
- ❑ A Linear Performance-Breakdown Model for GPU Programming Optimization Guidance
Mario A. Chapa M. and Sato Hiroyuki
- ❑ A Hybrid Parallel Tridiagonal Solver on Multi-core Architectures
Guangping Tang, Kenli Li, Keqin Li, Hang Chen, and Jiayi Du
- ❑ A Novel Computational Model for GPUs with Application to I/O Optimal Sorting Algorithms
Atsushi Koike and Kunihiko Sadakane
- ❑ Predicting Cache Contention for Multithread Applications at Compile Time
Munara Tolubaeva, Yonghong Yan, and Barbara Chapman

Papers by Session

APDCM Session 2

- ❑ Parallelism Extraction Algorithm from Stream-Based Processing Flow Applying Spanning Tree
Guyue Wang, Shinichi Yamagiwa, and Koichi Wada
- ❑ EEWA: Energy-Efficient Workload-Aware Task Scheduling in Multi-core Architectures
Quan Chen, Long Zheng, Minyi Guo, and Zhiyi Huang
- ❑ A Platform-Specific Code Smell Alert System for High Performance Computing Applications
Chunyan Wang, Shoichi Hirasawa, Hiroyuki Takizawa, and Hiroaki Kobayashi
- ❑ Optimizing Buffer Sizes for Pipeline Workflow Scheduling with Setup Times
Anne Benoit, Jean-Marc Nicod, and Veronika Rehn-Sonigo
- ❑ WECPAR: List Ranking Algorithm and Relative Computational Power
Hatem M. El-Boghdadi

Papers by Session

APDCM Session 3

- ❑ Assessing the Impact of ABFT and Checkpoint Composite Strategies
George Bosilca, Aurelien Bouteiller, Thomas Herault, Yves Robert, and Jack Dongarra
- ❑ Memory-Aware List Scheduling for Hybrid Platforms
Julien Herrmann, Loris Marchal, and Yves Robert
- ❑ A Parallel Framework for Handling Non-determinism with Expressive Description Logics
Jocelyne Faddoul and Wendy MacCaull
- ❑ Prototyping the MBTAC Processor for the REPLICa CMP
Martti Forsell, Jussi Roivainen, and Ville Leppänen
- ❑ Evaluation of the Global Address Space Programming Interface (GASPI)
Jens Breitbart, Mareike Schmidtobreck, and Vincent Heuveline

Papers by Session

APDCM Session 4

- ❑ GPS: Towards Simplified Communication on SGL Model
Chong Li and Gaétan Hains
- ❑ Near-Optimal Location Tracking Using Sensor Networks
Gokarna Sharma, Hari Krishnan, Costas Busch, and Steven R. Brandt
- ❑ Self-Stabilizing Algorithm for Maximal 2-Packing with Safe Convergence in an Arbitrary Graph
Yihua Ding, James Z. Wang, and Pradip K. Srimani
- ❑ Minimum Set Cover of Sparsely Distributed Sensor Nodes by a Collection of Unit Disks
Satoshi Fujita
- ❑ An Efficient Implementation of the Gradient-Based Hough Transform Using DSP Slices and Block RAMs on the FPGA
Xin Zhou, Yasuaki Ito, and Koji Nakano

Papers by Session

Workshop 7: HPPAC — High-Performance, Power-Aware Computing

- HPPAC Introduction and Committees
Dong Li and Robert J. Fowler

Papers by Session

HPPAC Session 1: Power and Energy Analysis and Profiling

- ❑ Characterizing the Impact of Program Optimizations on Power and Energy for Explicit Hydrodynamics
Edgar A. León and Ian Karlin
- ❑ Application Power Signature Analysis
Chung-Hsing Hsu, Jacob Combs, Jolie Nazor, Fabian Santiago, Rachelle Thysell, Suzanne Rivoire, and Stephen W. Poole
- ❑ Metrics for Evaluating Energy Saving Techniques for Resilient HPC Systems
Ryan E. Grant, Stephen L. Olivier, James H. Laros III, Ron Brightwell, and Allan K. Porterfield

Papers by Session

HPPAC Session 2: Power-Efficient Hardware

- ❑ Reducing Static and Dynamic Power of L1 Data Caches in GPGPUs
Ehsan Atoofian
- ❑ Exploiting DMA for Performance and Energy Optimized STREAM on a DSP
Gilbert Netzer, Lennart Johnsson, Daniel Ahlin, Eric Stotzer, Pekka Varis, and Erwin Laure
- ❑ A Study of Energy and Locality Effects Using Space-Filling Curves
Nico Reissman, Jan Christian Meyer, and Magnus Jahre

Papers by Session

HPPAC Session 3: Large Scale Power Management

- ❑ Energy-Aware Load Balancing Policies for the Cloud Ecosystem
Ashkan Paya and Dan C. Marinescu
- ❑ Bag-of-Task Scheduling on Power-Aware Clusters Using a DVFS-Based Mechanism
George Terzopoulos and Helen D. Karatza
- ❑ A Criticality-Aware DVFS Runtime Utility for Optimizing Power Efficiency of Multithreaded Applications
Haibo Zhang, Wenting Han, Feng Li, Songtao He, Yichao Cheng, Hong An, and Zhitao Chen

Papers by Session

Workshop 8: HPGC — High-Performance Grid and Cloud Computing Workshop

- ❑ HPGC Introduction and Committees
Eric Aubanel, Virendrakumar C. Bhavsar, and Michael Frumkin
- ❑ HPGC Keynotes
Rajkumar Buyya and Derek Murray

Papers by Session

HPGC Session 1

- ❑ Evaluating GPU Passthrough in Xen for High Performance Cloud Computing
Andrew J. Younge, John Paul Walters, Stephen Crago, and Geoffrey C. Fox
- ❑ Scalable System Environment Caching and Sharing for Distributed Virtual Machines
Teng Long, Ilchul Yoon, Alan Sussman, Adam Porter, and Atif Memon
- ❑ Mega Data Center for Elastic Internet Applications
Hangwei Qian and Michael Rabinovich

Papers by Session

HPGC Session 2

- ❑ Cloud-Based Simulation of a Smart Power Grid
Ashkan Paya and Dan C. Marinescu
- ❑ Analyzing Reliability of Virtual Machine Instances with Dynamic Pricing in the Public Cloud
Seung-Hwan Lim, Gautam S. Thakur, and James L. Horey
- ❑ Security of Applications Involving Multiple Organizations and Order Preserving Encryption in Hybrid Cloud Environments
Mohammad Ahmadian, Ashkan Paya, and Dan C. Marinescu

Papers by Session

Workshop 9: AsHES — Accelerators and Hybrid Exascale Systems

- ❑ AsHES Introduction and Committees
Yunquan Zhang
- ❑ AsHES Keynote
Jeffrey Vetter

Papers by Session

AsHES Session 1: Programming Model and Performance Optimizations

- ❑ Scalable Critical Path Analysis for Hybrid MPI-CUDA Applications
Felix Schmitt, Robert Dietrich, and Guido Juckeland
- ❑ Dymaxion++: A Directive-Based API to Optimize Data Layout and Memory Mapping for Heterogeneous Systems
Shuai Che, Jiayuan Meng, and Kevin Skadron
- ❑ Comparison of Parallel Programming Models on Intel MIC Computer Cluster
Chenggang Lai, Zhijun Hao, Miaoqing Huang, Xuan Shi, and Haihang You
- ❑ CoAdELL: Adaptivity and Compression for Improving Sparse Matrix-Vector Multiplication on GPUs
Marco Maggioni and Tanya Berger-Wolf

Papers by Session

AsHES Session 2: Accelerating Applications

- ❑ Optimizing Krylov Subspace Solvers on Graphics Processing Units
Hartwig Anzt, William Sawyer, Stanimire Tomov, Piotr Luszczek, Ichitaro Yamazaki, and Jack Dongarra
- ❑ XSW: Accelerating Biological Database Search on Xeon Phi
Lipeng Wang, Yuandong Chan, Xiaohui Duan, Haidong Lan, Xiangxu Meng, and Weiguo Liu
- ❑ Dynamically Balanced Synchronization-Avoiding LU Factorization with Multicore and GPUs
Simplice Donfack, Stanimire Tomov, and Jack Dongarra
- ❑ Scalable Fast Multipole Accelerated Vortex Methods
Qi Hu, Nail A. Gumerov, Rio Yokota, Lorena Barba, and Ramani Duraiswami

Papers by Session

AsHES Session 3: Emerging Hybrid Systems

- ❑ Infiniband-Verbs on GPU: A Case Study of Controlling an Infiniband Network Device from the GPU
Lena Oden, Holger Fröning, and Franz-Joseph Pfreundt
- ❑ Programming the Adapteva Epiphany 64-Core Network-on-Chip Coprocessor
Anish Varghese, Bob Edwards, Gaurav Mitra, and Alistair P. Rendell
- ❑ High-Performance Zonal Histogramming on Large-Scale Geospatial Rasters Using GPUs and GPU-Accelerated Clusters
Jianting Zhang and Dali Wang

Papers by Session

Workshop 10: PLC — Programming Models, Languages, and Compilers Workshop for Manycore and Heterogeneous Architectures

- ❑ PLC Introduction and Committees
Barbara Chapman

Papers by Session

PLC Session 1: Programming and Compilation Techniques for GPUs

- ❑ Transparent GPU Execution of NumPy Applications
Troels Blum, Mads R.B. Kristensen, and Brian Vinter
- ❑ KernelGen — The Design and Implementation of a Next Generation Compiler Platform for Accelerating Numerical Models on GPUs
Dmitry Mikushin, Nikolay Likhogrud, Eddy Z. Zhang, and Christopher Bergström
- ❑ Using GPU Shared Memory with a Directive-Based Approach
Wei Ding, Ligang Lu, Mauricio Araya-Polo, Amik St-Cyr, Detlef Hohl, and Barbara M. Chapman

Papers by Session

PLC Session 2: Libraries and Optimization Frameworks

- ❑ CFD Builder: A Library Builder for Computational Fluid Dynamics
Jagan Jayaraj, Pei-Hung Lin, Paul R. Woodward, and Pen-Chung Yew
- ❑ A Stream Processing Framework for On-Line Optimization of Performance and Energy Efficiency on Heterogeneous Systems
Benjamin Ranft, Oliver Denninger, and Philip Pfafe

Papers by Session

PLC Session 3: Tools and Performance Evaluation

- ❑ OpenMP Task Scheduling Analysis via OpenMP Runtime API and Tool Visualization
Ahmad Qawasmeh, Abid M. Malik, and Barbara M. Chapman
- ❑ A Case Study in Coordination Programming: Performance Evaluation of S-Net vs Intel's Concurrent Collections
Pavel Zaichenkov, Bert Gijssbers, Clemens Grellck, Olga Tveretina, and Alex Shafarenko

Papers by Session

Workshop 11: EduPar-NSF/TCPP Workshop on Parallel and Distributed Computing Education

- ❑ EduPar Introduction and Committees
Sushil K Prasad
- ❑ EduPar Keynote
Randy H. Katz

Papers by Session

EduPar Session: Introductory Course and Across Curriculum

- ❑ Limited Time and Experience: Parallelism in CS1
Steven A. Bogaerts
- ❑ NSF/IEEE-TCPP Curriculum Implementation at the State University of Nizhni Novgorod
Viktor Gergel, Alexey Liniov, Iosif Meyerov, and Alexander Sysoyev
- ❑ Parallel and Distributed Computing across the Computer Science Curriculum
David J. John and Stan J. Thomas

Papers by Session

EduPar Session: Software Engineering Courses

- ❑ Service-Oriented Computing and Software Integration in Computing Curriculum
Yinong Chen and Zhizheng Zhou
- ❑ EA: Research-Infused Teaching of Parallel Programming Concepts
for Undergraduate Software Engineering Students
Nasser Giacaman and Oliver Sinnen
- ❑ Using Patterns to Teach Parallel Computing
Clayton Ferner, Barry Wilkinson, and Barbara Heath

Papers by Session

EduPar Session: Miscellaneous

- ❑ Teaching HDFS/MapReduce Systems Concepts to Undergraduates
Linh Bao Ngo, Edward B. Duffy, and Amy W. Apon
- ❑ Interactively Exploring the Connection between Nested Dissection Orderings for Parallel Cholesky Factorization and Vertex Separators
H. Martin Bücken and M. Ali Rostami
- ❑ A Portable Cluster for Each Student
David Toth

Papers by Session

Workshop 12: GABB — Graph Algorithms Building Blocks

- GABB Introduction

Tim Mattson, David A. Bader, Aydın Buluç, John Gilbert, Joseph Gonzalez, and Jeremy Kepner

Papers by Session

Workshop 13: PDSEC — Workshop on Parallel and Distributed Scientific and Engineering Computing

- PDSEC Introduction and Committees

Peter Strazdins, Raphaël Couturier, Michelle Mills Strout, Keita Teranishi, Thomas Rauber, Gudula Rünger, and Laurence T. Yang

Papers by Session

PDSEC Session 1: Best Papers

- ❑ **llamaOS: A Solution for Virtualized High-Performance Computing Clusters**
William A. Magato and Philip A. Wilsey
- ❑ **New Algorithm for Computing Eigenvectors of the Symmetric Eigenvalue Problem**
Azzam Haidar, Piotr Luszczek, and Jack Dongarra

Papers by Session

PDSEC Session 2: Algorithms (I)

- ❑ Exhaustive Key Search on Clusters of GPUs
Daide Barbieri, Valeria Cardellini, and Salvatore Filippone
- ❑ Application Level Fault Recovery: Using Fault-Tolerant Open MPI in a PDE Solver
Md Mohsin Ali, James Southern, Peter Strazdins, and Brendan Harding
- ❑ Nanoscale Cluster Detection in Massive Atom Probe Tomography Data
Sudip K. Seal, Srikanth B. Yoginath, and Michael K. Miller
- ❑ Construction of Porous Networks Subjected to Geometric Restrictions by Using OpenMP
Angel González Méndez, Graciela Román Alonso, Fernando Rojas González, Miguel Alfonso Castro García, Miguel Aguilar Cornejo, and Salomón Cordero Sánchez

Papers by Session

PDSEC Session 3: Systems and Performance Analysis

- ❑ Integration and Evaluation of Decentralized Fairshare Prioritization (Aequus)
Daniel Espling, Per-Olov Östberg, and Erik Elmroth
- ❑ Coordination Languages and MPI Perturbation Theory: The FOX Tuple Space Framework for Resilience
Jeremiah J. Wilke
- ❑ DisSLib: CC: A Library for Distributed Search with a Central Common Search State
Tyson Kendon and Jörg Denzinger
- ❑ Improving I/O Performance with Adaptive Data Compression for Big Data Applications
Hongbo Zou, Yongen Yu, Wei Tang, and Hsuanwei Michelle Chen
- ❑ Analysis of MPI Shared-Memory Communication Performance from a Cache Coherence Perspective
Bertrand Putigny, Benoit Ruelle, and Brice Goglin

Papers by Session

PDSEC Session 4: Algorithms (II)

- ❑ Acceleration of GPU-Based Ultrasound Simulation via Data Compression
Andrew A. Haigh and Eric C. McCreath
- ❑ Kd-Tree Based N-Body Simulations with Volume-Mass Heuristic on the GPU
Klaus Kofler, Dominik Steinhauser, Biagio Cosenza, Ivan Grasso, Sabine Schindler, and Thomas Fahringer
- ❑ Nuclear Fusion Simulation Code Optimization and Performance Evaluation on GPU Cluster
Norihisa Fujita, Hideo Nuga, Taisuke Boku, and Yasuhiro Idomura
- ❑ Acceleration of a Python-Based Tsunami Modelling Application via CUDA and OpenHMPP
Zhe Weng and Peter E. Strazdins
- ❑ GPU Enhanced Path Finding for an Unmanned Aerial Vehicle
Roksana Hossain, Sebastian Magierowski, and Geoffery G. Messier

Papers by Session

Workshop 14: DPDNS — Dependable Parallel, Distributed, and Network-Centric Systems

- ❑ DPDNS Introduction and Committees
Dimiter Avresky, Erik Maehle, and Salvatore Distefano
- ❑ DPDNS Keynote
Edgar Nett

Papers by Session

DPDNS Session: Applications

- ❑ Maintaining Dependable Communication Service for Mobile Stations in Wireless Mesh Networks by Tracking Capacity Demands
Timo Lindhorst, Burkhard Weseloh, and Edgar Nett
- ❑ A Load Balancing Behavior for Underwater Robot Swarms to Increase Mission Time and Fault Tolerance
Ammar Amory, Thomas Tosik, and Erik Maehle
- ❑ ExCovery — A Framework for Distributed System Experiments and a Case Study of Service Discovery
Andreas Dittrich, Stefan Wanja, and Miroslaw Malek
- ❑ Managing Soft-Errors in Transactional Systems
Mohamed Mohamedin, Roberto Palmieri, and Binoy Ravindran

Papers by Session

DPDNS Session: Theoretical Aspects

- ❑ Standby System Reliability through DRBD
Salvatore Distefano
- ❑ Trust-Based Security for the Spanning Tree Protocol
Yingxu Lai, Qiuyue Pan, Zenghui Liu, Yinong Chen, and Zhizheng Zhou
- ❑ Autonomy Requirements Engineering for Self-Adaptive Science Clouds
Emil Vassev and Mike Hinchey

Papers by Session

Workshop 15: MTAAP — Workshop on Multi-threaded Architectures and Applications

- ❑ MTAAP Introduction and Committees
Luiz DeRose

Papers by Session

MTAAP Session: Algorithms and Position Papers

- ❑ A New Parallel Algorithm for Two-Pass Connected Component Labeling
Siddharth Gupta, Diana Palsetia, Md. Mostofa Ali Patwary, Ankit Agrawal, and Alok Choudhary
- ❑ Position Paper: Locality-Driven Scheduling of Tasks for Data-Dependent Multithreading
Jaime Arteaga, Stephane Zuckerman, Elkin Garcia, and Guang Gao
- ❑ Position Paper: Leveraging Strength-Based Dynamic Slicing to Identify Control Reconvergence Instructions
Walid J. Ghandour and Nadine J. Ghandour

Papers by Session

MTAAP Session: Graph Analytics

- ❑ Parallel Heuristics for Scalable Community Detection
Hao Lu, Mahantesh Halappanavar, Ananth Kalyanaraman, and Sutanay Choudhury
- ❑ Hardware/Software Vectorization for Closeness Centrality on Multi-/Many-Core Architectures
Ahmet Erdem Sariyuce, Erik Saule, Kamer Kaya, and Ümit V. Çatalyürek
- ❑ Revisiting Edge and Node Parallelism for Dynamic GPU Graph Analytics
Adam McLaughlin and David A. Bader

Papers by Session

MTAAP Session: Accelerators

- ❑ A Validation Testsuite for OpenACC 1.0
Cheng Wang, Rengan Xu, Sunita Chandrasekaran, Barbara Chapman, and Oscar Hernandez
- ❑ Extracting Maximal Exact Matches on GPU
Anas Abu-Doleh, Kamer Kaya, Mohamed Abouelhoda, and Ümit V. Çatalyürek
- ❑ Predicting an Optimal Sparse Matrix Format for SpMV Computation on GPU
B. Neelima, G. Ram Mohana Reddy, and Prakash S. Raghavendra

Papers by Session

Workshop 16: LSPP — Workshop on Large-Scale Parallel Processing

- ❑ LSPP Introduction and Committees

Darren J. Kerbyson, Ram Rajamony, and Charles Weems

Papers by Session

LSPP Session 1: Performance Analysis and Optimization

- Higher Dimensional Gaussian Networks
Arash Shamaei, Bella Bose, and Mary Flahive

Papers by Session

LSPP Session 2: Modeling Performance for Scaling

- ❑ The Power-Performance Tradeoffs of the Intel Xeon Phi on HPC Applications
Bo Li, Hung-Ching Chang, Shuaiwen Song, Chun-Yi Su, Timmy Meyer, John Mooring, and Kirk W. Cameron
- ❑ Performance Modeling for Hardware Thread-Level Speculation
Ying-Chieh Wang, Che-Rung Lee, Yeh-Ching Chung, I-Hsin Chung, and Michael Perrone
- ❑ HMC-Sim: A Simulation Framework for Hybrid Memory Cube Devices
John D. Leidel and Yong Chen

Papers by Session

LSPP Session 3: Large-Scale Systems

- ❑ Online Monitoring System for Performance Fault Detection
Roberto Gioiosa, Gokcen Kestor, and Darren J. Kerbyson

Papers by Session

LSPP Session 4: Scheduling

- ❑ Towards Extreme-Scale Simulations with Next-Generation Trilinos: A Low Mach Fluid Application Case Study
Paul Lin, Matthew Bettencourt, Stefan Domino, Travis Fisher, Mark Hoemmen, Jonathan Hu, Eric Phipps, Andrey Prokopenko, Sivasankaran Rajamanickam, Christopher Siefert, Eric Cyr, and Stephen Kennon
- ❑ Design and Implementation of a Large Scale Tree-Based QR Decomposition Using a 3D Virtual Systolic Array and a Lightweight Runtime
Ichitaro Yamazaki, Jakub Kurzak, Piotr Luszczek, and Jack Dongarra
- ❑ SupMR: Circumventing Disk and Memory Bandwidth Bottlenecks for Scale-up MapReduce
Michael Sevilla, Ike Nassi, Kleoni Ioannidou, Scott Brandt, and Carlos Maltzahn

Papers by Session

Workshop 17: PCO — Parallel Computing and Optimization

- ❑ PCO Introduction and Committees
Didier El-Baz

Papers by Session

PCO Session 1: Optimization Techniques for Parallel or Distributed Architectures

- ❑ Towards Energy Efficient Allocation for Applications in Volunteer Cloud
Congfeng Jiang, Jian Wan, Christophe Cérin, Paolo Gianessi, and Yanik Ngoko
- ❑ Fast Generation of Large Task Network Mappings
Karl-Eduard Berger, François Galea, Bertrand Le Cun, and Renaud Sirdey

Papers by Session

PCO Session 2: Parallel Optimization Algorithms

- ❑ Adaptive N to P Portfolio for Solving Constraint Programming Problems on Top of the Parallel Bobpp Framework
Tarek Menouer and Bertrand Le Cun
- ❑ Dependent Walks in Parallel Local Search
Yves Caniou and Philippe Codognet
- ❑ A Parallel Large Neighborhood Search-Based Heuristic for the Disjunctively Constrained Knapsack Problem
Mhand Hifi, Stephane Negre, Toufik Saadi, Sagvan Saleh, and Lei Wu
- ❑ Solving Hard MIPLIB2003 Problems with ParaSCIP on Supercomputers: An Update
Yuji Shinano, Tobias Achterberg, Timo Berthold, Stefan Heinz, Thorsten Koch, and Michael Winkler

Papers by Session

PCO Session 3: Task Scheduling and Miscellaneous

- ❑ A Task Scheduling Algorithm Based on Replication for Maximizing Reliability on Heterogeneous Computing Systems
Shuli Wang, Kenli Li, Jing Mei, Keqin Li, and Yan Wang
- ❑ SkewControl: Gini Out of the Bottle
Si Zheng, Yunhuai Liu, Tian He, Li Shanshan, and Xiangke Liao
- ❑ The Heuristic Static Load-Balancing Algorithm Applied to the Community Earth System Model
Yuri Alexeev, Sheri Mickelson, Sven Leyffer, Robert Jacob, and Anthony Craig
- ❑ A Distributed Algorithm for a Reconfigurable Modular Surface
Didier El-Baz, Benoît Piranda, and Julien Bourgeois

Papers by Session

Workshop 18: ParLearning — Workshop on Parallel and Distributed Computing for Large Scale Machine Learning and Big Data Analytics

- ❑ ParLearning Introduction and Committees
Abhinav Vishnu and Yinglong Xia
- ❑ ParLearning Keynote
Eric P. Xing

Papers by Session

ParLearning Session 1

- ❑ Wait-Free Primitives for Initializing Bayesian Network Structure Learning on Multicore Processors
Hsuan-Yi Chu, Yinglong Xia, Anand Panangadan, and Viktor K. Prasanna
- ❑ gpuRF and gpuERT: Efficient and Scalable GPU Algorithms for Decision Tree Ensembles
Karl Jansson, Håkan Sundell, and Henrik Boström
- ❑ Training Large Scale Deep Neural Networks on the Intel Xeon Phi Many-Core Coprocessor
Lei Jin, Zhaokang Wang, Rong Gu, Chunfeng Yuan, and Yihua Huang
- ❑ Parallel Bayesian Network Modelling for Pervasive Health Monitoring System
Xiujuan Qian, Yongli Wang, and Xiaohui Jiang

Papers by Session

ParLearning Session 2

- ❑ Portfolio-Based Selection of Robust Dynamic Loop Scheduling Algorithms Using Machine Learning
Nitin Sukhija, Brandon Malone, Srishti Srivastava, Ioana Banicescu, and Florina M. Ciorba
- ❑ A General P2P Scheme for Constructing Large-Scale Virtual Environments
Wei Wang, Guisong Yang, Naixue Xiong, Xingyu He, and Wenzhong Guo

Papers by Session

ParLearning Session 3

- ❑ Large Scale Discriminative Metric Learning
Peter D. Kirchner, Matthias Boehm, Berthold Reinwald, Daby Sow, Michael Schmidt, Deepak Turaga, and Alain Biem
- ❑ YAFIM: A Parallel Frequent Itemset Mining Algorithm with Spark
Hongjian Qiu, Rong Gu, Chunfeng Yuan, and Yihua Huang
- ❑ The Empirical Research of Virtual Enterprise Knowledge Transfer's Effectiveness Faced to the Independent Innovation Ability
Yang Bo, Naixue Xiong, and Wenzhong Guo
- ❑ A Distributed Speech Algorithm for Large Scale Data Communication Systems
Naixue Xiong, Guoxiang Tong, Wenzhong Guo, Jian Tan, and Guanning Wu

Papers by Session

Workshop 19: HPDIC - High Performance Data Intensive Computing

- HPDIC Introduction and Committees
Christophe Cerin and Cong-Feng Jiang

Papers by Session

HPDIC Session 1: Memory, I/O, and Performance Enhancement

- ❑ Compactor: Optimization Framework at Staging I/O Nodes
Vishwanath Venkatesan, Mohamad Chaarawi, Quincey Koziol, and Edgar Gabriel
- ❑ Hybrid BFS Approach Using Semi-external Memory
Keita Iwabuchi, Hitoshi Sato, Ryo Mizote, Yuichiro Yasui, Katsuki Fujisawa, and Satoshi Matsuoka
- ❑ Model-Driven Data Layout Selection for Improving Read Performance
Jialin Liu, Surendra Byna, Bin Dong, Kesheng Wu, and Yong Chen

Papers by Session

HPDIC Session 2: Clustering, Data Management, and Applications

- ❑ Scalable and Reliable Data Broadcast with Cascade
Stéphane Martin, Tomasz Buchert, Pierric Willemet, Olivier Richard, Emmanuel Jeanvoine, and Lucas Nussbaum
- ❑ SOM Clustering Using Spark-MapReduce
Tugdual Sarazin, Hanane Azzag, and Mustapha Lebbah
- ❑ Optimizing the Join Operation on Hive to Accelerate Cross-Matching in Astronomy
Liang Li, Dixin Tang, Taoying Liu, Hong Liu, Wei Li, and Chenzhou Cui

Papers by Session

Workshop 20: JSSPP — Workshop on Job Scheduling Strategies for Parallel Processing

- JSSPP Introduction and Committees
Walfredo Cirne and Narayan Desai

Papers by Session

Workshop 21: CHIUW — Chapel Implementers and Users Workshop

- ❑ CHIUW Introduction and Committees
Brad Chamberlain

Papers by Author

A

- Abe, Tatsuya
- Abouelhoda, Mohamed
- Abramson, David
- Abu-Doleh, Anas
- Achterberg, Tobias
- Adly, Fatima
- Agne, Andreas
- Agrawal, Ankit
- Agrawal, Gagan
- Ahlin, Daniel
- Ahmadian, Mohammad
- Al-Ars, Zaid
- Alefragis, Panayiotis
- Alexeev, Yuri
- Al-Hammadi, Yousof
- Ali, Md Mohsin
- Ali, Shoukat
- Allen, Jonathan E.
- Almulla, Mohammed
- Alonso, Graciela Román
- Aluru, Srinivas
- Ames, Sasha
- Amory, Ammar
- An, Hong
- Anwer, Jahanzeb
- Anzt, Hartwig
- Apon, Amy W.
- Araya-Polo, Mauricio
- Arteaga, Jaime
- Asher, Yosi Ben
- Atoofian, Ehsan

Papers by Author

- Attia, Osama G.
- Aubanel, Eric
- Avresky, Dimiter
- Azzag, Hanane

B

- Bader, David A.
- Baehr, Steffen
- Banicescu, Ioana
- Barba, Lorena
- Barbieri, Davide
- Becker, Juergen
- Becker, Jürgen
- Begcy, Kevin
- Behera, Sairam
- Bellini, Riccardo
- Benoit, Anne
- Benza, Silvia
- Berger, Karl-Eduard
- Berger-Wolf, Tanya
- Bergström, Christopher
- Bertels, Koen
- Berthold, Timo
- Bertholon, Benoît
- Bettencourt, Matthew
- Bhavsar, Virendrakumar C.
- Biem, Alain
- Blum, Troels
- Bo, Yang
- Boehm, Matthias
- Boelmann, Christopher
- Bogaerts, Steven A.
- Boku, Taisuke

Papers by Author

- Bose, Bella
- Bosilca, George
- Boström, Henrik
- Bouganis, Christos-Savvas
- Boukerche, Azzedine
- Bourgeois, Julien
- Bouteiller, Aurelien
- Bouvry, Pascal
- Brandt, Scott
- Brandt, Steven R.
- Breitbart, Jens
- Brightwell, Ron
- Buchert, Tomasz
- Bücken, H. Martin
- Buluç, Aydın
- Busch, Costas

- Buyya, Rajkumar
- Byna, Surendra

C

- Cameron, Kirk W.
- Campbell, Malachy
- Caniou, Yves
- Cao, Peng
- Cardellini, Valeria
- Cardoso, João M. P.
- Cattaneo, Riccardo
- Cavazos, John
- Cerin, Christophe
- Cérin, Christophe
- Chaarawi, Mohamad
- Chamberlain, Brad
- Chan, Yuandong

Papers by Author

- Chandrasekaran, Sunita
- Chang, Hung-Ching
- Chapman, Barbara
- Chapman, Barbara M.
- Che, Shuai
- Chen, Hang
- Chen, Hsuanwei Michelle
- Chen, Linchuan
- Chen, Quan
- Chen, Yinong
- Chen, Yong
- Chen, Zhitao
- Cheng, Huang
- Cheng, Yichao
- Choudhary, Alok
- Choudhury, Sutanay
- Chu, Hsuan-Yi
- Chung, I-Hsin
- Chung, Yeh-Ching
- Ciorba, Florina M.
- Cirne, Walfredo
- Clauss, Carsten
- Codognet, Philippe
- Combs, Jacob
- Copper, Jack
- Cornejo, Miguel Aguilar
- Cosenza, Biagio
- Couturier, Raphaël
- Crago, Stephen
- Craig, Anthony
- Cui, Chenzhou
- Cun, Bertrand Le

Papers by Author

Cyr, Eric

Ç

Çatalyürek, Ümit V.

d

de Oliveira, Daniel

D

DeFlumere, Ashley

Denninger, Oliver

Denzinger, Jörg

Deogun, Jitender S.

DeRose, Luiz

Desai, Narayan

Dias, Jonas

Dietrich, Robert

Ding, Wei

Ding, Yihua

Distefano, Salvatore

Dittrich, Andreas

Domino, Stefan

Donfack, Simplicé

Dong, Bin

Dongarra, Jack

Du, Jiayi

Duan, Xiaohui

Duarte, Rui Policarpo

Duffy, Edward B.

Duraiswami, Ramani

Durelli, Gianluca

Dutta, Sankha Baran

Papers by Author

E

- Edwards, Bob
- El-Baz, Didier
- El-Boghdadi, Hatem M.
- Elmroth, Erik
- Emeretlis, Andreas
- Espling, Daniel
- Essafi, Adel
- Eyraud-Dubois, Lionel

F

- Faddoul, Jocelyne
- Fahringer, Thomas
- Faverge, Mathieu
- Fei, Xin
- Fei, Yunsi

- Ferner, Clayton
- Filippone, Salvatore
- Fisher, Travis
- Flahive, Mary
- Fontaine, Florent
- Forsell, Martti
- Fowler, Robert J.
- Fox, Geoffrey C.
- Friese, Ryan
- Friesen, Marcia
- Fröning, Holger
- Frumkin, Michael
- Fujimori, Takumi
- Fujisawa, Katsuki
- Fujita, Norihisa
- Fujita, Satoshi

Papers by Author

Fuser, A.

Fuser, Alain

G

Gabriel, Edgar

Galea, François

Gallatin, Doug

Ganduri, Rajasekhar

Ganesan, Narayan

Gao, Guang

Garcia, Elkin

García, Miguel Alfonso Castro

Gasior, Jakub

Gergel, Viktor

Gerndt, Michael

Ghandour, Nadine J.

Ghandour, Walid J.

Giacaman, Nasser

Gianessi, Paolo

Gijsbers, Bert

Gilbert, John

Gioiosa, Roberto

Goglin, Brice

Gokhale, Maya

Gokhale, Maya B.

Goll, Christian

Gong, Bin

González, Fernando Rojas

Gonzalez, Joseph

Grant, Ryan E.

Grasso, Ivan

Greaves, David

Grelck, Clemens

Papers by Author

- Gu, Rong
- Gumerov, Nail A.
- Guo, Minyi
- Guo, Wenzhong
- Gupta, Rajiv
- Gupta, Siddharth
- Gustedt, Jens

H

- Haidar, Azzam
- Haigh, Andrew A.
- Hains, Gaétan
- Halappanavar, Mahantesh
- Hamidouche, Khaled
- Han, Wenting
- Hao, Zhijun
- Harding, Brendan
- He, Songtao
- He, Tian
- He, Xingyu
- Heath, Barbara
- Heinz, Stefan
- Herault, Thomas
- Hernandez, Oscar
- Herrmann, Julien
- Heuveline, Vincent
- Hifi, Mhand
- Hilton, Marcia
- Hinchey, Mike
- Hirasawa, Shoichi
- Hiroyuki, Sato
- Hochberger, Christian
- Hoemmen, Mark

Papers by Author

- Hohl, Detlef
- Horey, James L.
- Hossain, Roksana
- Hsu, Chung-Hsing
- Hu, Jonathan
- Hu, Qi
- Huang, Kun
- Huang, Miaoqing
- Huang, Yihua
- Huang, Zhiyi
- Hübner, Michael
- Huo, Xin
- Hysom, David A.

I

- Ibarra, Oscar H.
- Idomura, Yasuhiro

- Inagi, Masato
- Inoue, Keisuke
- Ioannidou, Kleoni
- Ito, Yasuaki
- Iwabuchi, Keita

J

- Jacob, Robert
- Jahre, Magnus
- Jansson, Karl
- Jayaraj, Jagan
- Jeanvoine, Emmanuel
- Jiang, Congfeng
- Jiang, Cong-Feng
- Jiang, Xiaohui
- Jin, Lei
- John, David J.

Papers by Author

- Johnson, Tyler
- Johnsson, Lennart
- Jones, Philip
- Jordan, K.E.
- Jose, Jithin
- Juckeland, Guido

K

- Kachris, Christoforos
- Kalyanaraman, Ananth
- Kaneko, Mineo
- Karatza, Helen D.
- Karlin, Ian
- Katz, Randy H.
- Kaya, Kamer
- Keen, Aaron
- Kendon, Tyson
- Kennon, Stephen
- Kepner, Jeremy
- Kerbyson, Darren J.
- Kessaci, Yacine
- Kestor, Gokcen
- Khadraoui, Djamel
- Khan, Danish Anis
- Khemka, Bhavesh
- Kim, C.S.
- Kirchner, Peter D.
- Kobayashi, Hiroaki
- Koch, Thorsten
- Koduru, Sai Charan
- Koenig, Gregory A.
- Kofler, Klaus
- Koike, Atsushi

Papers by Author

- Koziol, Quincey
- Kozlov, Alexey M.
- Krishnan, Hari
- Kristensen, Mads R.B.
- Kurzak, Jakub

L

- Lacoste, Xavier
- Lai, Chenggang
- Lai, Yingxu
- Lan, Haidong
- Lankes, Stefan
- Laros III, James H.
- Larson, Stephen
- Lastovetsky, Alexey
- Laure, Erwin
- Le Cun, Bertrand
- Lebbah, Mustapha
- Lee, Che-Rung
- Lee, Jongeun
- Leidel, John D.
- León, Edgar A.
- Leong, Philip
- Leppänen, Ville
- Leyffer, Sven
- Li, Bo
- Li, Chong
- Li, Dong
- Li, Feng
- Li, Jie
- Li, Kenli
- Li, Keqin
- Li, Liang

Papers by Author

- Li, Wei
- Liao, Xiangke
- Likhogrud, Nikolay
- Lim, Seung-Hwan
- Lin, Paul
- Lin, Pei-Hung
- Lindhorst, Timo
- Liniov, Alexey
- Liu, Bo
- Liu, Hong
- Liu, Jialin
- Liu, Leibo
- Liu, Taoying
- Liu, Weifeng
- Liu, Weiguo
- Liu, Yunhuai

- Liu, Zenghui
- Lloyd, G. Scott
- Long, Teng
- Lu, Hao
- Lu, Ligang
- Lukarski, Dimitar
- Lund, Simon A.F.
- Luo, Xiaozhong
- Lupo, Chris
- Luszczek, Piotr

M

- M., Mario A. Chapa
- MacCaull, Wendy
- Maciejewski, Anthony A.
- Maeda, Toshiyuki
- Maehle, Erik

Papers by Author

- Magato, William A.
- Maggioni, Marco
- Magierowski, Sebastian
- Mai, Toan X.
- Malek, Alirad
- Malek, Miroslaw
- Malik, Abid M.
- Malik, Tania
- Malladi, Mukund
- Malone, Brandon
- Maltzahn, Carlos
- Marchal, Loris
- Marinescu, Dan C.
- Marques, Eduardo
- Martin, Stéphane
- Matsuoka, Satoshi
- Mattoso, Marta
- Mattson, Tim
- McCreath, Eric C.
- McLaughlin, Adam
- McLeod, Robert
- Mehta, Gayatri
- Mei, Chen
- Mei, Jing
- Meisner, Sebastian
- Melab, Nouredine
- Mellor-Crummey, John
- Melo, Alba Cristina Magalhaes Alves de
- Memon, Atif
- Méndez, Angel González
- Meng, Jiayuan
- Meng, Xiangxu

Papers by Author

- Menouer, Tarek
- Mercier, Patrick
- Messier, Geoffery G.
- Meyer, Jan Christian
- Meyer, Timmy
- Meyerov, Iosif
- Mickelson, Sheri
- Mikushin, Dmitry
- Miller, Michael K.
- Mitra, Gaurav
- Mizote, Ryo
- Mohamedin, Mohamed
- Mooring, John
- Muhaidat, Sami
- Murray, Derek

N

- Nagayama, Shinobu
- Nakano, Koji
- Nakib, A.
- Nassi, Ike
- Nazor, Jolie
- Neelima, B.
- Negre, Stephane
- Nett, Edgar
- Netzer, Gilbert
- Ngo, Linh Bao
- Ngoko, Yanik
- Nicod, Jean-Marc
- Nishiyama, Hiroki
- Nuga, Hideo
- Nussbaum, Lucas

Papers by Author

O

- Ocaña, Kary
- Oden, Lena
- Oliveira, Cristiano Bacelar de
- Oliver, John
- Olivier, Stephen L.
- Ozer, Gulcin

Ö

- Östberg, Per-Olov

P

- Palmieri, Roberto
- Palsetia, Diana
- Pan, Qiuyue
- Panangadan, Anand

- Panda, Dhableswar K. (DK)
- Pasricha, Sudeep
- Patwary, Md. Mostofa Ali
- Pau, Danilo
- Pavlic, Theodore P.
- Pavlovikj, Natasha
- Paya, Ashkan
- Perrone, Michael
- Pfaffe, Philip
- Pfreundt, Franz-Joseph
- Pham-Quoc, Cuong
- Phipps, Eric
- Pickartz, Simon
- Pilato, Christian
- Pionteck, Thilo
- Piranda, Benoît

Papers by Author

- Platzner, Marco
- Ponpandi, Swamy D.
- Poole, Stephen W.
- Poole, Steve
- Porter, Adam
- Porterfield, Allan K.
- Powers, Sarah
- Prasad, Sushil K
- Prasanna, Viktor K.
- Prokopenko, Andrey
- Putigny, Bertrand

Q

- Qawasmeh, Ahmad
- Qian, Hangwei
- Qian, Xiujuan
- Qiu, Hongjian

- Quintin, Jean-Noël

R

- Rabinovich, Michael
- Raghavendra, Prakash S.
- Raitza, Michael
- Rajamanickam, Sivasankaran
- Rajamony, Ram
- Rambharos, Rajendra
- Ramet, Pierre
- Ranft, Benjamin
- Rauber, Thomas
- Ravindran, Binoy
- Reble, Pablo
- Reddy, G. Ram Mohana
- Rehn-Sonigo, Veronika
- Reinwald, Berthold

Papers by Author

- Reisner, Marc
- Reissman, Nico
- Rendell, Alistair P.
- Richard, Olivier
- Rivoire, Suzanne
- Robert, Yves
- Roivainen, Jussi
- Rostami, M. Ali
- Ruelle, Benoit
- Ruiz, Patricia
- Runger, Gudula
- Rychkov, Vladimir

S

- Saadi, Toufik
- Sachdeva, V.
- Sadakane, Kunihiro

- Saleh, Sagvan
- Saligehdar, Amin
- Salscheider, Niels Ole
- Sanchez, Salom3n Cordero
- Sander, Oliver
- Sandmann, Timo
- Santambrogio, Marco
- Santambrogio, Marco D.
- Santiago, Fabian
- Santos, Juan Carlos Mart3nez
- Sarazin, Tugdual
- Sariyuce, Ahmet Erdem
- Sass, Ron
- Sato, Hitoshi
- Saule, Erik
- Sawyer, William

Papers by Author

- Schindler, Sabine
- Schmidt, Bernhard
- Schmidt, Michael
- Schmidtbreick, Mareike
- Schmitt, Felix
- Schwiegelshohn, Uwe
- Schwittmann, Lorenz
- Sciuto, Donatella
- Seal, Sudip K.
- Seredynski, Franciszek
- Seredynski, Marcin
- Sevilla, Michael
- Shafarenko, Alex
- Shamaei, Arash
- Shanshan, Li
- Sharma, Gokarna
- Shi, Xuan
- Shinano, Yuji
- Shirazi, Behrooz
- Sidiropoulos, Harry
- Siefert, Christopher
- Siegel, Howard Jay
- Sinnen, Oliver
- Siozios, Kostas
- Sirdey, Renaud
- Sistla, AnilKumar
- Skadron, Kevin
- Skovhede, Kenneth
- Smaragdos, Georgios
- Song, Shuaiwen
- Soudris, Dimitrios
- Sourdis, Ioannis

Papers by Author

- Southern, James
- Sow, Daby
- Spada, Fabrizio
- Srimani, Pradip K.
- Srivastava, Srishti
- Stamatakis, Alexandros
- St-Cyr, Amik
- Stein, Esti
- Steinhäuser, Dominik
- Stotzer, Eric
- Strazdins, Peter
- Strazdins, Peter E.
- Strout, Michelle Mills
- Strydis, Christos
- Su, Chun-Yi
- Sukhija, Nitin

- Sundell, Håkan
- Sussman, Alan
- Sysoyev, Alexander
- Szczypiorski, Krzysztof

T

- Takafuji, Daisuke
- Takizawa, Hiroyuki
- Talbi, El-Ghazali
- Tan, Jian
- Tang, Dixin
- Tang, Guangping
- Tang, Wei
- Tani, Kazuya
- Tarakji, Ayman
- Teich, Jürgen
- Teranishi, Keita

Papers by Author

- Terzopoulos, George
- Thakur, Gautam S.
- Theodoridis, George
- Thibault, Samuel
- Thomas, Stan J.
- Thysell, Rachelle
- Tolubaeva, Munara
- Tomkos, Ioannis
- Tomov, Stanimire
- Tong, Guoxiang
- Torresen, Jim
- Tørresen, Jim
- Tosik, Thomas
- Toth, David
- Townsend, Kevin
- Tretyakova, Antonina

- Trystram, Denis
- Turaga, Deepak
- Tveretina, Olga
- Tyagi, Akhilesh
- Tzilis, Stavros
- Tzimpragos, Georgios

U

- Ureña, Isaías A. Comprés
- Uznański, Przemysław

V

- Vaidyanathan, Ramachandran
- Varghese, Anish
- Varis, Pekka
- Varrette, Sébastien
- Vassev, Emil

Papers by Author

- Venkatesan, Vishwanath
- Venkatesh, Akshay
- Vetter, Jeffrey
- Vialle, Stephane
- Vinter, Brian
- Vishnu, Abhinav
- Vogt, Markus
- Vora, Keval
- Voros, Nikolaos
- Vu, Duy Viet

W

- Wada, Koichi
- Wakabayashi, Shin'ichi
- Walia, Harkamal
- Walstrom, Joshua
- Walters, John Paul

- Wan, Jian
- Wang, Cheng
- Wang, Chunyan
- Wang, Dali
- Wang, Guyue
- Wang, James Z.
- Wang, Lipeng
- Wang, Shuli
- Wang, Wei
- Wang, Xinying
- Wang, Yan
- Wang, Yi
- Wang, Ying-Chieh
- Wang, Yongli
- Wang, Zhaokang
- Wanja, Stefan

Papers by Author

- Watanabe, Minoru
- Weems, Charles
- Wei, Lai
- Weis, Torben
- Weng, Zhe
- Weseloh, Burkhard
- Wilke, Jeremiah J.
- Wilkinson, Barry
- Willemet, Pierric
- Wilsey, Philip A.
- Winkler, Michael
- Winn, M.D.
- Wold, Alexander
- Woodward, Paul R.
- Wu, Guanning
- Wu, Kesheng

- Wu, Lei

X

- Xia, Yinglong
- Xing, Eric P.
- Xiong, Naixue
- Xu, Rengan

Y

- Yamagiwa, Shinichi
- Yamazaki, Ichitaro
- Yan, Yonghong
- Yang, Guisong
- Yang, Laurence T.
- Yantir, Hasan Erdem
- Yasui, Yuichiro
- Yew, Pen-Chung

Papers by Author

- Yoginath, Srikanth B.
- Yokota, Rio
- Yoo, Paul D.
- Yoon, Ilchul
- You, Haihang
- Younge, Andrew J.
- Yu, Yongen
- Yuan, Chunfeng
- Yurdakul, Arda

Z

- Zaichenkov, Pavel
- Zaidi, Ali Mustafa
- Zaidi, Zied
- Zambreno, Joseph
- Zhang, Eddy Z.
- Zhang, Haibo
- Zhang, Jianting
- Zhang, Jie
- Zhang, Yang
- Zhang, Yunquan
- Zhang, Zhang
- Zhao, Kun
- Zheng, Long
- Zheng, Si
- Zhou, Xin
- Zhou, Zhizheng
- Ziener, Daniel
- Zola, Jaroslaw
- Zou, Hongbo
- Zuckerman, Stephane

Papers by Author

Abe, Tatsuya

- ❑ A General Model Checking Framework for Various Memory Consistency Models

Abouelhoda, Mohamed

- ❑ Extracting Maximal Exact Matches on GPU

Abramson, David

- ❑ HCW 2014 Keynote Talk

Abu-Doleh, Anas

- ❑ Extracting Maximal Exact Matches on GPU

Achterberg, Tobias

- ❑ Solving Hard MIPLIB2003 Problems with ParaSCIP on Supercomputers: An Update

Papers by Author

Adly, Fatima

- ❑ Machine-Learning-Based Identification of Defect Patterns in Semiconductor Wafer Maps: An Overview and Proposal

Agne, Andreas

- ❑ Module Placement Using Constraint Programming in Run-Time Reconfigurable Systems

Agrawal, Ankit

- ❑ A New Parallel Algorithm for Two-Pass Connected Component Labeling

Agrawal, Gagan

- ❑ Scheduling Methods for Accelerating Applications on Architectures with Heterogeneous Cores
- ❑ Removing Sequential Bottlenecks in Analysis of Next-Generation Sequencing Data

Papers by Author

Ahlin, Daniel

- ❑ Exploiting DMA for Performance and Energy Optimized STREAM on a DSP

Ahmadian, Mohammad

- ❑ Security of Applications Involving Multiple Organizations and Order Preserving Encryption in Hybrid Cloud Environments

Al-Ars, Zaid

- ❑ Automated Hybrid Interconnect Design for FPGA Accelerators Using Data Communication Profiling

Alefragis, Panayiotis

- ❑ A Hybrid ILP-CP Model for Mapping Directed Acyclic Task Graphs to Multicore Architectures

Papers by Author

Alexeev, Yuri

- ❑ The Heuristic Static Load-Balancing Algorithm Applied to the Community Earth System Model

Al-Hammadi, Yousof

- ❑ Machine-Learning-Based Identification of Defect Patterns in Semiconductor Wafer Maps: An Overview and Proposal

Ali, Md Mohsin

- ❑ Application Level Fault Recovery: Using Fault-Tolerant Open MPI in a PDE Solver

Ali, Shoukat

- ❑ Message from the HCW Program Chair

Allen, Jonathan E.

- ❑ Design and Optimization of a Metagenomics Analysis Workflow for NVRAM

Papers by Author

Almulla, Mohammed

- ❑ A Genetic Algorithm-Based Sparse Coverage over Urban VANETs

Alonso, Graciela Román

- ❑ Construction of Porous Networks Subjected to Geometric Restrictions by Using OpenMP

Aluru, Srinivas

- ❑ HiCOMB Introduction and Committees

Ames, Sasha

- ❑ Design and Optimization of a Metagenomics Analysis Workflow for NVRAM

Amory, Ammar

- ❑ A Load Balancing Behavior for Underwater Robot Swarms to Increase Mission Time and Fault Tolerance

Papers by Author

An, Hong

- ❑ A Criticality-Aware DVFS Runtime Utility for Optimizing Power Efficiency of Multithreaded Applications

Anwer, Jahanzeb

- ❑ FPGA Redundancy Configurations: An Automated Design Space Exploration

Anzt, Hartwig

- ❑ Hybrid Multi-elimination ILU Preconditioners on GPUs
- ❑ Optimizing Krylov Subspace Solvers on Graphics Processing Units

Apon, Amy W.

- ❑ Teaching HDFS/MapReduce Systems Concepts to Undergraduates

Araya-Polo, Mauricio

- ❑ Using GPU Shared Memory with a Directive-Based Approach

Papers by Author

Arteaga, Jaime

- Position Paper: Locality-Driven Scheduling of Tasks for Data-Dependent Multithreading

Asher, Yosi Ben

- Adaptive Booth Algorithm for Three-Integers Multiplication for Reconfigurable Mesh

Atoofian, Ehsan

- Reducing Static and Dynamic Power of L1 Data Caches in GPGPUs

Attia, Osama G.

- CyGraph: A Reconfigurable Architecture for Parallel Breadth-First Search

Aubanel, Eric

- HPGC Introduction and Committees

Papers by Author

Avresky, Dimiter

- DPDNS Introduction and Committees

Azzag, Hanane

- SOM Clustering Using Spark-MapReduce

Bader, David A.

- HiCOMB Introduction and Committees
- GABB Introduction
- Revisiting Edge and Node Parallelism for Dynamic GPU Graph Analytics

Baehr, Steffen

- Virtualization Support for FPGA-Based Coprocessors Connected via PCI Express to an Intel Multicore Platform

Papers by Author

Banicescu, Ioana

- Portfolio-Based Selection of Robust Dynamic Loop Scheduling Algorithms Using Machine Learning

Barba, Lorena

- Scalable Fast Multipole Accelerated Vortex Methods

Barbieri, Davide

- Exhaustive Key Search on Clusters of GPUs

Becker, Juergen

- Virtualization Support for FPGA-Based Coprocessors Connected via PCI Express to an Intel Multicore Platform

Becker, Jürgen

- RAW Introduction and Committees

Papers by Author

Begcy, Kevin

- ❑ A Comparison of a Campus Cluster and Open Science Grid Platforms for Protein-Guided Assembly Using Pegasus Workflow Management System

Behera, Sairam

- ❑ A Comparison of a Campus Cluster and Open Science Grid Platforms for Protein-Guided Assembly Using Pegasus Workflow Management System

Bellini, Riccardo

- ❑ PaRA-Sched: A Reconfiguration-Aware Scheduler for Reconfigurable Architectures

Benoit, Anne

- ❑ Optimizing Buffer Sizes for Pipeline Workflow Scheduling with Setup Times

Papers by Author

Benza, Silvia

- ❑ Exploring Large Scale Receptor-Ligand Pairs in Molecular Docking Workflows in HPC Clouds

Berger, Karl-Eduard

- ❑ Fast Generation of Large Task Network Mappings

Berger-Wolf, Tanya

- ❑ CoAdELL: Adaptivity and Compression for Improving Sparse Matrix-Vector Multiplication on GPUs

Bergström, Christopher

- ❑ KernelGen — The Design and Implementation of a Next Generation Compiler Platform for Accelerating Numerical Models on GPUs

Papers by Author

Bertels, Koen

- ❑ Automated Hybrid Interconnect Design for FPGA Accelerators Using Data Communication Profiling

Berthold, Timo

- ❑ Solving Hard MIPLIB2003 Problems with ParaSCIP on Supercomputers: An Update

Bertholon, Benoît

- ❑ Comparison of Multi-objective Optimization Algorithms for the JShadObf JavaScript Obfuscator

Bettencourt, Matthew

- ❑ Towards Extreme-Scale Simulations with Next-Generation Trilinos: A Low Mach Fluid Application Case Study

Papers by Author

Bhavsar, Virendrakumar C.

- HPGC Introduction and Committees

Biem, Alain

- Large Scale Discriminative Metric Learning

Blum, Troels

- Bohrium: A Virtual Machine Approach to Portable Parallelism
- Transparent GPU Execution of NumPy Applications

Bo, Yang

- The Empirical Research of Virtual Enterprise Knowledge Transfer's Effectiveness Faced to the Independent Innovation Ability

Boehm, Matthias

- Large Scale Discriminative Metric Learning

Papers by Author

Boelmann, Christopher

- Deterministic Synchronization of Multi-threaded Programs with Operational Transformation

Bogaerts, Steven A.

- Limited Time and Experience: Parallelism in CS1

Boku, Taisuke

- Nuclear Fusion Simulation Code Optimization and Performance Evaluation on GPU Cluster

Bose, Bella

- Higher Dimensional Gaussian Networks

Papers by Author

Bosilca, George

- ❑ Taking Advantage of Hybrid Systems for Sparse Direct Solvers via Task-Based Runtimes
- ❑ Assessing the Impact of ABFT and Checkpoint Composite Strategies

Boström, Henrik

- ❑ gpuRF and gpuERT: Efficient and Scalable GPU Algorithms for Decision Tree Ensembles

Bouganis, Christos-Savvas

- ❑ Over-clocking of Linear Projection Designs through Device Specific Optimisations

Boukerche, Azzedine

- ❑ A Genetic Algorithm-Based Sparse Coverage over Urban VANETs

Papers by Author

Bourgeois, Julien

- ❑ A Distributed Algorithm for a Reconfigurable Modular Surface

Bouteiller, Aurelien

- ❑ Assessing the Impact of ABFT and Checkpoint Composite Strategies

Bouvry, Pascal

- ❑ NIDISC Introduction and Committees
- ❑ Graph-Based Cellular Automata Approach to Maximum Lifetime Coverage Problem in Wireless Sensor Networks
- ❑ Comparison of Multi-objective Optimization Algorithms for the JShadObf JavaScript Obfuscator

Brandt, Scott

- ❑ SupMR: Circumventing Disk and Memory Bandwidth Bottlenecks for Scale-up MapReduce

Papers by Author

Brandt, Steven R.

- ❑ Near-Optimal Location Tracking Using Sensor Networks

Breitbart, Jens

- ❑ Evaluation of the Global Address Space Programming Interface (GASPI)

Brightwell, Ron

- ❑ Metrics for Evaluating Energy Saving Techniques for Resilient HPC Systems

Buchert, Tomasz

- ❑ Scalable and Reliable Data Broadcast with Cascade

Bücker, H. Martin

- ❑ Interactively Exploring the Connection between Nested Dissection Orderings for Parallel Cholesky Factorization and Vertex Separators

Papers by Author

Buluç, Aydın

- GABB Introduction

Busch, Costas

- Near-Optimal Location Tracking Using Sensor Networks

Buyya, Rajkumar

- HPGC Keynotes

Byna, Surendra

- Model-Driven Data Layout Selection for Improving Read Performance

Cameron, Kirk W.

- The Power-Performance Tradeoffs of the Intel Xeon Phi on HPC Applications

Papers by Author

Campbell, Malachy

- ❑ A Comparison of a Campus Cluster and Open Science Grid Platforms for Protein-Guided Assembly Using Pegasus Workflow Management System

Caniou, Yves

- ❑ Dependent Walks in Parallel Local Search

Cao, Peng

- ❑ Hierarchical Pipeline Optimization of Coarse Grained Reconfigurable Processor for Multimedia Applications

Cardellini, Valeria

- ❑ Exhaustive Key Search on Clusters of GPUs

Cardoso, João M. P.

- ❑ High-Level Synthesis from C vs. a DSL-Based Approach

Papers by Author

Çatalyürek, Ümit V.

- HiCOMB Keynote and Invited Talks
- Hardware/Software Vectorization for Closeness Centrality on Multi-/Many-Core Architectures
- Extracting Maximal Exact Matches on GPU

Cattaneo, Riccardo

- Adaptive Raytracing Implementation Using Partial Dynamic Reconfiguration
- PaRA-Sched: A Reconfiguration-Aware Scheduler for Reconfigurable Architectures

Cavazos, John

- HIPS Introduction and Committees

Cerin, Christophe

- HPDIC Introduction and Committees

Papers by Author

Cérin, Christophe

- ❑ Towards Energy Efficient Allocation for Applications in Volunteer Cloud

Chaarawi, Mohamad

- ❑ Compactor: Optimization Framework at Staging I/O Nodes

Chamberlain, Brad

- ❑ CHIUW Introduction and Committees

Chan, Yuandong

- ❑ XSW: Accelerating Biological Database Search on Xeon Phi

Chandrasekaran, Sunita

- ❑ A Validation Testsuite for OpenACC 1.0

Papers by Author

Chang, Hung-Ching

- ❑ The Power-Performance Tradeoffs of the Intel Xeon Phi on HPC Applications

Chapman, Barbara

- ❑ Predicting Cache Contention for Multithread Applications at Compile Time
- ❑ PLC Introduction and Committees
- ❑ A Validation Testsuite for OpenACC 1.0

Chapman, Barbara M.

- ❑ Using GPU Shared Memory with a Directive-Based Approach
- ❑ OpenMP Task Scheduling Analysis via OpenMP Runtime API and Tool Visualization

Che, Shuai

- ❑ Dymaxion++: A Directive-Based API to Optimize Data Layout and Memory Mapping for Heterogeneous Systems

Papers by Author

Chen, Hang

- ❑ A Hybrid Parallel Tridiagonal Solver on Multi-core Architectures

Chen, Hsuanwei Michelle

- ❑ Improving I/O Performance with Adaptive Data Compression for Big Data Applications

Chen, Linchuan

- ❑ Scheduling Methods for Accelerating Applications on Architectures with Heterogeneous Cores

Chen, Quan

- ❑ EEWA: Energy-Efficient Workload-Aware Task Scheduling in Multi-core Architectures

Chen, Yinong

- ❑ Service-Oriented Computing and Software Integration in Computing Curriculum

Papers by Author

- ❑ Trust-Based Security for the Spanning Tree Protocol

Chen, Yong

- ❑ HMC-Sim: A Simulation Framework for Hybrid Memory Cube Devices
- ❑ Model-Driven Data Layout Selection for Improving Read Performance

Chen, Zhitao

- ❑ A Criticality-Aware DVFS Runtime Utility for Optimizing Power Efficiency of Multithreaded Applications

Cheng, Huang

- ❑ A Genetic Algorithm-Based Sparse Coverage over Urban VANETs

Cheng, Yichao

- ❑ A Criticality-Aware DVFS Runtime Utility for Optimizing Power Efficiency of Multithreaded Applications

Papers by Author

Choudhary, Alok

- ❑ A New Parallel Algorithm for Two-Pass Connected Component Labeling

Choudhury, Sutanay

- ❑ Parallel Heuristics for Scalable Community Detection

Chu, Hsuan-Yi

- ❑ Wait-Free Primitives for Initializing Bayesian Network Structure Learning on Multicore Processors

Chung, I-Hsin

- ❑ Performance Modeling for Hardware Thread-Level Speculation

Chung, Yeh-Ching

- ❑ Performance Modeling for Hardware Thread-Level Speculation

Papers by Author

Ciorba, Florina M.

- Portfolio-Based Selection of Robust Dynamic Loop Scheduling Algorithms Using Machine Learning

Cirne, Walfredo

- JSSPP Introduction and Committees

Clauss, Carsten

- SWIFT: A Transparent and Flexible Communication Layer for PCIe-Coupled Accelerators and (Co-)Processors

Codognet, Philippe

- Dependent Walks in Parallel Local Search

Combs, Jacob

- Application Power Signature Analysis

Papers by Author

Copper, Jack

- ❑ Data Quality, Consistency, and Interpretation Management for Wind Farms by Using Neural Networks

Cornejo, Miguel Aguilar

- ❑ Construction of Porous Networks Subjected to Geometric Restrictions by Using OpenMP

Cosenza, Biagio

- ❑ Kd-Tree Based N-Body Simulations with Volume-Mass Heuristic on the GPU

Couturier, Raphaël

- ❑ PDSEC Introduction and Committees

Crago, Stephen

- ❑ Evaluating GPU Passthrough in Xen for High Performance Cloud Computing

Papers by Author

Craig, Anthony

- ❑ The Heuristic Static Load-Balancing Algorithm Applied to the Community Earth System Model

Cui, Chenzhou

- ❑ Optimizing the Join Operation on Hive to Accelerate Cross-Matching in Astronomy

Cun, Bertrand Le

- ❑ Adaptive N to P Portfolio for Solving Constraint Programming Problems on Top of the Parallel Bobpp Framework

Cyr, Eric

- ❑ Towards Extreme-Scale Simulations with Next-Generation Trilinos: A Low Mach Fluid Application Case Study

Papers by Author

de Oliveira, Daniel

- ❑ Exploring Large Scale Receptor-Ligand Pairs in Molecular Docking Workflows in HPC Clouds

DeFlumere, Ashley

- ❑ Searching for the Optimal Data Partitioning Shape for Parallel Matrix Matrix Multiplication on 3 Heterogeneous Processors

Denninger, Oliver

- ❑ A Stream Processing Framework for On-Line Optimization of Performance and Energy Efficiency on Heterogeneous Systems

Denzinger, Jörg

- ❑ DisSLib: CC: A Library for Distributed Search with a Central Common Search State

Papers by Author

Deogun, Jitender S.

- ❑ A Comparison of a Campus Cluster and Open Science Grid Platforms for Protein-Guided Assembly Using Pegasus Workflow Management System

DeRose, Luiz

- ❑ MTAAP Introduction and Committees

Desai, Narayan

- ❑ JSSPP Introduction and Committees

Dias, Jonas

- ❑ Exploring Large Scale Receptor-Ligand Pairs in Molecular Docking Workflows in HPC Clouds

Dietrich, Robert

- ❑ Scalable Critical Path Analysis for Hybrid MPI-CUDA Applications

Papers by Author

Ding, Wei

- ❑ Using GPU Shared Memory with a Directive-Based Approach

Ding, Yihua

- ❑ Self-Stabilizing Algorithm for Maximal 2-Packing with Safe Convergence in an Arbitrary Graph

Distefano, Salvatore

- ❑ DPDNS Introduction and Committees
- ❑ Standby System Reliability through DRBD

Dittrich, Andreas

- ❑ ExCovery — A Framework for Distributed System Experiments and a Case Study of Service Discovery

Papers by Author

Domino, Stefan

- ❑ Towards Extreme-Scale Simulations with Next-Generation Trilinos: A Low Mach Fluid Application Case Study

Donfack, Simplicie

- ❑ Dynamically Balanced Synchronization-Avoiding LU Factorization with Multicore and GPUs

Dong, Bin

- ❑ Model-Driven Data Layout Selection for Improving Read Performance

Dongarra, Jack

- ❑ Hybrid Multi-elimination ILU Preconditioners on GPUs
- ❑ Assessing the Impact of ABFT and Checkpoint Composite Strategies
- ❑ Optimizing Krylov Subspace Solvers on Graphics Processing Units
- ❑ Dynamically Balanced Synchronization-Avoiding LU Factorization with Multicore and GPUs

Papers by Author

- ❑ New Algorithm for Computing Eigenvectors of the Symmetric Eigenvalue Problem
- ❑ Design and Implementation of a Large Scale Tree-Based QR Decomposition Using a 3D Virtual Systolic Array and a Lightweight Runtime

Du, Jiayi

- ❑ A Hybrid Parallel Tridiagonal Solver on Multi-core Architectures

Duan, Xiaohui

- ❑ HiPGA: A High Performance Genome Assembler for Short Read Sequence Data
- ❑ XSW: Accelerating Biological Database Search on Xeon Phi

Duarte, Rui Polcarpo

- ❑ Over-clocking of Linear Projection Designs through Device Specific Optimisations

Duffy, Edward B.

- ❑ Teaching HDFS/MapReduce Systems Concepts to Undergraduates

Papers by Author

Duraiswami, Ramani

- ❑ Scalable Fast Multipole Accelerated Vortex Methods

Durelli, Gianluca

- ❑ Adaptive Raytracing Implementation Using Partial Dynamic Reconfiguration
- ❑ PaRA-Sched: A Reconfiguration-Aware Scheduler for Reconfigurable Architectures

Dutta, Sankha Baran

- ❑ GPU Accelerated Nature Inspired Methods for Modelling Large Scale Bi-directional Pedestrian Movement

Edwards, Bob

- ❑ Programming the Adapteva Epiphany 64-Core Network-on-Chip Coprocessor

Papers by Author

El-Baz, Didier

- PCO Introduction and Committees
- A Distributed Algorithm for a Reconfigurable Modular Surface

El-Boghdadi, Hatem M.

- WECPAR: List Ranking Algorithm and Relative Computational Power

Elmroth, Erik

- Integration and Evaluation of Decentralized Fairshare Prioritization (Aequus)

Emeretlis, Andreas

- A Hybrid ILP-CP Model for Mapping Directed Acyclic Task Graphs to Multicore Architectures

Espling, Daniel

- Integration and Evaluation of Decentralized Fairshare Prioritization (Aequus)

Papers by Author

Essafi, Adel

- ❑ An Efficient Algorithm for Scheduling Jobs in Volunteer Computing Platforms

Eyraud-Dubois, Lionel

- ❑ Point-to-Point and Congestion Bandwidth Estimation: Experimental Evaluation on PlanetLab Data

Faddoul, Jocelyne

- ❑ A Parallel Framework for Handling Non-determinism with Expressive Description Logics

Fahringer, Thomas

- ❑ Kd-Tree Based N-Body Simulations with Volume-Mass Heuristic on the GPU

Papers by Author

Faverge, Mathieu

- Taking Advantage of Hybrid Systems for Sparse Direct Solvers via Task-Based Runtimes

Fei, Xin

- A Genetic Algorithm-Based Sparse Coverage over Urban VANETs

Fei, Yunsi

- HATI: Hardware Assisted Thread Isolation for Concurrent C/C++ Programs

Ferner, Clayton

- Using Patterns to Teach Parallel Computing

Filippone, Salvatore

- Exhaustive Key Search on Clusters of GPUs

Papers by Author

Fisher, Travis

- Towards Extreme-Scale Simulations with Next-Generation Trilinos: A Low Mach Fluid Application Case Study

Flahive, Mary

- Higher Dimensional Gaussian Networks

Fontaine, Florent

- Data Quality, Consistency, and Interpretation Management for Wind Farms by Using Neural Networks

Forsell, Martti

- Prototyping the MBTAC Processor for the REPLICA CMP

Fowler, Robert J.

- HPPAC Introduction and Committees

Papers by Author

Fox, Geoffrey C.

- ❑ Evaluating GPU Passthrough in Xen for High Performance Cloud Computing

Friese, Ryan

- ❑ Utility Driven Dynamic Resource Management in an Oversubscribed Energy-Constrained Heterogeneous System

Friesen, Marcia

- ❑ GPU Accelerated Nature Inspired Methods for Modelling Large Scale Bi-directional Pedestrian Movement

Fröning, Holger

- ❑ Infiniband-Verbs on GPU: A Case Study of Controlling an Infiniband Network Device from the GPU

Frumkin, Michael

- ❑ HPGC Introduction and Committees

Papers by Author

Fujimori, Takumi

- Radiation Tolerance of Color Configuration on an Optically Reconfigurable Gate Array

Fujisawa, Katsuki

- Hybrid BFS Approach Using Semi-external Memory

Fujita, Norihisa

- Nuclear Fusion Simulation Code Optimization and Performance Evaluation on GPU Cluster

Fujita, Satoshi

- Minimum Set Cover of Sparsely Distributed Sensor Nodes by a Collection of Unit Disks

Fuser, A.

- Hybrid Metaheuristic for Annual Hydropower Generation Optimization

Papers by Author

Fuser, Alain

- ❑ Data Quality, Consistency, and Interpretation Management for Wind Farms by Using Neural Networks

Gabriel, Edgar

- ❑ Compactor: Optimization Framework at Staging I/O Nodes

Galea, François

- ❑ Fast Generation of Large Task Network Mappings

Gallatin, Doug

- ❑ Twill: A Hybrid Microcontroller-FPGA Framework for Parallelizing Single-Threaded C Programs

Papers by Author

Ganduri, Rajasekhar

- ❑ SmartBricks: A Visual Environment to Design and Explore Novel Custom Domain-Specific Architectures

Ganesan, Narayan

- ❑ Process Simulation of Complex Biochemical Pathways in Explicit 3D Space Enabled by Heterogeneous Computing Platform

Gao, Guang

- ❑ Position Paper: Locality-Driven Scheduling of Tasks for Data-Dependent Multithreading

Garcia, Elkin

- ❑ Position Paper: Locality-Driven Scheduling of Tasks for Data-Dependent Multithreading

Papers by Author

García, Miguel Alfonso Castro

- ❑ Construction of Porous Networks Subjected to Geometric Restrictions by Using OpenMP

Gasior, Jakub

- ❑ A Game-Theoretic Approach to Multiobjective Job Scheduling in Cloud Computing Systems

Gergel, Viktor

- ❑ NSF/IEEE-TCPP Curriculum Implementation at the State University of Nizhni Novgorod

Gerndt, Michael

- ❑ Automatic MPI-IO Tuning with the Periscope Tuning Framework

Papers by Author

Ghandour, Nadine J.

- ❑ Position Paper: Leveraging Strength-Based Dynamic Slicing to Identify Control Reconvergence Instructions

Ghandour, Walid J.

- ❑ Position Paper: Leveraging Strength-Based Dynamic Slicing to Identify Control Reconvergence Instructions

Giacaman, Nasser

- ❑ EA: Research-Infused Teaching of Parallel Programming Concepts for Undergraduate Software Engineering Students

Gianessi, Paolo

- ❑ Towards Energy Efficient Allocation for Applications in Volunteer Cloud

Papers by Author

Gijsbers, Bert

- A Case Study in Coordination Programming: Performance Evaluation of S-Net vs Intel's Concurrent Collections

Gilbert, John

- GABB Introduction

Gioiosa, Roberto

- Online Monitoring System for Performance Fault Detection

Goglin, Brice

- Analysis of MPI Shared-Memory Communication Performance from a Cache Coherence Perspective

Gokhale, Maya

- RAW 2014 Keynotes

Papers by Author

Gokhale, Maya B.

- ❑ Design and Optimization of a Metagenomics Analysis Workflow for NVRAM

Goll, Christian

- ❑ Efficient Computation of the Phylogenetic Likelihood Function on the Intel MIC Architecture

Gong, Bin

- ❑ Automatic MPI-IO Tuning with the Periscope Tuning Framework

González, Fernando Rojas

- ❑ Construction of Porous Networks Subjected to Geometric Restrictions by Using OpenMP

Gonzalez, Joseph

- ❑ GABB Introduction

Papers by Author

Grant, Ryan E.

- ❑ Metrics for Evaluating Energy Saving Techniques for Resilient HPC Systems

Grasso, Ivan

- ❑ Kd-Tree Based N-Body Simulations with Volume-Mass Heuristic on the GPU

Greaves, David

- ❑ A New Dataflow Compiler IR for Accelerating Control-Intensive Code in Spatial Hardware

Grelck, Clemens

- ❑ A Case Study in Coordination Programming: Performance Evaluation of S-Net vs Intel's Concurrent Collections

Papers by Author

Gu, Rong

- ❑ Training Large Scale Deep Neural Networks on the Intel Xeon Phi Many-Core Coprocessor
- ❑ YAFIM: A Parallel Frequent Itemset Mining Algorithm with Spark

Gumerov, Nail A.

- ❑ Scalable Fast Multipole Accelerated Vortex Methods

Guo, Minyi

- ❑ EEWA: Energy-Efficient Workload-Aware Task Scheduling in Multi-core Architectures

Guo, Wenzhong

- ❑ A General P2P Scheme for Constructing Large-Scale Virtual Environments
- ❑ The Empirical Research of Virtual Enterprise Knowledge Transfer's Effectiveness Faced to the Independent Innovation Ability
- ❑ A Distributed Speech Algorithm for Large Scale Data Communication Systems

Papers by Author

Gupta, Rajiv

- ❑ ABC2: Adaptively Balancing Computation and Communication in a DSM Cluster of Multicores for Irregular Applications

Gupta, Siddharth

- ❑ A New Parallel Algorithm for Two-Pass Connected Component Labeling

Gustedt, Jens

- ❑ Resource Centered Computing Delivering High Parallel Performance

Haidar, Azzam

- ❑ New Algorithm for Computing Eigenvectors of the Symmetric Eigenvalue Problem

Haigh, Andrew A.

- ❑ Acceleration of GPU-Based Ultrasound Simulation via Data Compression

Papers by Author

Hains, Gaétan

- ❑ GPS: Towards Simplified Communication on SGL Model

Halappanavar, Mahantesh

- ❑ Parallel Heuristics for Scalable Community Detection

Hamidouche, Khaled

- ❑ Optimizing Collective Communication in UPC

Han, Wenting

- ❑ A Criticality-Aware DVFS Runtime Utility for Optimizing Power Efficiency of Multithreaded Applications

Hao, Zhijun

- ❑ Comparison of Parallel Programming Models on Intel MIC Computer Cluster

Papers by Author

Harding, Brendan

- ❑ Application Level Fault Recovery: Using Fault-Tolerant Open MPI in a PDE Solver

He, Songtao

- ❑ A Criticality-Aware DVFS Runtime Utility for Optimizing Power Efficiency of Multithreaded Applications

He, Tian

- ❑ SkewControl: Gini Out of the Bottle

He, Xingyu

- ❑ A General P2P Scheme for Constructing Large-Scale Virtual Environments

Heath, Barbara

- ❑ Using Patterns to Teach Parallel Computing

Papers by Author

Heinz, Stefan

- Solving Hard MIPLIB2003 Problems with ParaSCIP on Supercomputers: An Update

Herault, Thomas

- Assessing the Impact of ABFT and Checkpoint Composite Strategies

Hernandez, Oscar

- A Validation Testsuite for OpenACC 1.0

Herrmann, Julien

- Memory-Aware List Scheduling for Hybrid Platforms

Heuveline, Vincent

- Evaluation of the Global Address Space Programming Interface (GASPI)

Papers by Author

Hifi, Mhand

- ❑ A Parallel Large Neighborhood Search-Based Heuristic for the Disjunctively Constrained Knapsack Problem

Hilton, Marcia

- ❑ Utility Driven Dynamic Resource Management in an Oversubscribed Energy-Constrained Heterogeneous System

Hinchey, Mike

- ❑ Autonomy Requirements Engineering for Self-Adaptive Science Clouds

Hirasawa, Shoichi

- ❑ A Platform-Specific Code Smell Alert System for High Performance Computing Applications

Papers by Author

Hiroyuki, Sato

- ❑ A Linear Performance-Breakdown Model for GPU Programming Optimization Guidance

Hochberger, Christian

- ❑ Influence of Magnetic Fields and X-Radiation on Ring Oscillators in FPGAs

Hoemmen, Mark

- ❑ Towards Extreme-Scale Simulations with Next-Generation Trilinos: A Low Mach Fluid Application Case Study

Hohl, Detlef

- ❑ Using GPU Shared Memory with a Directive-Based Approach

Papers by Author

Horey, James L.

- Analyzing Reliability of Virtual Machine Instances with Dynamic Pricing in the Public Cloud

Hossain, Roksana

- GPU Enhanced Path Finding for an Unmanned Aerial Vehicle

Hsu, Chung-Hsing

- Application Power Signature Analysis

Hu, Jonathan

- Towards Extreme-Scale Simulations with Next-Generation Trilinos: A Low Mach Fluid Application Case Study

Hu, Qi

- Scalable Fast Multipole Accelerated Vortex Methods

Papers by Author

Huang, Kun

- ❑ Removing Sequential Bottlenecks in Analysis of Next-Generation Sequencing Data

Huang, Miaoqing

- ❑ Comparison of Parallel Programming Models on Intel MIC Computer Cluster

Huang, Yihua

- ❑ Training Large Scale Deep Neural Networks on the Intel Xeon Phi Many-Core Coprocessor
- ❑ YAFIM: A Parallel Frequent Itemset Mining Algorithm with Spark

Huang, Zhiyi

- ❑ EEWA: Energy-Efficient Workload-Aware Task Scheduling in Multi-core Architectures

Papers by Author

Hübner, Michael

- A Framework for Customizing Virtual 3-D Reconfigurable Platforms at Run-Time

Huo, Xin

- Scheduling Methods for Accelerating Applications on Architectures with Heterogeneous Cores

Hysom, David A.

- Design and Optimization of a Metagenomics Analysis Workflow for NVRAM

Ibarra, Oscar H.

- APDCM Introduction and Committees

Idomura, Yasuhiro

- Nuclear Fusion Simulation Code Optimization and Performance Evaluation on GPU Cluster

Papers by Author

Inagi, Masato

- ❑ An ILP-Based Optimal Circuit Mapping Method for PLDs

Inoue, Keisuke

- ❑ An ILP-Based Optimal Circuit Mapping Method for PLDs

Ioannidou, Kleoni

- ❑ SupMR: Circumventing Disk and Memory Bandwidth Bottlenecks for Scale-up MapReduce

Ito, Yasuaki

- ❑ Bulk Execution of Oblivious Algorithms on the Unified Memory Machine, with GPU Implementation
- ❑ An Efficient Implementation of the Gradient-Based Hough Transform Using DSP Slices and Block RAMs on the FPGA

Papers by Author

Iwabuchi, Keita

- ❑ Hybrid BFS Approach Using Semi-external Memory

Jacob, Robert

- ❑ The Heuristic Static Load-Balancing Algorithm Applied to the Community Earth System Model

Jahre, Magnus

- ❑ A Study of Energy and Locality Effects Using Space-Filling Curves

Jansson, Karl

- ❑ gpuRF and gpuERT: Efficient and Scalable GPU Algorithms for Decision Tree Ensembles

Jayaraj, Jagan

- ❑ CFD Builder: A Library Builder for Computational Fluid Dynamics

Papers by Author

Jeanvoine, Emmanuel

- ❑ Scalable and Reliable Data Broadcast with Kascade

Jiang, Congfeng

- ❑ Towards Energy Efficient Allocation for Applications in Volunteer Cloud

Jiang, Cong-Feng

- ❑ HPDIC Introduction and Committees

Jiang, Xiaohui

- ❑ Parallel Bayesian Network Modelling for Pervasive Health Monitoring System

Jin, Lei

- ❑ Training Large Scale Deep Neural Networks on the Intel Xeon Phi Many-Core Coprocessor

Papers by Author

John, David J.

- ❑ Parallel and Distributed Computing across the Computer Science Curriculum

Johnson, Tyler

- ❑ CyGraph: A Reconfigurable Architecture for Parallel Breadth-First Search

Johnsson, Lennart

- ❑ Exploiting DMA for Performance and Energy Optimized STREAM on a DSP

Jones, Philip

- ❑ CyGraph: A Reconfigurable Architecture for Parallel Breadth-First Search

Jordan, K.E.

- ❑ Parallelization of the Trinity Pipeline for De Novo Transcriptome Assembly

Papers by Author

Jose, Jithin

- ❑ Optimizing Collective Communication in UPC

Juckeland, Guido

- ❑ Scalable Critical Path Analysis for Hybrid MPI-CUDA Applications

Kachris, Christoforos

- ❑ A Low-Latency Algorithm and FPGA Design for the Min-Search of LDPC Decoders

Kalyanaraman, Ananth

- ❑ HiCOMB Keynote and Invited Talks
- ❑ Parallel Heuristics for Scalable Community Detection

Kaneko, Mineo

- ❑ An ILP-Based Optimal Circuit Mapping Method for PLDs

Papers by Author

Karatza, Helen D.

- Bag-of-Task Scheduling on Power-Aware Clusters Using a DVFS-Based Mechanism

Karlin, Ian

- Characterizing the Impact of Program Optimizations on Power and Energy for Explicit Hydrodynamics

Katz, Randy H.

- EduPar Keynote

Kaya, Kamer

- Hardware/Software Vectorization for Closeness Centrality on Multi-/Many-Core Architectures
- Extracting Maximal Exact Matches on GPU

Papers by Author

Keen, Aaron

- Twill: A Hybrid Microcontroller-FPGA Framework for Parallelizing Single-Threaded C Programs

Kendon, Tyson

- DisSLib: CC: A Library for Distributed Search with a Central Common Search State

Kennon, Stephen

- Towards Extreme-Scale Simulations with Next-Generation Trilinos: A Low Mach Fluid Application Case Study

Kepner, Jeremy

- GABB Introduction

Kerbyson, Darren J.

- LSPP Introduction and Committees

Papers by Author

- ❑ Online Monitoring System for Performance Fault Detection

Kessaci, Yacine

- ❑ Multi-level and Multi-objective Survey on Cloud Scheduling

Kestor, Gokcen

- ❑ Online Monitoring System for Performance Fault Detection

Khadraoui, Djamel

- ❑ Improving Bus Ride Comfort Using GLOSA-Based Dynamic Speed Optimisation

Khan, Danish Anis

- ❑ A Dependable Coarse-Grain Reconfigurable Multicore Array

Khemka, Bhavesh

- ❑ Utility Driven Dynamic Resource Management in an Oversubscribed Energy-Constrained Heterogeneous System

Papers by Author

Kim, C.S.

- ❑ Parallelization of the Trinity Pipeline for De Novo Transcriptome Assembly

Kirchner, Peter D.

- ❑ Large Scale Discriminative Metric Learning

Kobayashi, Hiroaki

- ❑ A Platform-Specific Code Smell Alert System for High Performance Computing Applications

Koch, Thorsten

- ❑ Solving Hard MIPLIB2003 Problems with ParaSCIP on Supercomputers: An Update

Papers by Author

Koduru, Sai Charan

- ❑ ABC2: Adaptively Balancing Computation and Communication in a DSM Cluster of Multicores for Irregular Applications

Koenig, Gregory A.

- ❑ Utility Driven Dynamic Resource Management in an Oversubscribed Energy-Constrained Heterogeneous System

Kofler, Klaus

- ❑ Kd-Tree Based N-Body Simulations with Volume-Mass Heuristic on the GPU

Koike, Atsushi

- ❑ A Novel Computational Model for GPUs with Application to I/O Optimal Sorting Algorithms

Koziol, Quincey

- ❑ Compactor: Optimization Framework at Staging I/O Nodes

Papers by Author

Kozlov, Alexey M.

- ❑ Efficient Computation of the Phylogenetic Likelihood Function on the Intel MIC Architecture

Krishnan, Hari

- ❑ Near-Optimal Location Tracking Using Sensor Networks

Kristensen, Mads R.B.

- ❑ Bohrium: A Virtual Machine Approach to Portable Parallelism
- ❑ Transparent GPU Execution of NumPy Applications

Kurzak, Jakub

- ❑ Design and Implementation of a Large Scale Tree-Based QR Decomposition Using a 3D Virtual Systolic Array and a Lightweight Runtime

Papers by Author

Lacoste, Xavier

- ❑ Taking Advantage of Hybrid Systems for Sparse Direct Solvers via Task-Based Runtimes

Lai, Chenggang

- ❑ Comparison of Parallel Programming Models on Intel MIC Computer Cluster

Lai, Yingxu

- ❑ Trust-Based Security for the Spanning Tree Protocol

Lan, Haidong

- ❑ XSW: Accelerating Biological Database Search on Xeon Phi

Lankes, Stefan

- ❑ SWIFT: A Transparent and Flexible Communication Layer for PCIe-Coupled Accelerators and (Co-)Processors

Papers by Author

Laros III, James H.

- ❑ Metrics for Evaluating Energy Saving Techniques for Resilient HPC Systems

Larson, Stephen

- ❑ HiCOMB Keynote and Invited Talks

Lastovetsky, Alexey

- ❑ Searching for the Optimal Data Partitioning Shape for Parallel Matrix Matrix Multiplication on 3 Heterogeneous Processors
- ❑ Topology-Aware Optimization of Communications for Parallel Matrix Multiplication on Hierarchical Heterogeneous HPC Platform

Laure, Erwin

- ❑ Exploiting DMA for Performance and Energy Optimized STREAM on a DSP

Papers by Author

Le Cun, Bertrand

- ❑ Fast Generation of Large Task Network Mappings

Lebbah, Mustapha

- ❑ SOM Clustering Using Spark-MapReduce

Lee, Che-Rung

- ❑ Performance Modeling for Hardware Thread-Level Speculation

Lee, Jongeun

- ❑ Efficient Software-Based Runtime Binary Translation for Coarse-Grained Reconfigurable Architectures

Leidel, John D.

- ❑ HMC-Sim: A Simulation Framework for Hybrid Memory Cube Devices

Papers by Author

León, Edgar A.

- Characterizing the Impact of Program Optimizations on Power and Energy for Explicit Hydrodynamics

Leong, Philip

- RAW Introduction and Committees

Leppänen, Ville

- Prototyping the MBTAC Processor for the REPLICA CMP

Leyffer, Sven

- The Heuristic Static Load-Balancing Algorithm Applied to the Community Earth System Model

Li, Bo

- The Power-Performance Tradeoffs of the Intel Xeon Phi on HPC Applications

Papers by Author

Li, Chong

- ❑ GPS: Towards Simplified Communication on SGL Model

Li, Dong

- ❑ HPPAC Introduction and Committees

Li, Feng

- ❑ A Criticality-Aware DVFS Runtime Utility for Optimizing Power Efficiency of Multithreaded Applications

Li, Jie

- ❑ Process Simulation of Complex Biochemical Pathways in Explicit 3D Space Enabled by Heterogeneous Computing Platform

Li, Kenli

- ❑ A Hybrid Parallel Tridiagonal Solver on Multi-core Architectures

Papers by Author

- ❑ A Task Scheduling Algorithm Based on Replication for Maximizing Reliability on Heterogeneous Computing Systems

Li, Keqin

- ❑ A Hybrid Parallel Tridiagonal Solver on Multi-core Architectures
- ❑ A Task Scheduling Algorithm Based on Replication for Maximizing Reliability on Heterogeneous Computing Systems

Li, Liang

- ❑ Optimizing the Join Operation on Hive to Accelerate Cross-Matching in Astronomy

Li, Wei

- ❑ Optimizing the Join Operation on Hive to Accelerate Cross-Matching in Astronomy

Liao, Xiangke

- ❑ SkewControl: Gini Out of the Bottle

Papers by Author

Likhogrud, Nikolay

- ❑ KernelGen — The Design and Implementation of a Next Generation Compiler Platform for Accelerating Numerical Models on GPUs

Lim, Seung-Hwan

- ❑ Analyzing Reliability of Virtual Machine Instances with Dynamic Pricing in the Public Cloud

Lin, Paul

- ❑ Towards Extreme-Scale Simulations with Next-Generation Trilinos: A Low Mach Fluid Application Case Study

Lin, Pei-Hung

- ❑ CFD Builder: A Library Builder for Computational Fluid Dynamics

Papers by Author

Lindhorst, Timo

- ❑ Maintaining Dependable Communication Service for Mobile Stations in Wireless Mesh Networks by Tracking Capacity Demands

Liniov, Alexey

- ❑ NSF/IEEE-TCPP Curriculum Implementation at the State University of Nizhni Novgorod

Liu, Bo

- ❑ Hierarchical Pipeline Optimization of Coarse Grained Reconfigurable Processor for Multimedia Applications

Liu, Hong

- ❑ Optimizing the Join Operation on Hive to Accelerate Cross-Matching in Astronomy

Papers by Author

Liu, Jialin

- ❑ Model-Driven Data Layout Selection for Improving Read Performance

Liu, Leibo

- ❑ Hierarchical Pipeline Optimization of Coarse Grained Reconfigurable Processor for Multimedia Applications

Liu, Taoying

- ❑ Optimizing the Join Operation on Hive to Accelerate Cross-Matching in Astronomy

Liu, Weifeng

- ❑ Automatic MPI-IO Tuning with the Periscope Tuning Framework

Liu, Weiguo

- ❑ HiPGA: A High Performance Genome Assembler for Short Read Sequence Data

Papers by Author

- ❑ XSW: Accelerating Biological Database Search on Xeon Phi

Liu, Yunhuai

- ❑ SkewControl: Gini Out of the Bottle

Liu, Zenghui

- ❑ Trust-Based Security for the Spanning Tree Protocol

Lloyd, G. Scott

- ❑ Design and Optimization of a Metagenomics Analysis Workflow for NVRAM

Long, Teng

- ❑ Scalable System Environment Caching and Sharing for Distributed Virtual Machines

Lu, Hao

- ❑ Parallel Heuristics for Scalable Community Detection

Papers by Author

Lu, Ligang

- Using GPU Shared Memory with a Directive-Based Approach

Lukarski, Dimitar

- Hybrid Multi-elimination ILU Preconditioners on GPUs

Lund, Simon A.F.

- Bohrium: A Virtual Machine Approach to Portable Parallelism

Luo, Xiaozhong

- SmartBricks: A Visual Environment to Design and Explore Novel Custom Domain-Specific Architectures

Lupo, Chris

- Twill: A Hybrid Microcontroller-FPGA Framework for Parallelizing Single-Threaded C Programs

Papers by Author

Luszczek, Piotr

- ❑ Optimizing Krylov Subspace Solvers on Graphics Processing Units
- ❑ New Algorithm for Computing Eigenvectors of the Symmetric Eigenvalue Problem
- ❑ Design and Implementation of a Large Scale Tree-Based QR Decomposition Using a 3D Virtual Systolic Array and a Lightweight Runtime

M., Mario A. Chapa

- ❑ A Linear Performance-Breakdown Model for GPU Programming Optimization Guidance

MacCaull, Wendy

- ❑ A Parallel Framework for Handling Non-determinism with Expressive Description Logics

Maciejewski, Anthony A.

- ❑ Utility Driven Dynamic Resource Management in an Oversubscribed Energy-Constrained Heterogeneous System

Papers by Author

Maeda, Toshiyuki

- ❑ A General Model Checking Framework for Various Memory Consistency Models

Maehle, Erik

- ❑ DPDNS Introduction and Committees
- ❑ A Load Balancing Behavior for Underwater Robot Swarms to Increase Mission Time and Fault Tolerance

Magato, William A.

- ❑ llamaOS: A Solution for Virtualized High-Performance Computing Clusters

Maggioni, Marco

- ❑ CoAdELL: Adaptivity and Compression for Improving Sparse Matrix-Vector Multiplication on GPUs

Papers by Author

Magierowski, Sebastian

- ❑ GPU Enhanced Path Finding for an Unmanned Aerial Vehicle

Mai, Toan X.

- ❑ Efficient Software-Based Runtime Binary Translation for Coarse-Grained Reconfigurable Architectures

Malek, Alirad

- ❑ A Dependable Coarse-Grain Reconfigurable Multicore Array

Malek, Miroslaw

- ❑ ExCovery — A Framework for Distributed System Experiments and a Case Study of Service Discovery

Papers by Author

Malik, Abid M.

- ❑ OpenMP Task Scheduling Analysis via OpenMP Runtime API and Tool Visualization

Malik, Tania

- ❑ Topology-Aware Optimization of Communications for Parallel Matrix Multiplication on Hierarchical Heterogeneous HPC Platform

Malladi, Mukund

- ❑ SmartBricks: A Visual Environment to Design and Explore Novel Custom Domain-Specific Architectures

Malone, Brandon

- ❑ Portfolio-Based Selection of Robust Dynamic Loop Scheduling Algorithms Using Machine Learning

Papers by Author

Maltzahn, Carlos

- SupMR: Circumventing Disk and Memory Bandwidth Bottlenecks for Scale-up MapReduce

Marchal, Loris

- Memory-Aware List Scheduling for Hybrid Platforms

Marinescu, Dan C.

- Energy-Aware Load Balancing Policies for the Cloud Ecosystem
- Cloud-Based Simulation of a Smart Power Grid
- Security of Applications Involving Multiple Organizations and Order Preserving Encryption in Hybrid Cloud Environments

Marques, Eduardo

- High-Level Synthesis from C vs. a DSL-Based Approach

Papers by Author

Martin, Stéphane

- Scalable and Reliable Data Broadcast with Kascade

Matsuoka, Satoshi

- Hybrid BFS Approach Using Semi-external Memory

Mattoso, Marta

- Exploring Large Scale Receptor-Ligand Pairs in Molecular Docking Workflows in HPC Clouds

Mattson, Tim

- GABB Introduction

McCreath, Eric C.

- Acceleration of GPU-Based Ultrasound Simulation via Data Compression

Papers by Author

McLaughlin, Adam

- ❑ Revisiting Edge and Node Parallelism for Dynamic GPU Graph Analytics

McLeod, Robert

- ❑ GPU Accelerated Nature Inspired Methods for Modelling Large Scale Bi-directional Pedestrian Movement

Mehta, Gayatri

- ❑ SmartBricks: A Visual Environment to Design and Explore Novel Custom Domain-Specific Architectures

Mei, Chen

- ❑ Hierarchical Pipeline Optimization of Coarse Grained Reconfigurable Processor for Multimedia Applications

Papers by Author

Mei, Jing

- ❑ A Task Scheduling Algorithm Based on Replication for Maximizing Reliability on Heterogeneous Computing Systems

Meisner, Sebastian

- ❑ FPGA Redundancy Configurations: An Automated Design Space Exploration

Melab, Nouredine

- ❑ Multi-level and Multi-objective Survey on Cloud Scheduling

Mellor-Crummey, John

- ❑ Autotuning Tensor Transposition

Melo, Alba Cristina Magalhaes Alves de

- ❑ HiCOMB Introduction and Committees

Papers by Author

Memon, Atif

- ❑ Scalable System Environment Caching and Sharing for Distributed Virtual Machines

Méndez, Angel González

- ❑ Construction of Porous Networks Subjected to Geometric Restrictions by Using OpenMP

Meng, Jiayuan

- ❑ Dymaxion++: A Directive-Based API to Optimize Data Layout and Memory Mapping for Heterogeneous Systems

Meng, Xiangxu

- ❑ XSW: Accelerating Biological Database Search on Xeon Phi

Papers by Author

Menouer, Tarek

- ❑ Adaptive N to P Portfolio for Solving Constraint Programming Problems on Top of the Parallel Bobpp Framework

Mercier, Patrick

- ❑ Resource Centered Computing Delivering High Parallel Performance

Messier, Geoffery G.

- ❑ GPU Enhanced Path Finding for an Unmanned Aerial Vehicle

Meyer, Jan Christian

- ❑ A Study of Energy and Locality Effects Using Space-Filling Curves

Meyer, Timmy

- ❑ The Power-Performance Tradeoffs of the Intel Xeon Phi on HPC Applications

Papers by Author

Meyerov, Iosif

- ❑ NSF/IEEE-TCPP Curriculum Implementation at the State University of Nizhni Novgorod

Mickelson, Sheri

- ❑ The Heuristic Static Load-Balancing Algorithm Applied to the Community Earth System Model

Mikushin, Dmitry

- ❑ KernelGen — The Design and Implementation of a Next Generation Compiler Platform for Accelerating Numerical Models on GPUs

Miller, Michael K.

- ❑ Nanoscale Cluster Detection in Massive Atom Probe Tomography Data

Mitra, Gaurav

- ❑ Programming the Adapteva Epiphany 64-Core Network-on-Chip Coprocessor

Papers by Author

Mizote, Ryo

- ❑ Hybrid BFS Approach Using Semi-external Memory

Mohamedin, Mohamed

- ❑ Managing Soft-Errors in Transactional Systems

Mooring, John

- ❑ The Power-Performance Tradeoffs of the Intel Xeon Phi on HPC Applications

Muhaidat, Sami

- ❑ Machine-Learning-Based Identification of Defect Patterns in Semiconductor Wafer Maps: An Overview and Proposal

Murray, Derek

- ❑ HPGC Keynotes

Papers by Author

Nagayama, Shinobu

- ❑ An ILP-Based Optimal Circuit Mapping Method for PLDs

Nakano, Koji

- ❑ Bulk Execution of Oblivious Algorithms on the Unified Memory Machine, with GPU Implementation
- ❑ An Efficient Implementation of the Gradient-Based Hough Transform Using DSP Slices and Block RAMs on the FPGA

Nakib, A.

- ❑ Hybrid Metaheuristic for Annual Hydropower Generation Optimization

Nassi, Ike

- ❑ SupMR: Circumventing Disk and Memory Bandwidth Bottlenecks for Scale-up MapReduce

Papers by Author

Nazor, Jolie

- ❑ Application Power Signature Analysis

Neelima, B.

- ❑ Predicting an Optimal Sparse Matrix Format for SpMV Computation on GPU

Negre, Stephane

- ❑ A Parallel Large Neighborhood Search-Based Heuristic for the Disjunctively Constrained Knapsack Problem

Nett, Edgar

- ❑ DPDNS Keynote
- ❑ Maintaining Dependable Communication Service for Mobile Stations in Wireless Mesh Networks by Tracking Capacity Demands

Papers by Author

Netzer, Gilbert

- ❑ Exploiting DMA for Performance and Energy Optimized STREAM on a DSP

Ngo, Linh Bao

- ❑ Teaching HDFS/MapReduce Systems Concepts to Undergraduates

Ngoko, Yanik

- ❑ Towards Energy Efficient Allocation for Applications in Volunteer Cloud

Nicod, Jean-Marc

- ❑ Optimizing Buffer Sizes for Pipeline Workflow Scheduling with Setup Times

Nishiyama, Hiroki

- ❑ An ILP-Based Optimal Circuit Mapping Method for PLDs

Papers by Author

Nuga, Hideo

- ❑ Nuclear Fusion Simulation Code Optimization and Performance Evaluation on GPU Cluster

Nussbaum, Lucas

- ❑ Scalable and Reliable Data Broadcast with Kascade

Ocaña, Kary

- ❑ Exploring Large Scale Receptor-Ligand Pairs in Molecular Docking Workflows in HPC Clouds

Oden, Lena

- ❑ Infiniband-Verbs on GPU: A Case Study of Controlling an Infiniband Network Device from the GPU

Oliveira, Cristiano Bacelar de

- ❑ High-Level Synthesis from C vs. a DSL-Based Approach

Papers by Author

Oliver, John

- ❑ Twill: A Hybrid Microcontroller-FPGA Framework for Parallelizing Single-Threaded C Programs

Olivier, Stephen L.

- ❑ Metrics for Evaluating Energy Saving Techniques for Resilient HPC Systems

Östberg, Per-Olov

- ❑ Integration and Evaluation of Decentralized Fairshare Prioritization (Aequus)

Ozer, Gulcin

- ❑ Removing Sequential Bottlenecks in Analysis of Next-Generation Sequencing Data

Palmieri, Roberto

- ❑ Managing Soft-Errors in Transactional Systems

Papers by Author

Palsetia, Diana

- ❑ A New Parallel Algorithm for Two-Pass Connected Component Labeling

Pan, Qiuyue

- ❑ Trust-Based Security for the Spanning Tree Protocol

Panangadan, Anand

- ❑ Wait-Free Primitives for Initializing Bayesian Network Structure Learning on Multicore Processors

Panda, Dhableswar K. (DK)

- ❑ Optimizing Collective Communication in UPC

Pasricha, Sudeep

- ❑ Utility Driven Dynamic Resource Management in an Oversubscribed Energy-Constrained Heterogeneous System

Papers by Author

Patwary, Md. Mostofa Ali

- ❑ A New Parallel Algorithm for Two-Pass Connected Component Labeling

Pau, Danilo

- ❑ Adaptive Raytracing Implementation Using Partial Dynamic Reconfiguration

Pavlic, Theodore P.

- ❑ Using Physical Stigmergy in Decentralized Optimization under Multiple Non-separable Constraints: Formal Methods and an Intelligent Lighting Example

Pavlovikj, Natasha

- ❑ A Comparison of a Campus Cluster and Open Science Grid Platforms for Protein-Guided Assembly Using Pegasus Workflow Management System

Paya, Ashkan

- ❑ Energy-Aware Load Balancing Policies for the Cloud Ecosystem

Papers by Author

- ❑ Cloud-Based Simulation of a Smart Power Grid
- ❑ Security of Applications Involving Multiple Organizations and Order Preserving Encryption in Hybrid Cloud Environments

Perrone, Michael

- ❑ Performance Modeling for Hardware Thread-Level Speculation

Pfaffe, Philip

- ❑ A Stream Processing Framework for On-Line Optimization of Performance and Energy Efficiency on Heterogeneous Systems

Pfreundt, Franz-Joseph

- ❑ Infiniband-Verbs on GPU: A Case Study of Controlling an Infiniband Network Device from the GPU

Pham-Quoc, Cuong

- ❑ Automated Hybrid Interconnect Design for FPGA Accelerators Using Data Communication Profiling

Papers by Author

Phipps, Eric

- ❑ Towards Extreme-Scale Simulations with Next-Generation Trilinos: A Low Mach Fluid Application Case Study

Pickartz, Simon

- ❑ SWIFT: A Transparent and Flexible Communication Layer for PCIe-Coupled Accelerators and (Co-)Processors

Pilato, Christian

- ❑ Adaptive Raytracing Implementation Using Partial Dynamic Reconfiguration
- ❑ PaRA-Sched: A Reconfiguration-Aware Scheduler for Reconfigurable Architectures

Pionteck, Thilo

- ❑ Influence of Magnetic Fields and X-Radiation on Ring Oscillators in FPGAs

Papers by Author

Piranda, Benoît

- ❑ A Distributed Algorithm for a Reconfigurable Modular Surface

Platzner, Marco

- ❑ FPGA Redundancy Configurations: An Automated Design Space Exploration

Ponpandi, Swamy D.

- ❑ An Evaluation of User Satisfaction Driven Scheduling in a Polymorphic Embedded System

Poole, Stephen W.

- ❑ Application Power Signature Analysis

Poole, Steve

- ❑ Utility Driven Dynamic Resource Management in an Oversubscribed Energy-Constrained Heterogeneous System

Papers by Author

Porter, Adam

- Scalable System Environment Caching and Sharing for Distributed Virtual Machines

Porterfield, Allan K.

- Metrics for Evaluating Energy Saving Techniques for Resilient HPC Systems

Powers, Sarah

- Utility Driven Dynamic Resource Management in an Oversubscribed Energy-Constrained Heterogeneous System

Prasad, Sushil K

- EduPar Introduction and Committees

Papers by Author

Prasanna, Viktor K.

- ❑ Wait-Free Primitives for Initializing Bayesian Network Structure Learning on Multicore Processors

Prokopenko, Andrey

- ❑ Towards Extreme-Scale Simulations with Next-Generation Trilinos: A Low Mach Fluid Application Case Study

Putigny, Bertrand

- ❑ Analysis of MPI Shared-Memory Communication Performance from a Cache Coherence Perspective

Qawasmeh, Ahmad

- ❑ OpenMP Task Scheduling Analysis via OpenMP Runtime API and Tool Visualization

Papers by Author

Qian, Hangwei

- ❑ Mega Data Center for Elastic Internet Applications

Qian, Xiujuan

- ❑ Parallel Bayesian Network Modelling for Pervasive Health Monitoring System

Qiu, Hongjian

- ❑ YAFIM: A Parallel Frequent Itemset Mining Algorithm with Spark

Quintin, Jean-Noël

- ❑ Topology-Aware Optimization of Communications for Parallel Matrix Multiplication on Hierarchical Heterogeneous HPC Platform

Rabinovich, Michael

- ❑ Mega Data Center for Elastic Internet Applications

Papers by Author

Raghavendra, Prakash S.

- ❑ Predicting an Optimal Sparse Matrix Format for SpMV Computation on GPU

Raitza, Michael

- ❑ Influence of Magnetic Fields and X-Radiation on Ring Oscillators in FPGAs

Rajamanickam, Sivasankaran

- ❑ Towards Extreme-Scale Simulations with Next-Generation Trilinos: A Low Mach Fluid Application Case Study

Rajamony, Ram

- ❑ LSPP Introduction and Committees

Rambharos, Rajendra

- ❑ Utility Driven Dynamic Resource Management in an Oversubscribed Energy-Constrained Heterogeneous System

Papers by Author

Ramet, Pierre

- ❑ Taking Advantage of Hybrid Systems for Sparse Direct Solvers via Task-Based Runtimes

Ranft, Benjamin

- ❑ A Stream Processing Framework for On-Line Optimization of Performance and Energy Efficiency on Heterogeneous Systems

Rauber, Thomas

- ❑ PDSEC Introduction and Committees

Ravindran, Binoy

- ❑ Managing Soft-Errors in Transactional Systems

Papers by Author

Reble, Pablo

- ❑ SWIFT: A Transparent and Flexible Communication Layer for PCIe-Coupled Accelerators and (Co-)Processors

Reddy, G. Ram Mohana

- ❑ Predicting an Optimal Sparse Matrix Format for SpMV Computation on GPU

Rehn-Sonigo, Veronika

- ❑ Optimizing Buffer Sizes for Pipeline Workflow Scheduling with Setup Times

Reinwald, Berthold

- ❑ Large Scale Discriminative Metric Learning

Reisner, Marc

- ❑ SmartBricks: A Visual Environment to Design and Explore Novel Custom Domain-Specific Architectures

Papers by Author

Reissman, Nico

- ❑ A Study of Energy and Locality Effects Using Space-Filling Curves

Rendell, Alistair P.

- ❑ Programming the Adapteva Epiphany 64-Core Network-on-Chip Coprocessor

Richard, Olivier

- ❑ Scalable and Reliable Data Broadcast with Cascade

Rivoire, Suzanne

- ❑ Application Power Signature Analysis

Robert, Yves

- ❑ Assessing the Impact of ABFT and Checkpoint Composite Strategies
- ❑ Memory-Aware List Scheduling for Hybrid Platforms

Papers by Author

Roivainen, Jussi

- ❑ Prototyping the MBTAC Processor for the REPLICA CMP

Rostami, M. Ali

- ❑ Interactively Exploring the Connection between Nested Dissection Orderings for Parallel Cholesky Factorization and Vertex Separators

Ruelle, Benoit

- ❑ Analysis of MPI Shared-Memory Communication Performance from a Cache Coherence Perspective

Ruiz, Patricia

- ❑ Improving Bus Ride Comfort Using GLOSA-Based Dynamic Speed Optimisation

Rünger, Gudula

- ❑ PDSEC Introduction and Committees

Papers by Author

Rychkov, Vladimir

- ❑ Topology-Aware Optimization of Communications for Parallel Matrix Multiplication on Hierarchical Heterogeneous HPC Platform

Saadi, Toufik

- ❑ A Parallel Large Neighborhood Search-Based Heuristic for the Disjunctively Constrained Knapsack Problem

Sachdeva, V.

- ❑ Parallelization of the Trinity Pipeline for De Novo Transcriptome Assembly

Sadakane, Kunihiro

- ❑ A Novel Computational Model for GPUs with Application to I/O Optimal Sorting Algorithms

Papers by Author

Saleh, Sagvan

- ❑ A Parallel Large Neighborhood Search-Based Heuristic for the Disjunctively Constrained Knapsack Problem

Salighehdar, Amin

- ❑ Process Simulation of Complex Biochemical Pathways in Explicit 3D Space Enabled by Heterogeneous Computing Platform

Salscheider, Niels Ole

- ❑ Runtime Behavior Comparison of Modern Accelerators and Coprocessors

Sánchez, Salomón Cordero

- ❑ Construction of Porous Networks Subjected to Geometric Restrictions by Using OpenMP

Papers by Author

Sander, Oliver

- ❑ Virtualization Support for FPGA-Based Coprocessors Connected via PCI Express to an Intel Multicore Platform

Sandmann, Timo

- ❑ Virtualization Support for FPGA-Based Coprocessors Connected via PCI Express to an Intel Multicore Platform

Santambrogio, Marco

- ❑ RAW Introduction and Committees

Santambrogio, Marco D.

- ❑ Adaptive Raytracing Implementation Using Partial Dynamic Reconfiguration
- ❑ PaRA-Sched: A Reconfiguration-Aware Scheduler for Reconfigurable Architectures

Papers by Author

Santiago, Fabian

- ❑ Application Power Signature Analysis

Santos, Juan Carlos Martínez

- ❑ HATI: Hardware Assisted Thread Isolation for Concurrent C/C++ Programs

Sarazin, Tugdual

- ❑ SOM Clustering Using Spark-MapReduce

Sariyuce, Ahmet Erdem

- ❑ Hardware/Software Vectorization for Closeness Centrality on Multi-/Many-Core Architectures

Sass, Ron

- ❑ RAW Introduction and Committees

Papers by Author

Sato, Hitoshi

- ❑ Hybrid BFS Approach Using Semi-external Memory

Saule, Erik

- ❑ Hardware/Software Vectorization for Closeness Centrality on Multi-/Many-Core Architectures

Sawyer, William

- ❑ Optimizing Krylov Subspace Solvers on Graphics Processing Units

Schindler, Sabine

- ❑ Kd-Tree Based N-Body Simulations with Volume-Mass Heuristic on the GPU

Schmidt, Bernhard

- ❑ Minimizing Scrubbing Effort through Automatic Netlist Partitioning and Floorplanning

Papers by Author

Schmidt, Michael

- ❑ Large Scale Discriminative Metric Learning

Schmidtobreick, Mareike

- ❑ Evaluation of the Global Address Space Programming Interface (GASPI)

Schmitt, Felix

- ❑ Scalable Critical Path Analysis for Hybrid MPI-CUDA Applications

Schwiegelshohn, Uwe

- ❑ HCW Introduction
- ❑ Message from the HCW General Chair

Schwittmann, Lorenz

- ❑ Deterministic Synchronization of Multi-threaded Programs with Operational Transformation

Papers by Author

Sciuto, Donatella

- ❑ PaRA-Sched: A Reconfiguration-Aware Scheduler for Reconfigurable Architectures

Seal, Sudip K.

- ❑ Nanoscale Cluster Detection in Massive Atom Probe Tomography Data

Seredynski, Franciszek

- ❑ NIDISC Introduction and Committees
- ❑ Graph-Based Cellular Automata Approach to Maximum Lifetime Coverage Problem in Wireless Sensor Networks
- ❑ A Game-Theoretic Approach to Multiobjective Job Scheduling in Cloud Computing Systems

Seredynski, Marcin

- ❑ Improving Bus Ride Comfort Using GLOSA-Based Dynamic Speed Optimisation

Papers by Author

Sevilla, Michael

- ❑ SupMR: Circumventing Disk and Memory Bandwidth Bottlenecks for Scale-up MapReduce

Shafarenko, Alex

- ❑ A Case Study in Coordination Programming: Performance Evaluation of S-Net vs Intel's Concurrent Collections

Shamaei, Arash

- ❑ Higher Dimensional Gaussian Networks

Shanshan, Li

- ❑ SkewControl: Gini Out of the Bottle

Sharma, Gokarna

- ❑ Near-Optimal Location Tracking Using Sensor Networks

Papers by Author

Shi, Xuan

- ❑ Comparison of Parallel Programming Models on Intel MIC Computer Cluster

Shinano, Yuji

- ❑ Solving Hard MIPLIB2003 Problems with ParaSCIP on Supercomputers: An Update

Shirazi, Behrooz

- ❑ HCW Introduction
- ❑ Message from the HCW Steering Committee Chair

Sidiropoulos, Harry

- ❑ A Framework for Mapping Dynamic Virtual Kernels onto Heterogeneous Reconfigurable Platforms

Papers by Author

Siefert, Christopher

- ❑ Towards Extreme-Scale Simulations with Next-Generation Trilinos: A Low Mach Fluid Application Case Study

Siegel, Howard Jay

- ❑ Utility Driven Dynamic Resource Management in an Oversubscribed Energy-Constrained Heterogeneous System

Sinnen, Oliver

- ❑ EA: Research-Infused Teaching of Parallel Programming Concepts for Undergraduate Software Engineering Students

Siozios, Kostas

- ❑ A Framework for Mapping Dynamic Virtual Kernels onto Heterogeneous Reconfigurable Platforms
- ❑ A Framework for Customizing Virtual 3-D Reconfigurable Platforms at Run-Time

Papers by Author

Sirdey, Renaud

- ❑ Fast Generation of Large Task Network Mappings

Sistla, AnilKumar

- ❑ SmartBricks: A Visual Environment to Design and Explore Novel Custom Domain-Specific Architectures

Skadron, Kevin

- ❑ Dymaxion++: A Directive-Based API to Optimize Data Layout and Memory Mapping for Heterogeneous Systems

Skovhede, Kenneth

- ❑ Bohrium: A Virtual Machine Approach to Portable Parallelism

Smaragdos, Georgios

- ❑ A Dependable Coarse-Grain Reconfigurable Multicore Array

Papers by Author

Song, Shuaiwen

- ❑ The Power-Performance Tradeoffs of the Intel Xeon Phi on HPC Applications

Soudris, Dimitrios

- ❑ A Framework for Mapping Dynamic Virtual Kernels onto Heterogeneous Reconfigurable Platforms
- ❑ A Framework for Customizing Virtual 3-D Reconfigurable Platforms at Run-Time
- ❑ A Low-Latency Algorithm and FPGA Design for the Min-Search of LDPC Decoders

Sourdis, Ioannis

- ❑ A Dependable Coarse-Grain Reconfigurable Multicore Array

Southern, James

- ❑ Application Level Fault Recovery: Using Fault-Tolerant Open MPI in a PDE Solver

Papers by Author

Sow, Daby

- ❑ Large Scale Discriminative Metric Learning

Spada, Fabrizio

- ❑ Adaptive Raytracing Implementation Using Partial Dynamic Reconfiguration

Srimani, Pradip K.

- ❑ Self-Stabilizing Algorithm for Maximal 2-Packing with Safe Convergence in an Arbitrary Graph

Srivastava, Srishti

- ❑ Portfolio-Based Selection of Robust Dynamic Loop Scheduling Algorithms Using Machine Learning

Papers by Author

Stamatakis, Alexandros

- ❑ Efficient Computation of the Phylogenetic Likelihood Function on the Intel MIC Architecture

St-Cyr, Amik

- ❑ Using GPU Shared Memory with a Directive-Based Approach

Stein, Esti

- ❑ Adaptive Booth Algorithm for Three-Integers Multiplication for Reconfigurable Mesh

Steinhauser, Dominik

- ❑ Kd-Tree Based N-Body Simulations with Volume-Mass Heuristic on the GPU

Stotzer, Eric

- ❑ Exploiting DMA for Performance and Energy Optimized STREAM on a DSP

Papers by Author

Strazdins, Peter

- PDSEC Introduction and Committees
- Application Level Fault Recovery: Using Fault-Tolerant Open MPI in a PDE Solver

Strazdins, Peter E.

- Acceleration of a Python-Based Tsunami Modelling Application via CUDA and OpenHMPP

Strout, Michelle Mills

- PDSEC Introduction and Committees

Strydis, Christos

- A Dependable Coarse-Grain Reconfigurable Multicore Array

Su, Chun-Yi

- The Power-Performance Tradeoffs of the Intel Xeon Phi on HPC Applications

Papers by Author

Sukhija, Nitin

- ❑ Portfolio-Based Selection of Robust Dynamic Loop Scheduling Algorithms Using Machine Learning

Sundell, Håkan

- ❑ gpuRF and gpuERT: Efficient and Scalable GPU Algorithms for Decision Tree Ensembles

Sussman, Alan

- ❑ Scalable System Environment Caching and Sharing for Distributed Virtual Machines

Sysoyev, Alexander

- ❑ NSF/IEEE-TCPP Curriculum Implementation at the State University of Nizhni Novgorod

Papers by Author

Szczypiorski, Krzysztof

- ❑ Improving Bus Ride Comfort Using GLOSA-Based Dynamic Speed Optimisation

Takafuji, Daisuke

- ❑ Bulk Execution of Oblivious Algorithms on the Unified Memory Machine, with GPU Implementation

Takizawa, Hiroyuki

- ❑ A Platform-Specific Code Smell Alert System for High Performance Computing Applications

Talbi, El-Ghazali

- ❑ NIDISC Introduction and Committees
- ❑ Hybrid Metaheuristic for Annual Hydropower Generation Optimization
- ❑ Multi-level and Multi-objective Survey on Cloud Scheduling

Papers by Author

Tan, Jian

- ❑ A Distributed Speech Algorithm for Large Scale Data Communication Systems

Tang, Dixin

- ❑ Optimizing the Join Operation on Hive to Accelerate Cross-Matching in Astronomy

Tang, Guangping

- ❑ A Hybrid Parallel Tridiagonal Solver on Multi-core Architectures

Tang, Wei

- ❑ Improving I/O Performance with Adaptive Data Compression for Big Data Applications

Papers by Author

Tani, Kazuya

- ❑ Bulk Execution of Oblivious Algorithms on the Unified Memory Machine, with GPU Implementation

Tarakji, Ayman

- ❑ Runtime Behavior Comparison of Modern Accelerators and Coprocessors

Teich, Jürgen

- ❑ Minimizing Scrubbing Effort through Automatic Netlist Partitioning and Floorplanning

Teranishi, Keita

- ❑ PDSEC Introduction and Committees

Papers by Author

Terzopoulos, George

- ❑ Bag-of-Task Scheduling on Power-Aware Clusters Using a DVFS-Based Mechanism

Thakur, Gautam S.

- ❑ Analyzing Reliability of Virtual Machine Instances with Dynamic Pricing in the Public Cloud

Theodoridis, George

- ❑ A Hybrid ILP-CP Model for Mapping Directed Acyclic Task Graphs to Multicore Architectures

Thibault, Samuel

- ❑ Taking Advantage of Hybrid Systems for Sparse Direct Solvers via Task-Based Runtimes

Papers by Author

Thomas, Stan J.

- ❑ Parallel and Distributed Computing across the Computer Science Curriculum

Thysell, Rachelle

- ❑ Application Power Signature Analysis

Tolubaeva, Munara

- ❑ Predicting Cache Contention for Multithread Applications at Compile Time

Tomkos, Ioannis

- ❑ A Low-Latency Algorithm and FPGA Design for the Min-Search of LDPC Decoders

Tomov, Stanimire

- ❑ Hybrid Multi-elimination ILU Preconditioners on GPUs
- ❑ Optimizing Krylov Subspace Solvers on Graphics Processing Units

Papers by Author

- Dynamically Balanced Synchronization-Avoiding LU Factorization with Multicore and GPUs

Tong, Guoxiang

- A Distributed Speech Algorithm for Large Scale Data Communication Systems

Torresen, Jim

- Module Placement Using Constraint Programming in Run-Time Reconfigurable Systems

Tørresen, Jim

- RAW Introduction and Committees

Tosik, Thomas

- A Load Balancing Behavior for Underwater Robot Swarms to Increase Mission Time and Fault Tolerance

Papers by Author

Toth, David

- A Portable Cluster for Each Student

Townsend, Kevin

- CyGraph: A Reconfigurable Architecture for Parallel Breadth-First Search

Tretyakova, Antonina

- Graph-Based Cellular Automata Approach to Maximum Lifetime Coverage Problem in Wireless Sensor Networks

Trystram, Denis

- An Efficient Algorithm for Scheduling Jobs in Volunteer Computing Platforms

Turaga, Deepak

- Large Scale Discriminative Metric Learning

Papers by Author

Tveretina, Olga

- ❑ A Case Study in Coordination Programming: Performance Evaluation of S-Net vs Intel's Concurrent Collections

Tyagi, Akhilesh

- ❑ An Evaluation of User Satisfaction Driven Scheduling in a Polymorphic Embedded System

Tzilis, Stavros

- ❑ A Dependable Coarse-Grain Reconfigurable Multicore Array

Tzimpragos, Georgios

- ❑ A Low-Latency Algorithm and FPGA Design for the Min-Search of LDPC Decoders

Ureña, Isaías A. Comprés

- ❑ Automatic MPI-IO Tuning with the Periscope Tuning Framework

Papers by Author

Uznański, Przemysław

- ❑ Point-to-Point and Congestion Bandwidth Estimation: Experimental Evaluation on PlanetLab Data

Vaidyanathan, Ramachandran

- ❑ RAW Introduction and Committees

Varghese, Anish

- ❑ Programming the Adapteva Epiphany 64-Core Network-on-Chip Coprocessor

Varis, Pekka

- ❑ Exploiting DMA for Performance and Energy Optimized STREAM on a DSP

Varrette, Sébastien

- ❑ Comparison of Multi-objective Optimization Algorithms for the JShadObf JavaScript Obfuscator

Papers by Author

Vassev, Emil

- ❑ Autonomy Requirements Engineering for Self-Adaptive Science Clouds

Venkatesan, Vishwanath

- ❑ Compactor: Optimization Framework at Staging I/O Nodes

Venkatesh, Akshay

- ❑ Optimizing Collective Communication in UPC

Vetter, Jeffrey

- ❑ AsHES Keynote

Vialle, Stephane

- ❑ Resource Centered Computing Delivering High Parallel Performance

Papers by Author

Vinter, Brian

- ❑ Bohrium: A Virtual Machine Approach to Portable Parallelism
- ❑ Transparent GPU Execution of NumPy Applications

Vishnu, Abhinav

- ❑ ParLearning Introduction and Committees

Vogt, Markus

- ❑ Influence of Magnetic Fields and X-Radiation on Ring Oscillators in FPGAs

Vora, Keval

- ❑ ABC2: Adaptively Balancing Computation and Communication in a DSM Cluster of Multicores for Irregular Applications

Papers by Author

Voros, Nikolaos

- ❑ A Hybrid ILP-CP Model for Mapping Directed Acyclic Task Graphs to Multicore Architectures

Vu, Duy Viet

- ❑ Virtualization Support for FPGA-Based Coprocessors Connected via PCI Express to an Intel Multicore Platform

Wada, Koichi

- ❑ Parallelism Extraction Algorithm from Stream-Based Processing Flow Applying Spanning Tree

Wakabayashi, Shin'ichi

- ❑ An ILP-Based Optimal Circuit Mapping Method for PLDs

Papers by Author

Walia, Harkamal

- ❑ A Comparison of a Campus Cluster and Open Science Grid Platforms for Protein-Guided Assembly Using Pegasus Workflow Management System

Walstrom, Joshua

- ❑ RAW 2014 Keynotes

Walters, John Paul

- ❑ Evaluating GPU Passthrough in Xen for High Performance Cloud Computing

Wan, Jian

- ❑ Towards Energy Efficient Allocation for Applications in Volunteer Cloud

Wang, Cheng

- ❑ A Validation Testsuite for OpenACC 1.0

Papers by Author

Wang, Chunyan

- ❑ A Platform-Specific Code Smell Alert System for High Performance Computing Applications

Wang, Dali

- ❑ High-Performance Zonal Histogramming on Large-Scale Geospatial Rasters Using GPUs and GPU-Accelerated Clusters

Wang, Guyue

- ❑ Parallelism Extraction Algorithm from Stream-Based Processing Flow Applying Spanning Tree

Wang, James Z.

- ❑ Self-Stabilizing Algorithm for Maximal 2-Packing with Safe Convergence in an Arbitrary Graph

Papers by Author

Wang, Lipeng

- ❑ XSW: Accelerating Biological Database Search on Xeon Phi

Wang, Shuli

- ❑ A Task Scheduling Algorithm Based on Replication for Maximizing Reliability on Heterogeneous Computing Systems

Wang, Wei

- ❑ A General P2P Scheme for Constructing Large-Scale Virtual Environments

Wang, Xinying

- ❑ An FPGA Implementation of the Hestenes-Jacobi Algorithm for Singular Value Decomposition

Papers by Author

Wang, Yan

- ❑ A Task Scheduling Algorithm Based on Replication for Maximizing Reliability on Heterogeneous Computing Systems

Wang, Yi

- ❑ Removing Sequential Bottlenecks in Analysis of Next-Generation Sequencing Data

Wang, Ying-Chieh

- ❑ Performance Modeling for Hardware Thread-Level Speculation

Wang, Yongli

- ❑ Parallel Bayesian Network Modelling for Pervasive Health Monitoring System

Papers by Author

Wang, Zhaokang

- Training Large Scale Deep Neural Networks on the Intel Xeon Phi Many-Core Coprocessor

Wanja, Stefan

- ExCoverly — A Framework for Distributed System Experiments and a Case Study of Service Discovery

Watanabe, Minoru

- Radiation Tolerance of Color Configuration on an Optically Reconfigurable Gate Array

Weems, Charles

- LSPP Introduction and Committees

Wei, Lai

- Autotuning Tensor Transposition

Papers by Author

Weis, Torben

- ❑ Deterministic Synchronization of Multi-threaded Programs with Operational Transformation

Weng, Zhe

- ❑ Acceleration of a Python-Based Tsunami Modelling Application via CUDA and OpenHMPP

Weseloh, Burkhard

- ❑ Maintaining Dependable Communication Service for Mobile Stations in Wireless Mesh Networks by Tracking Capacity Demands

Wilke, Jeremiah J.

- ❑ Coordination Languages and MPI Perturbation Theory: The FOX Tuple Space Framework for Resilience

Papers by Author

Wilkinson, Barry

- Using Patterns to Teach Parallel Computing

Willemet, Pierric

- Scalable and Reliable Data Broadcast with Kascade

Wilsey, Philip A.

- llamaOS: A Solution for Virtualized High-Performance Computing Clusters

Winkler, Michael

- Solving Hard MIPLIB2003 Problems with ParaSCIP on Supercomputers: An Update

Winn, M.D.

- Parallelization of the Trinity Pipeline for De Novo Transcriptome Assembly

Papers by Author

Wold, Alexander

- Module Placement Using Constraint Programming in Run-Time Reconfigurable Systems

Woodward, Paul R.

- CFD Builder: A Library Builder for Computational Fluid Dynamics

Wu, Guanning

- A Distributed Speech Algorithm for Large Scale Data Communication Systems

Wu, Kesheng

- Model-Driven Data Layout Selection for Improving Read Performance

Wu, Lei

- A Parallel Large Neighborhood Search-Based Heuristic for the Disjunctively Constrained Knapsack Problem

Papers by Author

Xia, Yinglong

- ParLearning Introduction and Committees
- Wait-Free Primitives for Initializing Bayesian Network Structure Learning on Multicore Processors

Xing, Eric P.

- ParLearning Keynote

Xiong, Naixue

- A General P2P Scheme for Constructing Large-Scale Virtual Environments
- The Empirical Research of Virtual Enterprise Knowledge Transfer's Effectiveness Faced to the Independent Innovation Ability
- A Distributed Speech Algorithm for Large Scale Data Communication Systems

Xu, Rengan

- A Validation Testsuite for OpenACC 1.0

Papers by Author

Yamagiwa, Shinichi

- ❑ Parallelism Extraction Algorithm from Stream-Based Processing Flow Applying Spanning Tree

Yamazaki, Ichitaro

- ❑ Optimizing Krylov Subspace Solvers on Graphics Processing Units
- ❑ Design and Implementation of a Large Scale Tree-Based QR Decomposition Using a 3D Virtual Systolic Array and a Lightweight Runtime

Yan, Yonghong

- ❑ Predicting Cache Contention for Multithread Applications at Compile Time

Yang, Guisong

- ❑ A General P2P Scheme for Constructing Large-Scale Virtual Environments

Papers by Author

Yang, Laurence T.

- ❑ PDSEC Introduction and Committees

Yantir, Hasan Erdem

- ❑ An Efficient Heterogeneous Register File Implementation for FPGAs

Yasui, Yuichiro

- ❑ Hybrid BFS Approach Using Semi-external Memory

Yew, Pen-Chung

- ❑ CFD Builder: A Library Builder for Computational Fluid Dynamics

Yoginath, Srikanth B.

- ❑ Nanoscale Cluster Detection in Massive Atom Probe Tomography Data

Papers by Author

Yokota, Rio

- ❑ Scalable Fast Multipole Accelerated Vortex Methods

Yoo, Paul D.

- ❑ Machine-Learning-Based Identification of Defect Patterns in Semiconductor Wafer Maps: An Overview and Proposal

Yoon, Ilchul

- ❑ Scalable System Environment Caching and Sharing for Distributed Virtual Machines

You, Haihang

- ❑ Comparison of Parallel Programming Models on Intel MIC Computer Cluster

Younge, Andrew J.

- ❑ Evaluating GPU Passthrough in Xen for High Performance Cloud Computing

Papers by Author

Yu, Yongen

- ❑ Improving I/O Performance with Adaptive Data Compression for Big Data Applications

Yuan, Chunfeng

- ❑ Training Large Scale Deep Neural Networks on the Intel Xeon Phi Many-Core Coprocessor
- ❑ YAFIM: A Parallel Frequent Itemset Mining Algorithm with Spark

Yurdakul, Arda

- ❑ An Efficient Heterogeneous Register File Implementation for FPGAs

Zaichenkov, Pavel

- ❑ A Case Study in Coordination Programming: Performance Evaluation of S-Net vs Intel's Concurrent Collections

Papers by Author

Zaidi, Ali Mustafa

- ❑ A New Dataflow Compiler IR for Accelerating Control-Intensive Code in Spatial Hardware

Zaidi, Zied

- ❑ An Efficient Algorithm for Scheduling Jobs in Volunteer Computing Platforms

Zambreno, Joseph

- ❑ An FPGA Implementation of the Hestenes-Jacobi Algorithm for Singular Value Decomposition
- ❑ CyGraph: A Reconfigurable Architecture for Parallel Breadth-First Search

Zhang, Eddy Z.

- ❑ KernelGen — The Design and Implementation of a Next Generation Compiler Platform for Accelerating Numerical Models on GPUs

Papers by Author

Zhang, Haibo

- ❑ A Criticality-Aware DVFS Runtime Utility for Optimizing Power Efficiency of Multithreaded Applications

Zhang, Jianting

- ❑ High-Performance Zonal Histogramming on Large-Scale Geospatial Rasters Using GPUs and GPU-Accelerated Clusters

Zhang, Jie

- ❑ Optimizing Collective Communication in UPC

Zhang, Yang

- ❑ Hierarchical Pipeline Optimization of Coarse Grained Reconfigurable Processor for Multimedia Applications

Zhang, Yunquan

- ❑ AsHES Introduction and Committees

Papers by Author

Zhang, Zhang

- ❑ An Evaluation of User Satisfaction Driven Scheduling in a Polymorphic Embedded System

Zhao, Kun

- ❑ HiPGA: A High Performance Genome Assembler for Short Read Sequence Data

Zheng, Long

- ❑ EEWA: Energy-Efficient Workload-Aware Task Scheduling in Multi-core Architectures

Zheng, Si

- ❑ SkewControl: Gini Out of the Bottle

Papers by Author

Zhou, Xin

- ❑ An Efficient Implementation of the Gradient-Based Hough Transform Using DSP Slices and Block RAMs on the FPGA

Zhou, Zhizheng

- ❑ Service-Oriented Computing and Software Integration in Computing Curriculum
- ❑ Trust-Based Security for the Spanning Tree Protocol

Ziener, Daniel

- ❑ Minimizing Scrubbing Effort through Automatic Netlist Partitioning and Floorplanning

Zola, Jaroslav

- ❑ Constructing Similarity Graphs from Large-Scale Biological Sequence Collections

Papers by Author

Zou, Hongbo

- Improving I/O Performance with Adaptive Data Compression for Big Data Applications

Zuckerman, Stephane

- Position Paper: Locality-Driven Scheduling of Tasks for Data-Dependent Multithreading

Papers by Author

A B C D E F G
H I J K L M N
O P Q R S T U
V W X Y Z