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by

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Efficient Computation of the Number of Paths in a Grid Graph with Minimal Perfect Hash Functions

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Abstract

It is not easy to find the number of self-avoiding walks from $(0,0)$ to (n,n) , because the number increases rapidly with the increase of *n* and mathematical formula for calculating it is not known. Our challenge is to develop advanced algorithmic techniques through efforts of finding the answer to the larger *n*. The idea of Knuth's algorithm is so effective that it can compute the answer to $n = 11$ in a second and $n = 21$ in a few days, even though it is designed for general graphs. In this paper, we specialize it in grid graphs and maximize space and time efficiency. Our program have successfully computed the answer to $n = 25$, which had not known before.

1 Introduction

A path (way to go from a vertex to another vertex without visiting any vertex twice) in a grid graph is called a self-avoiding walk (SAW), which is known to be introduced by the chemist Flory as a model of polymer chains [1]. In spite of its simple definition, many difficult mathematical problems are hidden behind the SAW [2, 3]. They include a problem of counting the number of paths connecting opposite corners of a $(n+1) \times (n+1)$ grid graph, which have become popular through a YouTube-animation [4]. The answer is known to grow as $\lambda^{n^2+o(n^2)}$ and $\lambda \simeq 1.7$ [5]. When $n = 10$, it is about 10^{24} and cannot be counted one by one in a realistic time even if we could find trillions of paths in a second.

We are studying advanced algorithmic techniques through efforts of finding the answer to the larger *n*. According to The On-Line Encyclopedia of Integer Sequences (OEIS) [6],

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Figure 1: Paths in a graph

Rosendale computed the answers up to *n* = 11 in 1981 and Knuth computed the answer to $n = 12$ in 1995. Bousquet-Mélou et al. presented the answers up tp $n = 19$ in their paper [5] in 2005. Their algorithm is based on the method of Conway et al. [7], which makes good use of the fact that the target is a grid graph. On the other hand, Knuth introduced an algorithm for general graphs called SIMPATH in 2008 [8][9, exercise 225 in 7.1.4], which constructs a zero-suppressed binary decision diagram (ZDD) [10] representing a set of all paths between two vertices in a graph. We have extended the answers up to $n = 21$ in 2012 by reimplementing SIMPATH to directly count the number of paths instead of building the ZDD structure [11]. It would be notable that the implementation is not dedicated to grid graphs but is made for general graphs.

We specialize it in grid graphs and maximize space and time efficiency. On the premise that the graph is a square grid, we can foretell the exact number of entries in the hash table filled in the algorithm. It allows us to use a simple array with a minimal perfect hash function instead of an ordinary hash table, which has a large impact on actual space and time efficiency. We improve it still more by additional techniques such as in-place update and parallel processing.

This paper is organized as follows. Section 2 introduces the original algorithm for general graphs. The basic idea specific to grid graphs is shown in Section 3. Detailed techniques to further improve space and time efficiency are described in Section 4. Section 5 shows experimental results and Section 6 mentions some additional ideas. Finally, conclusions are drawn in Section 7.

2 Original Algorithm for General Graphs

Input to the original algorithm [11] is an undirected graph $G = (V, E)$ and two vertices *s*,*t* ∈ *V* where $V = \{v_1, \ldots, v_m\}$ is a set of vertices and $E = \{e_1, \ldots, e_n\}$ is a set of edges. The output is the number of all paths between *s* and *t*. For example, if the input is a 3×3 grid graph and two vertices at opposite corners, the output will be 12 as shown in Figure 1.

The algorithm is a kind of dynamic programming. A path can be represented by a subset of edges $E' \subseteq E$. The algorithm performs breadth-first search in the 2^E space that corresponds to all decision patterns on edge selections. Figure 2 illustrates the computation for $G_{3,3}$, where dotted and solid branches represent exclusion and inclusion of the

Figure 2: Computation for $G_{3,3}$

corresponding edge respectively. A branch is pruned when it makes an inconsistent situation with the final target, e.g. making a cycle or an unexpected endpoint.

Each node *p* at level *i* has a *frontier state* and an integer value. A frontier is a set of the vertices that are contiguous with both decided and undecided edges. The frontier state is represented by a map, $\text{mate}_p : V_i \to V_i \cup \{0\}$, where $V_i \subseteq V$ is the frontier at level *i*. It maintains information about path fragments formed by a set of selected edges as follows:

$$
mate_p[v] = \begin{cases} v & \text{if vertex } v \text{ is not a part of any path fragment,} \\ w & \text{if vertices } v \text{ and } w \text{ are endpoints of a path fragment,} \\ 0 & \text{if vertex } v \text{ is an intermediate point of a path fragment.} \end{cases}
$$

An entry for *mate*_p $[v]$ is created when vertex *v* is entering the frontier and an entry for *mate*_{*p*}[*v*] is deleted when vertex *v* is leaving the frontier.

Since the mate table includes sufficient information of constraints for future edge selections, nodes with equivalent mate tables can be merged during the computation. The integer values of merged nodes are summed. The final result is the value totaled at the leaf node. This algorithm is implemented using a hash table of which keys are mate tables and values are integers.

Figure 3: Frontier state representation

3 Basic Idea for Grid Graphs

3.1 Frontier States

Let $v_{(i,j)}$ be the vertex at row *i* and column *j* of an $n \times n$ grid graph $(1 \le i \le n, 1 \le j \le n)$ and we compute the number of paths between $v_{(1,1)}$ and $v_{(n,n)}$. We visit the vertices in the order of $v_{(1,1)}, \ldots, v_{(1,n)}$, $v_{(2,1)}, \ldots, v_{(2,n)}$, \ldots and make decisions on vertical line $\{v_{(i-1,j)}, v_{(i,j)}\}$ and horizontal line $\{v_{(i,j-1)}, v_{(i,j)}\}$. The frontier at the step visiting $v_{(i,j)}$ is $\{v_{(i,1)},\ldots,v_{(i,j)},v_{(i-1,j+1)},\ldots,v_{(i-1,n)}\}.$

The original algorithm keeps vertex identifiers of path endpoints in *mate^p* (Figure 3a), while we can compress the information efficiently on the premise that the graph is embedded in the plane (Figure 3b). Pairs of path endpoints always form nested structure on the frontier because no path fragments can intersect. We do not need to record all information of endpoint pairs but record only whether they are left or right endpoints. Let $s = c_1 c_2 \cdots c_n$ be a string representing a frontier state at a step visiting $v_{(i,j)}$ where $\Sigma = \{[],[],[],[],[]\}$ and $c_k \in \Sigma$ is a character representing the state of vertex at the *k*-th column in the frontier:

 $c_k =$ $\sqrt{ }$ \int \mathcal{L} if the *k*-th vertex is a left endpoint of a path fragment, if the *k*-th vertex is a right endpoint of a path fragment, if $k = j$ and the *k*-th vertex is an intermediate point of a path fragment, otherwise.

We consider the endpoint connected to $v_{(1,1)}$ is always a right endpoint. \bullet is a special character only for c_j , meaning that it is different from \odot as we cannot include the next horizontal line $\{v_{(i,j)}, v_{(i,j+1)}\}.$

Examples of state transitions are shown in Figure 4. We have no alternatives of vertical line selections in every step, because $\{v_{(i-1,j)}, v_{(i,j)}\}$ must be excluded if $v_{(i-1,j)}$ is not an endpoint and $\{v_{(i-1,j)}, v_{(i,j)}\}$ must be included if $v_{(i-1,j)}$ is an endpoint. Branches are always made by selection of horizontal lines. A frontier state changes only when horizontal line $\{v_{(i,j-1)}, v_{(i,j)}\}$ is included except for \bigcirc to \odot changes. The state transition is summarized in Figure 5.

Since \Box \Box pairs are nested, the number of valid frontier states is closely related to Motzkin numbers [12]. Let $x_1 < x_2$ and $\mathcal{M}_{(x_1,y_1) \to (x_2,y_2)}$ be a set of routes from coordinates (x_1, y_1) to coordinates (x_2, y_2) on $x_2 - x_1$ steps of $(1, 1)$, $(1, -1)$, or $(1, 0)$ moves without

Figure 4: Examples of state transitions on a 6×6 grid graph

Current state	Next state		
	Line excluded	Line included	
\circ \circ \cdots .	\ldots 0 0 \ldots	\rightarrow . (
\ldots 0) \ldots	\cdots 0 .	\bullet \cdots	
\cdots 0 (\cdots	\cdots 0 (\cdots	\ldots (\bullet \ldots	
$ $ \circ $ $ \cdots	$ $ \circ \cdots .	. 0)	
\cdots (\cdots $\left\{ \left. \right. \right\} \left. \right\} \left. \right. \left. \right. \left. \right.$	\cdots (\cdots 111	\ldots) \ldots o \bullet	
.) (\ldots) (\ldots	. ○ ● .	
\ldots (\circ \ldots	. (0	\ldots 0) \ldots	
\cdots $\left(\begin{array}{c} 1 \\ 1 \end{array} \right)$ $\left[\begin{array}{c} 0 \\ 1 \end{array} \right]$	\cdots $\left($ $\left($ $\left[$ $\left($ $\right]$, \cdots $\left[$ $\right]$ $\right)$ $\left[$ \cdots	\ldots \circ \bullet \ldots (\vdots \ldots	
\cdots $($ $ $ $)$ $ $ \cdots	\rightarrow \cdots	N/A (making cycle)	
\ldots \bullet \circ \ldots	\cdots 0 0 \cdots	N/A (branching)	
\bullet \circ \cdot \cdot \cdot .	\circ $\mathbf{1}$	N/A (branching)	
\cdots .	\circ	N/A (branching)	

Figure 5: State transition table

Figure 6: Possible frontier states for a 6×6 grid graph

visiting negative *y*-coordinates. The *n*-th Motzkin number is given by $M_n = |\mathcal{M}_{(0,0)\to(n,0)}|$. They are given by recurrence relation:

$$
M_0 = M_1 = 1,
$$

\n
$$
M_n = \frac{3(n-1)M_{n-2} + (2n+1)M_{n-1}}{n+2}.
$$
\n(1)

We divide frontier states into two classes: *main states* and *blocked states*. A main state does not have \bullet and a blocked state has \bullet at the *j*-th position. Let $\Sigma' = \Sigma \setminus \{\bullet\}$, S_j be a set of frontier states after visiting *j*-th column, and $S' \subset S_j$ be a set of main states. *S*^{*i*} does not include $\overline{\bullet}$, i.e. $S' \subset \Sigma''$. It corresponds to $\mathcal{M}_{(0,1)\to(n,0)}$ (Figure 6a) where $\overline{(0,1)}$, and \textcircled{c} characters are associated with $(1,1)$, $(1,-1)$, and $(1,0)$ moves respectively. The number of main states is given by:

$$
N_n = |\mathscr{M}_{(0,1)\to(n,0)}| = M_{n+1} - M_n.
$$
 (2)

Let $S_j'' = S_j \setminus S'$ be a set of blocked states after visiting *j*-th column. As all blocked states have \bullet at the *j*-th position, it can be ignored in enumerating *S*^{*n*}. That corresponds to $\mathcal{M}_{(0,1)→(n-1,0)}$ (Figure 6b) and the number of blocked states is given by:

$$
N_{n-1} = |\mathcal{M}_{(0,1)\to(n-1,0)}| = M_n - M_{n-1}.
$$
 (3)

Therefore, the number of frontier states is:

$$
N_n + N_{n-1} = M_{n+1} - M_{n-1} \tag{4}
$$

3.2 Basic Algorithm

Since the domain of frontier states has become clear, we can define a minimal perfect hash function $\varphi_j : S_j \to \{1, ..., N_n + N_{n-1}\}\$ for each column position $1 \le j \le n$. It allows us to use a simple array instead of an ordinary hash table to keep intermediate results. A simple implementation is to define some lexicographical order on the frontier states, and to calculate the order of a given state string by scanning its characters one by one. An improved implementation is described later in Section 4.2.

Algorithm 1 shows the basic method for computing the number of paths in a grid graph. The *count* array keeps the integers that represent the numbers of cases for all frontier states at the current step. For example, if we apply this algorithm to a 24×24 grid graph using 56-byte integers, *count* requires (*M*25−*M*23)×56 bytes = 413 gigabytes of memory. The *tmp* array is a temporary storage, which is the same size as *count*.

Algorithm 1 Computing the number of paths in a grid graph 1: *count*[k] ← 0 for all $1 \leq k \leq N_n + N_{n-1}$; 2: $\textit{count}[\varphi_1(\overbrace{\text{)}\circ\circ\cdots\circ}]{\circ\cdots}\] \leftarrow 1;$ *n* 3: for $i = 1$ to *n* do 4: **for** $j = 1$ **to** $n - 1$ **do** 5: $tmp[k] \leftarrow 0 \text{ for all } 1 \leq k \leq N_n + N_{n-1};$ 6: **for all** $s \in S_j$ **do** 7: *t* ← the next state of *s* when $\{v_{(i,j)}, v_{(i,j+1)}\}$ is excluded; 8: $tmp[\varphi_{j+1}(t)] \leftarrow tmp[\varphi_{j+1}(t)] + count[\varphi_j(s)];$ 9: $u \leftarrow$ the next state of *s* when $\{v_{(i,j)}, v_{(i,j+1)}\}$ is included; 10: **if** u is defined **then** 11: $tmp[\varphi_{j+1}(u)] \leftarrow tmp[\varphi_{j+1}(u)] + count[\varphi_j(s)];$ 12: end if 13: end for 14: *count*[k] \leftarrow *tmp*[k] for all $1 \leq k \leq N_n + N_{n-1}$; 15: end for 16: *tmp*[k] ← 0 for all $1 \le k \le N_n + N_{n-1}$; 17: **for all** $s \in S_n$ **do** 18: $t \leftarrow$ the string made from *s* by replacing its \bullet with \circledcirc ; 19: $tmp[\varphi_1(t)] \leftarrow tmp[\varphi_1(t)] + count[\varphi_n(s)];$ 20: end for 21: *count*[k] \leftarrow *tmp*[k] for all $1 \leq k \leq N_n + N_{n-1}$; 22: end for 23: **return** $count[\varphi_1(\overbrace{\odot\odot\cdots\odot\odot})];$ *n*

$ 11\rangle$	$\boxed{ \begin{picture}(10,10) \put(0,0){\line(1,0){10}} \put(15,0){\line(1,0){10}} \put(15,0){\$	
	$\begin{picture}(150,10) \put(0,0){\line(1,0){10}} \put(15,0){\line(1,0){10}} \put(15,0){\line($	
$\frac{1}{2}$	$\boxed{(\begin{array}{c} \bullet \\ \bullet \end{array}) \begin{array}{c} \bullet \\ \bullet \$	

Figure 7: Data dependency

4 Techniques to Improve the Efficiency

4.1 In-place Update of the Array

If we eliminate the *tmp* array, the memory usage of Algorithm 1 can be reduced by half. We put it into practice by reordering the access to the *count* array elements. Let $1 \leq i \leq j$ $n-1, \, \alpha \in \Sigma^{j-1}, \, \beta \in \Sigma^{j-1-1}$, and $c \in \Sigma'$. We choose the minimal perfect hash functions that holds the following relations:

$$
\varphi_j(s) = \varphi_{j+1}(s) \quad \text{for all } s \in S', \text{ and} \n\varphi_j(\alpha \odot c\beta) = \varphi_{j+1}(\alpha c \odot \beta) \quad \text{for all } \alpha \odot c\beta \in S''_j \text{ where } c \in \Sigma'. \tag{5}
$$

Hereafter, we write as $count(s)$ to denote $count[\varphi_i(s)]$ because position *j* is not important anymore.

Data dependency in the *count* array is illustrated in Figure 7. For instance, $\lceil \lceil \circ \rceil \rceil$ in Figure 7 represents a set of the frontier states that have \Box at position *j* and \Box at position $j + 1$ when we are processing a horizontal line between columns *j* and $j + 1$ 1. A directed edge from \circledcircledcirc to $\circledcircledcircledcirc$ show that a value read from *count*(α $\circledcircledcircledcirc$ β) is added into *count*(α (\Box) β) for all α \circledcirc $\beta \in S'$. Since *count*(α \circledcirc β) is used to calculate the new value of *count*($\alpha(\Pi)\beta$), we update *count*($\alpha(\Pi)\beta$) before adding the value of $count(\alpha \bullet \circ \beta)$ into $count(\alpha \circ \circ \beta)$. Storage for $count(\alpha \circ \beta)$ and $count(\alpha \bullet \beta)$ is the same location. We update $count(\alpha \cap \bullet \beta)$ and $count(\alpha \circ \cap \beta)$ simultaneously because they depend on each other.

4.2 Fast Mapping from Frontier States to Serial Numbers

Hash functions are evaluated frequently and have a large impact to the total speedup. In this section, we describe a minimal perfect hash function for main states. One for blocked states can be made in the same way by ignoring \bullet and considering that the string size is *n*−1. The hash function for all frontier states can be made with their combination.

The minimal perfect hash function for main states $\varphi': S' \to \{1, \ldots, |S'|\}$ is implemented as a sum of two subfunctions:

$$
\varphi'(s) = \varphi_L'(l) + \varphi_R'(r) \tag{6}
$$

where *l* is the left half of string *s* and *r* is the right half of *s*. The sizes of *s*, *l*, and *r* are *n*, $m = |n/2|$, and $n - m$, respectively. This composition works because the main states

correspond to $\mathcal{M}_{(0,1)\to(n,0)}$. The left half must be in $\mathcal{M}_{(0,1)\to(m,h)}$ and the right half must be in $\mathcal{M}_{(m,h)\to(n,0)}$, where $0 \leq h \leq m$. We allocate serial numbers to main states in order of *h*. Let *base*_{*h*} + *i* denote the i-th serial number of the main states passing through (m, h) . It is given by:

$$
base_0 = 0,
$$

\n
$$
base_{h+1} = base_h + |\mathcal{M}_{(0,1)\to(m,h)}| \cdot |\mathcal{M}_{(m,h)\to(n,0)}|.
$$
\n(7)

When $1 \le i_l \le |\mathcal{M}_{(0,1)\to(m,h)}|$ is a serial number of string *l* in $\mathcal{M}_{(0,1)\to(m,h)}$ and $1 \le i_r \le$ $|\mathcal{M}_{(m,h)\to(n,0)}|$ is a serial number of string *r* in $\mathcal{M}_{(m,h)\to(n,0)}$, the subfunctions can be defined as:

$$
\begin{aligned} \varphi_L'(l) &= base_h + (i_l - 1) \cdot \left| \mathcal{M}_{(m,h) \to (n,0)} \right| \,, \\ \varphi_R'(r) &= i_r \,. \end{aligned} \tag{8}
$$

Main states are implemented as 2*n*-bit codes under such 2-bit code assignments as $\lbrack \circ \rbrack = 00, \lbrack \rbrack = 10$. We divide a 2*n*-bit code into the left 2*m*-bit subcode and the right $2(n-m)$ -bit subcode. The two subfunctions can be implemented by the simple arrays that are indexed directly by the subcodes. The total size of the two arrays is about 2^{n+1} and is small enough in comparison with the size of the *count* array.

4.3 Fast Enumeration of All Main States

As shown in Section 4.1, we want to enumerate all frontier states in some specific order for in-place update of *count* entries. It is also acceptable to enumerate all main states in some specific order and visit each blocked state while visiting a main state related to it.

A simple implementation would be a single array of states sorted in the desired order. Although it is very fast and accepts any state order, it consumes memory as much as the *count* array. We introduce a method with much better memory efficiency for visiting main states in lexicographic order. We divide main states again into the left and right halves. A left state array *lstate* has ordered collection of $\langle l, h \rangle$ values where *l* is a left state subcode and *l* corresponds to $\mathcal{M}_{(0,1)\to(m,h)}$. A right state array *rstate*_{*h*} $(0 \le h \le m)$ has ordered collection of right state subcodes in $\mathcal{M}_{(m,h)\to(n,0)}$. All main states can be visited by the following double loop:

```
for each \langle l, h \rangle in lstate do
   for each r in rstateh do
      visit lr;
   end for
end for.
```
Running time of this double loop would be almost the same as the single array implementation because most of the time is consumed in the inner loop and it is just scanning an array in the same way as the single array implementation.

4.4 Shared Memory Parallel Processing

Let $s = c_1 c_2 \cdots c_n$ be a state string and the current step is for the horizontal line connecting columns *j* and $j + 1$. As shown in Figure 5, the transition patterns of state strings have much locality: except for modification of c_j and c_{j+1} , at most one position can be changed from $\lceil \cdot \rceil$ to $\lceil \cdot \rceil$ or from $\lceil \cdot \rceil$ to $\lceil \cdot \rceil$.

We can partition the states into 2^{n-2} groups across which no transition occurs, based on an $(n-2)$ -bit binary code " $g(c_1) \cdots g(c_{j-1}) g(c_{j+2}) \cdots g(c_n)$ " where $g : Σ' → \{0,1\}$ is defined to be

$$
g(c) = \begin{cases} 0 & \text{if } c = \textcircled{\scriptsize{0}}, \\ 1 & \text{if } c = \textcircled{\scriptsize{1}} \text{ or } c = \textcircled{\scriptsize{1}}. \end{cases}
$$
(9)

It is suitable for shared memory parallel processing since data update within a group is independent of other groups. We use the leftmost *m*-bit of the binary code for task allocation, where *m* is decided to make enough number of tasks and not to make each task too small. Actually, $m \approx n/2$ is a convenient choice in combination with the technique described in Section 4.3.

5 Experimental Results

We have compared the original sequential program for general graphs [11] and three new programs for grid graphs:

- GGCount-SB a sequential program based on the techniques in Section 3 and Section 4.1 using *count* array of 56-byte integers,
- GGCount-SF an improved version of GGCount-SB with the techniques in Section 4.2 and Section 4.3, and
- GGCount-PF a parallel version of GGCount-SF with the technique in Section 4.4, using 12 CPU cores.

All those programs are written in C++. Experiments were performed on 2.67GHz Intel Xeon E7-8837 CPUs with 1.5TB memory, running 64-bit SUSE Linux Enterprise Server 11. Results for $(n+1) \times (n+1)$ grid graphs are shown in Table 1 and Table 2. In comparison with the original implementation [11], space and time efficiency has been improved five times and ten times respectively in the sequential processing (GGCount-SF); time improvement of one another digit is achieved by the parallel processing using 12 CPU cores (GGCount-PF). GGCount-SF used slightly more memory than GGCount-SB, while it achieved three times speedup.

As the parallel program runs fast enough, it is worth exchanging space costs for time costs using the Chinese Remainder Theorem in the same way as [5]. Using a 64-bit modular arithmetic version of GGCount-PF, we have succeeded in finding the answer to $n = 25$ as well as confirming the past results for $n = 22,23,24$ by Spaans [6]. The numbers are shown in Table 3. For $n = 25$, we performed 9 runs with coprime moduli, of which each

n	$[11]$	GGCount-SB	GGCount-SF	GGCount-PF
10	3		1	1
11	7	$\overline{2}$	3	3
12	18	6	6	6
13	44	15	15	15
14	145	40	41	41
15	432	110	111	111
16	1290	304	306	306
17	3676	847	849	849
18	9993	2367	2376	2376
19	34298	6641	6663	6663
20	95329	18688	18729	18729
21	297260	52723	52791	52791
22	~ 700000	149108	149254	149254
23	>1500000	422634	422861	422861

Table 1: Memory usage (megabytes)

n	[11]	GGCount-SB	GGCount-SF	GGCount-PF
10	0.2	0.2	0.0	0.0
11	0.7	0.5	0.1	0.0
12	2.4	1.6	0.3	0.1
13	8.6	5.1	1.0	0.2
14	38.0	16.7	4.7	0.5
15	140.1	53.7	14.0	1.9
16	508.1	172.5	45.8	7.9
17	1763.7	554.1	146.4	20.9
18	6003.0	1755.0	459.3	67.6
19	17961.9	5687.2	1759.6	212.4
20	61570.0	18121.4	4616.1	652.0
21	208001.7	56263.6	17917.2	3244.3
22	>604800	178439.6	53671.1	5695.0
23	$>$ 604800	554159.8	170475.7	21313.0

Table 2: Computation time (seconds)

run took 18 hours on 30 CPU cores and used 480 gigabytes of memory. We also confirmed a successful run for $n = 26$ on the same machine using 2 days and 1400 gigabytes; it will take three weeks to get the exact answer.

It was not difficult to modify Algorithm 1 to compute the number of cycles [13] instead of paths. The numbers of main and blocked states for cycle enumeration are M_n and M_{n-1} respectively; *count* array is initialized to be 1 for $\circledcirc \circ \cdot \cdot \circledcirc$; the answer is the total number of cycles found during the main loop. The answers to $(n+1) \times (n+1)$ grid graphs ($n \le 25$) are shown in Table 4. For $n = 25$, we performed 9 runs with coprime moduli, of which each run took 9 hours on 30 CPU cores and used 260 gigabytes of memory.

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Table 4: The number of cycles in an (*n* +1)×(*n* +1) grid graph

Figure 8: The number of frontier states against *n*

6 Additional Techniques

6.1 Using Line Symmetry

The problem is line symmetrical with respect to the line passing through $v_{(1,1)}$ and $v_{(n,n)}$; the number of paths starting from $\{v_{(1,1)}, v_{(1,2)}\}$ is exactly the same as the number of paths starting from $\{v_{(1,1)}, v_{(2,1)}\}$. It means that the answer can be computed by doubling the number of paths for one size. This method, however, would not contribute to much computational reduction because the two cases are merged soon in the breadth-first search algorithm.

One could make some algorithm utilizing line symmetry by introducing diagonal frontiers instead of horizontal frontiers. That approach seems difficult because it have to overcome faster growth of the number of frontier states than using horizontal frontiers. Figure 8 compares the peak numbers of frontier states appeared in the algorithm for general graphs [11].

6.2 Using Point Symmetry

If we use the point symmetry of the problem, Algorithm 1 can be stopped at the middle of computation. We can compute the answer by enumerating every pair of main states that matches. Figure 9 shows that paths are completed by $A = \sqrt{\ln(\ln(\ln(\ln(\theta)))}$ and $B =$ $(\lceil \circ \rceil) \rceil (\lceil \circ \rceil) \cdot ((C = \lceil \lceil \circ \rceil \cdot \lceil \rceil))$ when *B* (*C*) is turned over and they are combined. The total number of paths found here is

 $count(A) \times (count(B) + count(C)).$

Figure 9: Example of matching

Numbers stored in *count* grows exponentially against the number of loop iterations in the algorithm. This technique reduces the memory usage by half because integer size can be halved when we only compute numbers for the upper half of the graph. As for the time cost, however, this technique might be at a disadvantage. Time growth of the matching process against *n* was larger than that of the basic algorithm in our preliminary experiments.

7 Conclusions

We have maximized space and time efficiency by focusing only on grid graphs. On the premise that the graph is a square grid, we were able to clarify the exact set of possible frontier states and to analyze their transition patterns. They allowed us to use simple arrays instead of dynamic hash tables and to integrate various techniques, such as in-place update and parallel processing, into the algorithm. Our algorithm for grid graphs have achieved double-digit performance improvement on the original algorithm for general graphs. It have extended the numbers recorded in OEIS [6][13] and have verified those past results correctly.

The YouTube-animation [4] demonstrated the importance of algorithmic techniques against combinational explosion. We were able to realize it through this challenge. We would like to thank all staff involved in the animation and its 1.3 million viewers who took interest in this issue, which was a major force to boost our study.

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