

Slides Courtesy of Professor David Garlan

Why Document Architecture?

- Blueprint for the system
 - n Artifact for early analysis
 - n Primary carrier of quality attributes
 - Key to post-deployment maintenance and enhancement
- Documentation speaks for the architect, today and 20 years from today
 - n As long as the system is built, maintained, and evolved according to its documented architecture

What is Wrong Today?

- In practice today's documentation consists of
 - n Ambiguous box-and-line diagrams n Inconsistent use of notations

 - n Confusing combinations of viewtypes
- Many things are left unspecified:
- n What kind of elements?
- n What kind of relations?
- n What do the boxes and arrows mean?
- What is the significance of the layout?



What could the arrow mean?

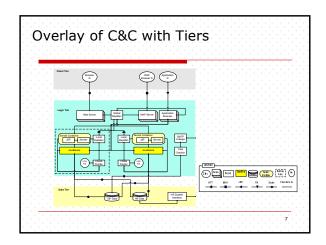
- Many possibilities
 - n A passes control to B
 - n A passes data to B
 - n A gets a value from B
 - n A streams data to B
 - n A sends a message to B
 - n A creates B
 - n ...

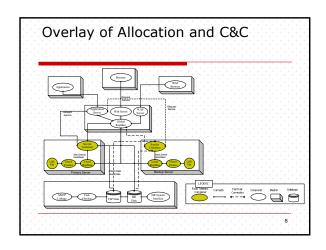
Guidelines: Avoiding Ambiguity

- o Always include a legend
- Define precisely what the boxes mean
- Define precisely what the lines mean
- Don't mix viewtypes unintentionally
 - n Recall: Module (classes), C&C (components)
- Supplement graphics with explanation
 - very important: rationale (architectural intent)
- Do not try to do too much in one diagram n Each view of architecture should fit on a page
 - n Use hierarchy

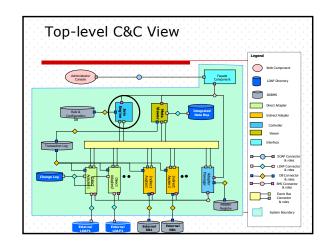
Technique: Multiple Views

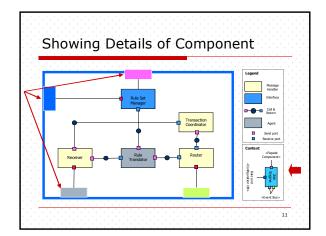
- Separate views to manage complexity
 - n Provide clean separation of concerns
 - n But lead to the problem of relating them and finding inconsistencies between them!
- Showing how views relate to each other can help better understand the architecture





Use hierarchy to define elements in more detail in separate views Helps keep an architectural description manageable





Analyzing Architectures o To more precisely document an architecture, you can use an Architecture Description Language (ADL) o We're going to illustrate one such ADL, Acme: n Supports hierarchical design n Support architectural styles n Supports expressing/analyzing extra-functional properties (performance, reliability, etc.) n Does not support checking of conformance of implementation to architecture

Many Architecture Description Languages

- o Each comes with different analysis capabilities
 - n Rapide: events with simulation and animation
 - n UniCon: emphasizing heterogeneity and compilation
 - n Wright: formal specification of connectors
 - n Aesop/Acme: style-specific arch design languages
 - n Darwin: service-oriented architectures
 - n SADL: architectural refinement
 - n Meta-H: arch description for avionics domain
 - n C-2: arch style using implicit invocation

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AcmeStudio Demo

- Create/Modify a Family
- Create/Modify a System

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Acme Primer

- Family
 - n Component Types, Port Types, Connector Types, ...
- Components
 - n Ports
- Connectors
 - n Roles
- Attachments
- System
- Representations
 - n Bindings

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Components and Ports

- Components
 - n Represent the computational elements and data stores of a system.
- Ports
 - n Are the points of interaction between a component and its environment.



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Connectors and Roles

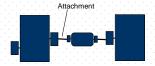
- Connectors
 - n Represent interactions between components such as method calls or an SQL connection
- The interface of a connector is defined as a set of roles (think of it as the "protocol")

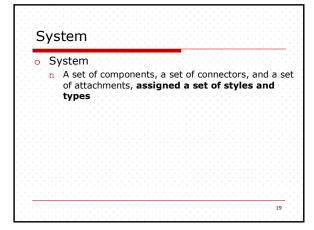


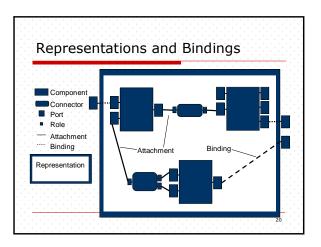
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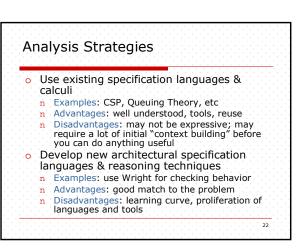
Attachments

- Attachments
 - n Links a component port to a connector role.







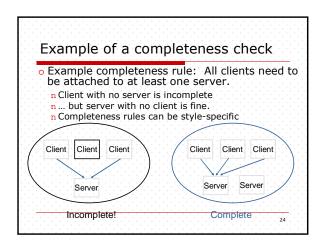


Analysis: consistency & completeness

o Consistency: do the parts fit together?

n analog of type checking
n depends on what you say about the parts
n behavior example: does the behavior of a component conform to the protocols of a connector to which it is attached?

o Completeness: are any parts missing?
n connector roles?
n unattached ports?
n missing functionality?



Example Consistency Checking

- Key idea: Acme can capture complex constraints about an architectural style
- AcmeStudio (the tool) can be used to automatically check an architecture's conformance to these constraints.
- Example: Topological constraints of a complex family of NASA systems.

Specifying Architectural Constraints

- Acme includes a constraint language

 - n Based on first-order predicate logic n Augmented with architecture-specific predicates o connected(comp1, comp2), self.ports, self.components, .
- Examples:
 - n A particular type of component can only have particular types of ports
 - n Property values must have certain values
- Two types of constraints
 - n Invariants must never be violated
 - Heuristics may be selectively violated

Recall Acme Constraints

Component Type naïve-client = { Property protocol = rpc-client }; Property request-rate : integer
 << default = 0; units = "rate-per-sec" >>; Invariant forall p in self.Ports (p.protocol = rpc-client);
Invariant size(Ports) <= 5;</pre> Invariant request-rate >= 0 Heuristic request-rate <= 100;

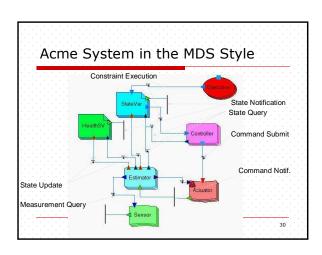
Consistency Example: MDS Architectural Style

- MDS defines an architectural framework for a family of NASA systems
 - n System of architectural component types n Rules on how they can be connected
- Checking/ensuring conformance to MDS is an important and hard problem
 - n Many rules, many components, complex interconnection topology

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Acme MDS Architectural Style

- To specify the MDS style requires
 - n 8 Component types (sensor, actuator, estimator ...)
 - n 12 Connector types (measurement query, command submit, state update)
 - n 22 Interface types
- o MDS rules defined using first order predicate
 - n Ten rules in English from MDS designers become 38 checkable predicates

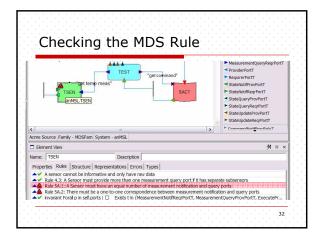


Example MDS Rule

- o As specified by MDS designers:
- For any given Sensor, the number of Measurement Notification ports must be equal to the number of Measurement Query ports (rule R5A).
- Acme rule (associated with the sensor component type):

numberOfPorts (self, MeasurementNotifReqrPortT) ==
numberOfPorts (self, MeasurementQueryProvPortT)

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More MDS Rules

o Rule 4: "Every estimator requires 0 or more Measurement Query ports. It can be 0 if estimator does not need/use measurements to make estimates, as in the case of estimation based solely on commands submitted and/or other states. Every sensor provides one or more Measurement Query ports. It can be more than one if the sensor has separate sub-sensors and there is a desire to manage the measurement histories separately. For each sensor provided port there can be zero or more estimators connected to it. It can be zero if the measurement is simply raw data to be transported such as a science image. It can be more than one if the measurements are informative in the estimation of more than one state variable."

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Checking More MDS Rules

- o As specified by MDS designers:
 - "...It can be more than one if the sensor has separate sub-sensors and there is a desire to manage the measurement histories separately...."
- Acme rule (associated with the sensor component type):

(numberOfPorts(self, MeasurementQueryPort) > 1) à
 self.manageHistoriesSeparately AND
 hasCommandableSubunits(self));

where

hasCommandableSubunits = ...

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Analyzing system-wide properties

- Key idea: calculate properties of a system, given properties of its parts
- Different kinds of properties will have different calculi for compositionality
- Usually depends on using a specific style
 - n Example 1: queuing theory can be used to calculate overall throughputs and latencies if use asynchronous message passing style
 - n Example 2: reliability block diagrams can be used to determine aggregate reliability from the parts, for certain styles

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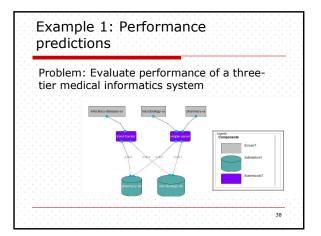
Performance Analysis Alternatives

- Measurement
 - n Most accurate; modifications difficult
 - n Requires existing system, workload
- Simulation
 - n Accuracy and ease of modifications vary
 - n Requires existing simulator, workload
- Analytical model
 - n Back-of-the-envelope accuracy in design phase
 - ${\bf n}$ Nontrivial systems require nontrivial calculation; analysis tools can help.

Architectural Performance Analysis

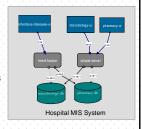
- Try out various "what-if" scenarios,
- Obvious ways to deal with a bottleneck component:
 - n replicate it,
 - n speed it up, or
 - n reduce the demand on the system.
- Options may vary in expense and difficulty, or even feasibility

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Evaluate performance of a threetier medical informatics system

- Can the servers handle the expected demand?
- Will the average response time meet requirements?
- How large should the buffers be?
- o Highest demand the servers can handle?
- o Which component is the bottleneck?
- How would server/database replication affect this?



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Queuing Network Theory 101

- Basic units of a queuing network:
 - n Queues: A buffer with some queuing discipline (e.g., FIFO, round robin)
 - n Service centers: provides some necessary service
- Service center:
 - ${\tt n}\,$ Has a queue containing jobs to process
 - n Can be replicated: i.e., m identical providers of service, which draw their jobs from a single queue
- A queuing network is an interconnected group of these queues.
 - n Jobs enter the network, receive service at service centers, and leave

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Queuing Network Theory 102

- o Given for each service center
 - n average arrival rate of jobs
 - n average service time to process a job
- Assumptions
 - n asynchronous arrival of jobs in queues
 - n exponential rates, independence
 - n each job exists in exactly one place at a time

Queuing Network Predictions

- Utilization (fraction of time the service center is occupied)
- Average time a job spends waiting in the queue
- Average queue length
- o Probability that the queue length is n
- o Latency: the time for a job to be completely processed
- Throughput: the rate at which jobs are processed
- o Number of outstanding jobs in the system
- Most-utilized service centers, i.e., possible bottlenecks
- o Whether the system is stable or overloaded
 - n In an overloaded system, queue grows faster than jobs can be processed; the server cannot keep up
 - n For a queue implemented as a buffer of length B, the rate at which incoming jobs are discarded due to buffer overflow (drop rate)

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Naive Application to Architectures

- o Define an architectural style in which
 - n Service centers → distributed processing components
 - n Transmission lines → directional asynchronous message passing connectors
- o Associate service times with components and arrival rates with system
- Use Queueing Networks to calculate derived values for the components and the system

Complicating Factors

- Cycles
 - n jobs may pass through same component several times before exiting
- Autonomous clients
 - n generate jobs by themselves
- Delays in connectors
 - n present for architectures of real systems
- Replication
 - n meaning and effect on calculations

Modeling Performance in Acme

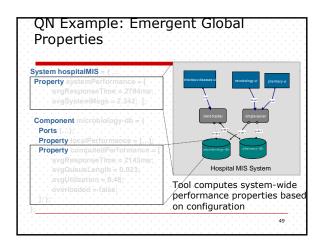
```
Family PerformanceFam = {
 Component Type PerformanceComponent = {
   perf-replication : int;
   perf-overloaded : boolean;
   perf-serviceTime : float;
   perf-responseTime : float;
   perf-utilization : float;
 Connector Type PerformanceConnector = {
   perf-delayTime : float;
```

Modeling Three-Tier Family in Acme

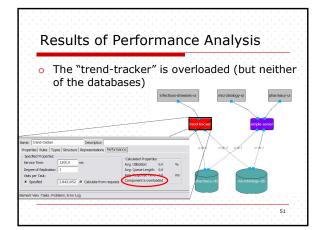
```
Family WebThreeTier extends PerformanceFam with {
  Component Type BrowserT extends
     PerformanceComponent with {...}
  Component Type SqlDataStore extends
     PerformanceComponent with {...}
  Component Type BusinessLogic extends
     PerformanceComponent with {...}
```

```
Modeling the System
System med-informatics : ThreeTierFam = {
  Component infectious-diseases-ui : BrowserT = {...};
  Component microbiology-ui : BrowserT = {...};
  Component pharmacy-ui : BrowserT = {...};
  Component simple-server : ServerT = {
    Property perf-replication = 10;
  Component trend-tracker : ServerT = {
    Property perf-replication = 1;
```

QN Example: Local Properties User defines local performance properties by: n Simulation n Guesstimate n Measured values Ports (...) Property localPerfo Property computedPerformance = |



Architectural Analysis in Acme o In Acme (the language): n Capture performance-related properties in a family n Systems desiring performance analysis (and satisfy the assumptions) use this family n Acme properties n Acme constraints o In AcmeStudio (the tool): n Calculate the values related to the properties



What can we do about overloading?

Scaling Strategies Goal of scaling: eliminate performance bottlenecks in application **Vertical partitioning** Add cache to process application requests more quickly Ex: cache some data (e.g., list of countries) instead of hitting database Vertical scaling Buy a bigger machine More expensive from a hardware standpoint but cost less to maintain Horizontal partitioning Partition application onto different servers. For example: Split off reporting server onto another server Requires careful thought about slicing the application Horizontal scaling Buy a farm of identical machines Drawback: you cannot store state information on any given machine Cheaper from a hardware standpoint but cost more to maintain

Basic idea: apply Reliability Block Diagrams to Software Architecture Reuses well developed reliability model Slightly modified to work with Software Architectures Initial information includes: reliability of individual components topology of interaction, concurrency, replication From this we calculate expected reliability of the system as a whole.

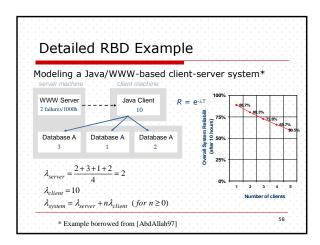
Reliability model for SW Architecture

- Reliability defined as $R = e^{-\lambda T}$ where:
 - \circ λ is a component's failure rate
 - o T is the time period over which reliability is measured

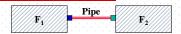
Reliability Block Diagrams Example $R_{sys} = 1 - \prod_{i=1}^{n} (1 - R_i)$

Reliability Block Diagram (RBD) Complications

- o Simple model has some complications
 - n Concurrency
 - n Distribution
 - n Dynamism
 - n Connectors may be unreliable
- These complications are addressed for many (but not necessarily all) styles.
 - n For details, see [AbdAllah97] Abd-Allah, Ahmed, "Extending Reliability Block Diagrams to Software Architectures", USC Technical Report USC-CSE-97-501



Specifying Architectural Behavior



- o Which is the reading/writing end of the pipe?
- o Is writing synchronous?
- What if F₂ tries to read and the pipe is empty?
- Can F₁ choose to stop writing?
 Can F₂ choose to stop reading without consuming all of the data?
- o If F₁ closes the pipe, can it start writing again?
- o If F₂ never reads, can F1 write indefinitely?

Questions